

Learning Guide Unit 5

Overview

UNIT 5: Memory and Programmable Logic

Topics:

- Memory types and hierarchies
- Read-only memory (ROM) and random-access memory (RAM)
- Programmable logic devices (PLDs)

Learning Objectives:

By the end of this Unit, you will be able to:

- Explain memory hierarchy in computers.
- Discuss the memory type performance benefits and trade-offs.
- Compare and contrast programmable logic devices (PLDs) to design digital circuits.

Tasks:

- Read the Learning Guide and the Reading Assignment
- Complete the Discussion Assignment by posting in the Discussion Forum
- Respond to two of your fellow classmates' posts in the Discussion Forum
- Complete and submit the Assignment Activity
- Take and submit the Self-Quiz

