

Learning Guide Unit 3

Overview**UNIT 3: Sequential Logic Design****Topics:**

- [Flip-flops and latches](#)
- Analysis and design of [sequential circuits](#)
- Registers and counters

Learning Objectives:

By the end of this Unit, you will be able to:

- Compare and contrast the functionality and characteristics of [flip-flops](#).
- Apply [sequential circuit design](#) with registers and counters.

Tasks:

- Read the Learning Guide and the Reading Assignment
- Complete the Discussion Assignment by posting in the Discussion Forum
- Respond to two of your fellow classmates' posts in the Discussion Forum
- Complete and submit the Assignment Activity
- Take and submit the Self-Quiz
- Take and submit the Graded Quiz

