

# Discussion On the Out-of-Sync TDC Module from the HMS Chambers

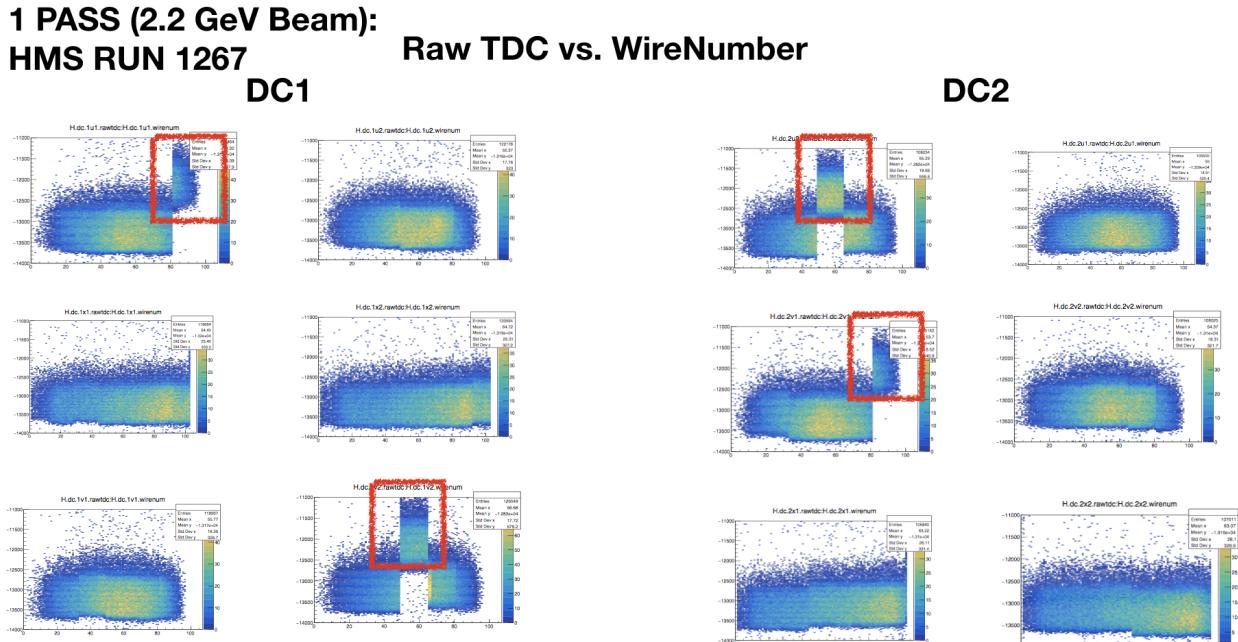
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During the Fall-2017/Spring-2018 Hall C Commissioning Run Period, it was found that a particular TDC module in the HMS Drift Chambers Crate (ROC03) was shifting the tdc times of the corresponding groups of wires being fed into it. At that time, this did not seem to be a serious concern, as this could be fixed in software. There also seem to be no issues with the residuals per wire after calibration. After the end of the Spring Run, I decided to look at the residuals per wire for a defocused run in the HMS (run 1267), and found certain groups of wire residuals to be significantly smeared compared to adjacent groups of wires (smeared residuals were being overshadowed by another problem fixed by Mark Jones during Spring 2018 SIDIS, concerning the hodoscope starttime). These were the same group of wires that exhibited the shift in time, which pointed to the same TDC module in ROC03, Slot 2. In this document I discuss the following:

- 1) the observations made that led to the determination of an out-of-sync TDC module
- 2) what was done to fix this problem in future experiments
- 3) how to minimize the impact on wire residuals for the Spring 2018 Run

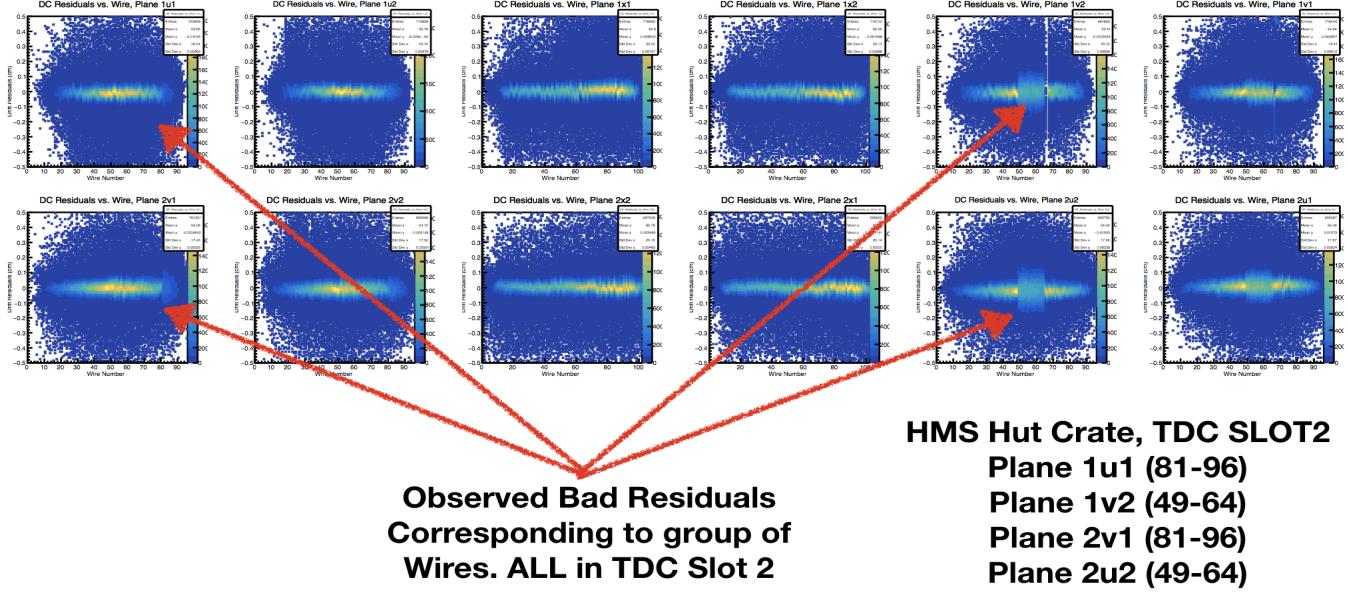
## 1 Observed Correlations on the Out-of-Sync TDC Module



**Figure 1:** The Raw TDC vs. Wire Number Correlations for all 12 planes. An offset of  $\sim 1000$  Channels ( $\sim 100$  ns) is observed in wire groups of four distinct planes.

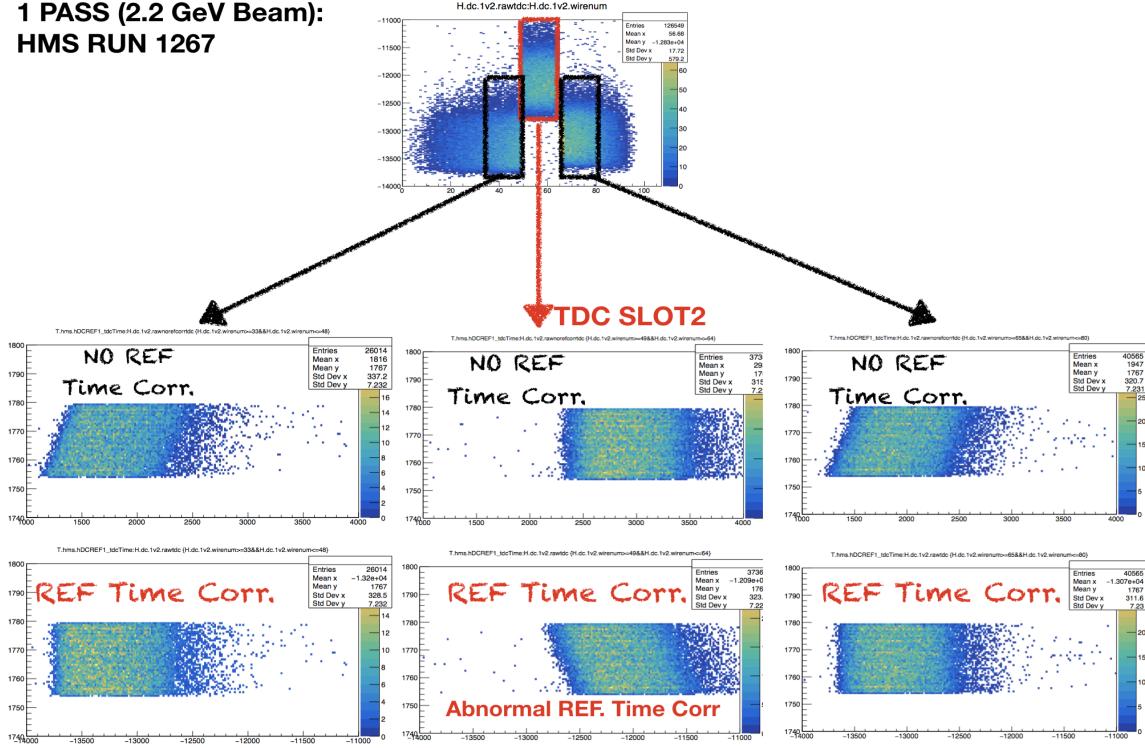
**1 PASS (2.2 GeV Beam):  
HMS RUN 1267**

# DC RESIDUALS



**Figure 2:** Correlation of Residuals vs. Wire Number for all 12 planes. Four wire groups stand out from the rest, with a noticeable smearing. The adjacent planes are affected by this smearing as well, even though they are NOT part of the problematic TDC.

**1 PASS (2.2 GeV Beam):  
HMS RUN 1267**

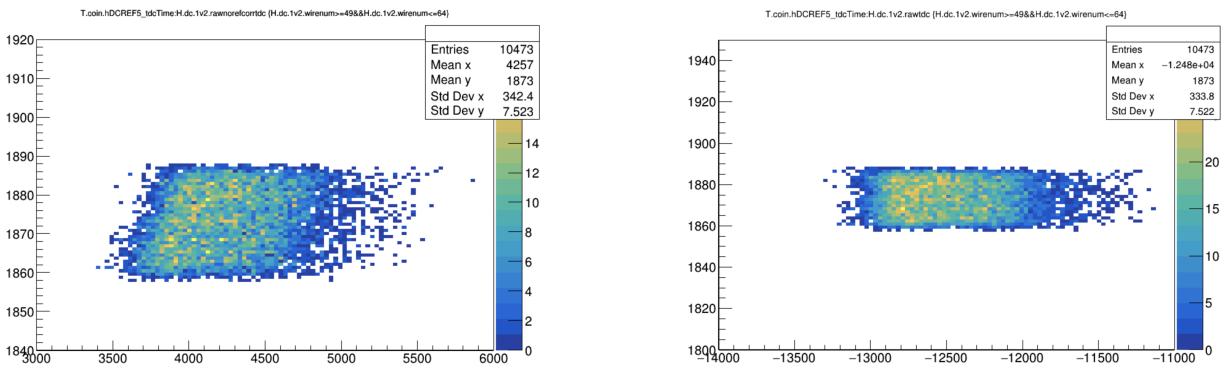


**Figure 3:** Correlation between the HMS DC reference time and the raw TDC times. The DC 1, V'-Plane raw TDC vs. Wire Number is shown. Adjacent wirgroups which are NOT in TDC Slot 2 are compared to the TDC Slot 2 wire group before and after applying the reference time correction to the TDC time.

The figure above is determinant in this study, as it shows the correlation of the reference time with the raw TDC time before and after applying the reference time correction. As expected from a normal TDC, when tdc times are reference time subtracted, the correlation disappears. However, in TDC Slot 2, the reference time and raw tdc time appear to be correlated, which is an indication that its copy of the reference time is Out-of-Sync with the rest of the modules.

## 2 Temporary Solution for the Out-Of-Sync TDC

As a temporary solution (until we replace the TDC module), it was decided that a copy of the reference time should be fed independently to the bad TDC module. This way, the TDC can synchronize with its own reference time, as opposed to receiving an alternate copy from the trigger interface (TI) module. This modification was done on hardware on July 30, 2018. See Log Entry <https://logbooks.jlab.org/entry/3583094>.



(a) TDC Slot 2 Reference Time vs. Raw Uncorrected TDC Time for wire group 1V2 (49-64)

(b) TDC Slot 2 Reference Time vs. Raw Corrected TDC Time for wire group 1V2 (49-64)

**Figure 4:** Results of HMS singles (DAQ in Coincidence Mode) cosmic run 4437 to verify that adding the new reference time to TDC Slot 2 fixes the problem. The subfigures above show the correlation between the newly added reference time and raw TDC Time from a group of wires in TDC Slot 2. As shown in subfigure (b), the correlation between the new reference time and raw TDC time is NOT present when the raw time is reference time corrected.

A modification in software map was also necessary, as the TDC Slot 2 needed to point to the new reference time. This change affects the data replay from the Spring 2018 Commissioning Run Period since during that time, TDC Slot 2 was pointing to a different reference time in the software. Therefore, if you have the most up-to-date copy of the hallc replay analyzer, to replay data from the previous run period, make sure to load the correct map into the replay script being used. See Figure 5

```
// Load the Hall C detector map
gHcDetectorMap = new THcDetectorMap();

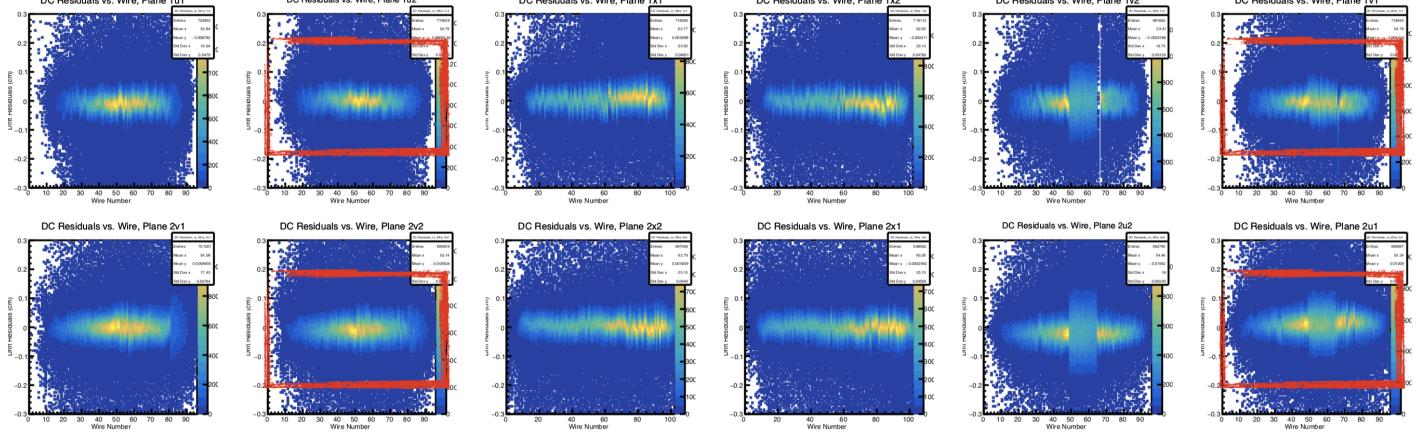
//gHcDetectorMap->Load("MAPS/HMS/DETEC/STACK/hms_stack.map");
gHcDetectorMap->Load("MAPS/HMS/DETEC/STACK/hms_stack_comm18.map");
```

**Figure 5:** Snippet of the hms replay script that shows how to load the correct map for Spring 2018 run period.

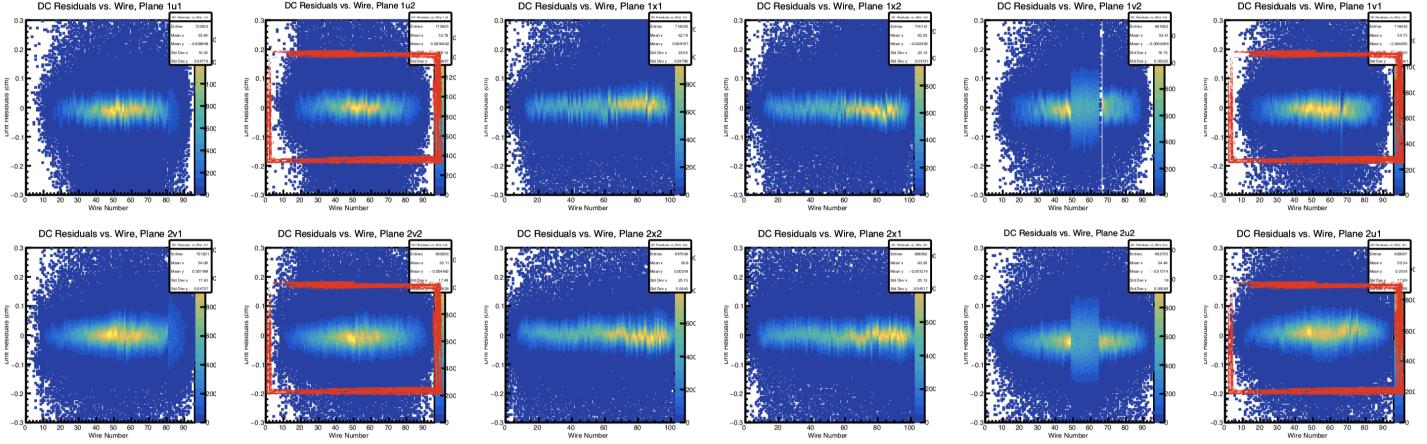
## 3 How to Minimize the impact the Out-of-Sync TDC had on Residuals

The group of wires that were affected by the TDC synchronization issue cannot be restored to normal, so their residuals will remain significantly smeared out. Fortunately, two of the groups are located at the edge of each chamber, so they have minimal impact. The other two groups, even though they are near the center of the chamber, they are in different chambers, so tracking in NOT severely affected. The group of wires in adjacent planes are affected (See Figure 3) by

this smearing, since they depend heavily on their adjacent partners (U,V planes) for the (x,y) coordinate determination from tracking. To minimize this effect, the Hall C source code *hcana* was modified to read in the sigma parameters on a per-wire basis, if a certain flag is set. Originally, the sigma parameters were read on a per-plane basis, and were assigned a value of 0.02 by default. If the sigma-per-wire flag is set, then the sigma parameter values are read on a wire-by-wire basis, and the wire groups associated with the Out-of-Sync TDC are assigned a sigma of 0.06, while the rest are assigned a value of 0.02 so that during the track-fitting algorithm, if a track passes through one of these groups, they will have a smaller contribution (or weight) to the fit. The results are shown in Figures 6.



(a) Residuals vs. Wire Number for all Chamber planes, with sigma-per-wire flag OFF.

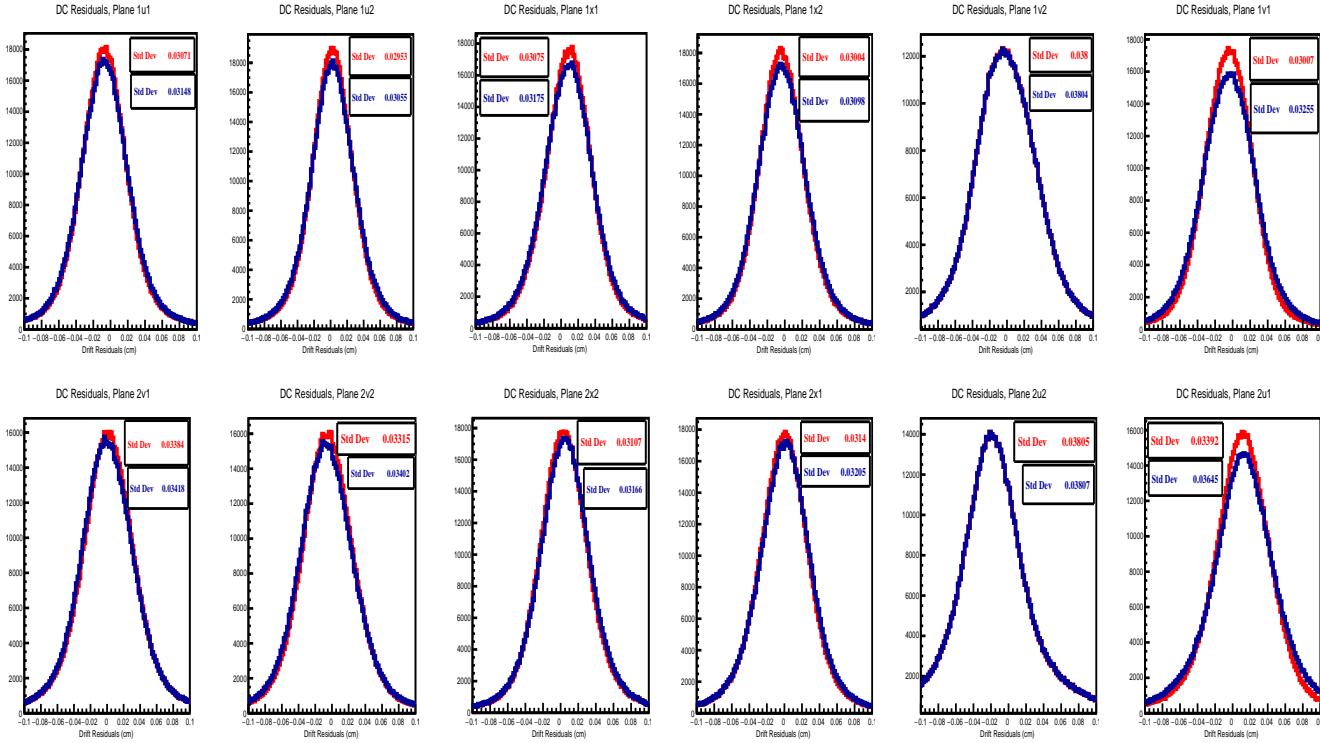


(b) Residuals vs. Wire Number for all Chamber planes, with sigma-per-wire flag ON.

**Figure 6:** Residuals vs. Wire Number for all Chamber planes. The **RED BOXES** highlight the group of wires that were affected, but had nothing to do with the out-of-sync TDCs wire groups.

In subfigure (b), when a sigma of 0.06 was applied to the bad groups (Planes 1u1, 1v2, 2v1, 2u2), the adjacent planes (highlighted in **RED**) wire residuals improved significantly. This is due to the fact that less weight (larger  $\sigma(\sigma)$ ) was given to the affected group of wires.

The plane residuals, however, did not have much improvement after increasing the weight factor. At most, there was a  $25 \mu\text{m}$  improvement in the Standard Deviation. See Figure 7



**Figure 7:** Plane residuals with the sigma-per-wire flag ON (RED) and OFF (BLUE)

It is important to note that the sigma-per-wire flag and the parameter file are NOT included in `hal1c_replay`, since this is a very specific scenario that ONLY applies to the Winter-2017/Spring-2018 Run Period. If users who took data during that time, and would like to try adding this flag and the parameter file, the instructions are as follows:

- Create the parameter file in path: /PARAM/HMS/DC/hdc\_sigma\_per\_wire.param

**Figure 8:** Snippet of sigma-per-wire parameter file.

- Include the parameter file in path: /DBASE/HMS/general.param

```
; HMS default calibration parameter files
#include "PARAM/HMS/CER/hcer_calib.param"
#include "PARAM/HMS/DC/hdc_calib.param"
#include "PARAM/HMS/DC/hdc_tzero_per_wire.param"
#include "PARAM/HMS/DC/hdc_sigma_per_wire.param"
#include "PARAM/HMS/HODO/hhodo_calib.param"
#include "PARAM/HMS/HODO/hhodo_Twcalib.param"
#include "PARAM/HMS/HODO/hhodo_Vpcalib.param"
#include "PARAM/HMS/AERO/haero_calib.param"
#include "PARAM/HMS/CAL/hcal_calib.param"
```

**Figure 9:** Snippet of general.param file.

In case you need help or do NOT have the time to create this parameter file, I have uploaded both the script that produces the parameter file and the parameter file itself to my github page.

**Link to script:**

[https://github.com/Yero1990/scientific\\_articles/blob/master/Spring\\_2018\\_Run/OutOfSync\\_TDC/write\\_sig\\_param.C](https://github.com/Yero1990/scientific_articles/blob/master/Spring_2018_Run/OutOfSync_TDC/write_sig_param.C)

**Link to parameter file:**

[https://github.com/Yero1990/scientific\\_articles/blob/master/Spring\\_2018\\_Run/OutOfSync\\_TDC/hdc\\_sigma\\_per\\_wire.param](https://github.com/Yero1990/scientific_articles/blob/master/Spring_2018_Run/OutOfSync_TDC/hdc_sigma_per_wire.param)