



## 1. Description

### 1.1. Project

Project Name	Project
Board Name	custom
Generated with:	STM32CubeMX 6.8.0
Date	05/08/2023

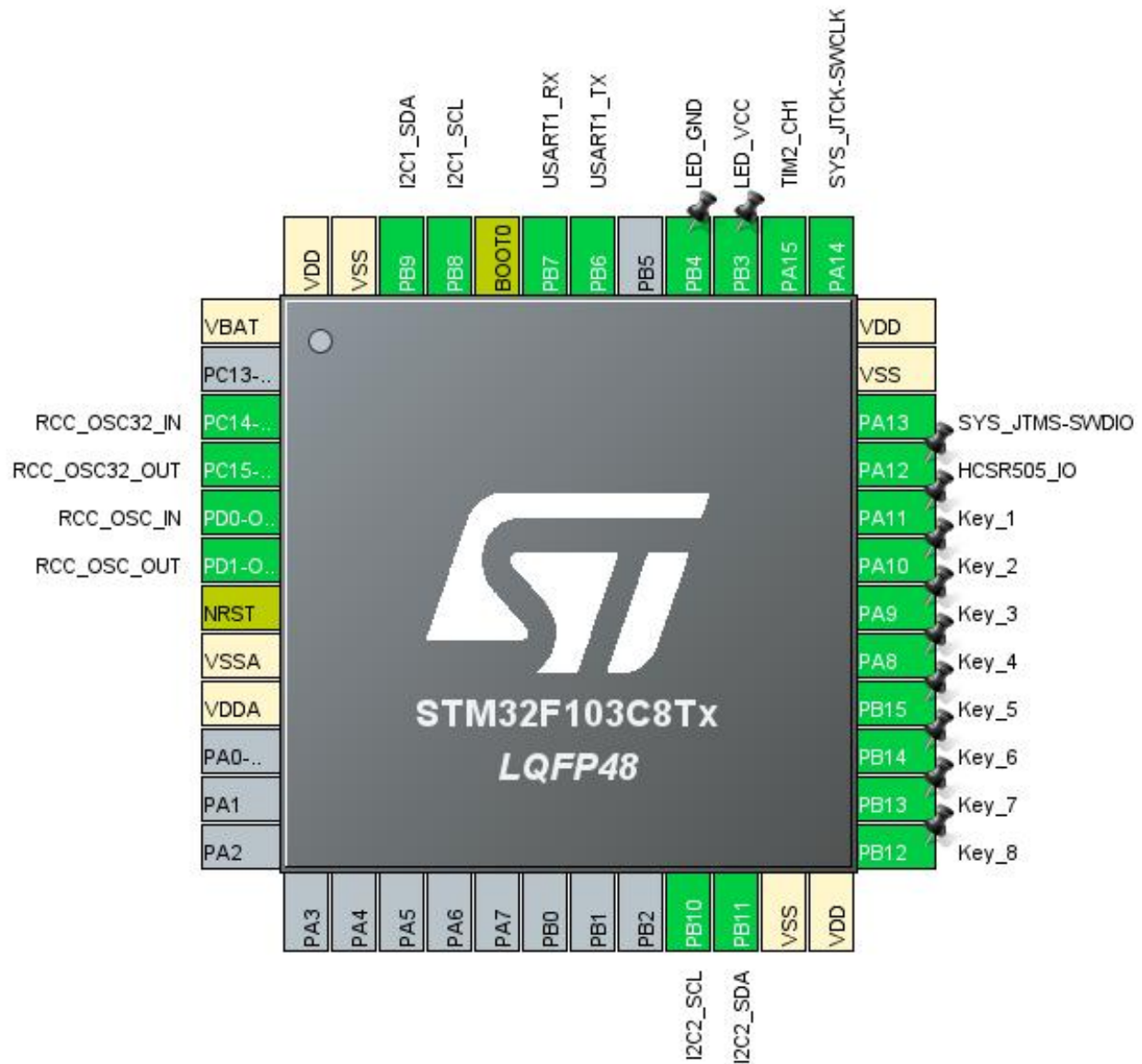
### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

### 1.3. Core(s) information

Core(s)	Arm Cortex-M3
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## 2. Pinout Configuration

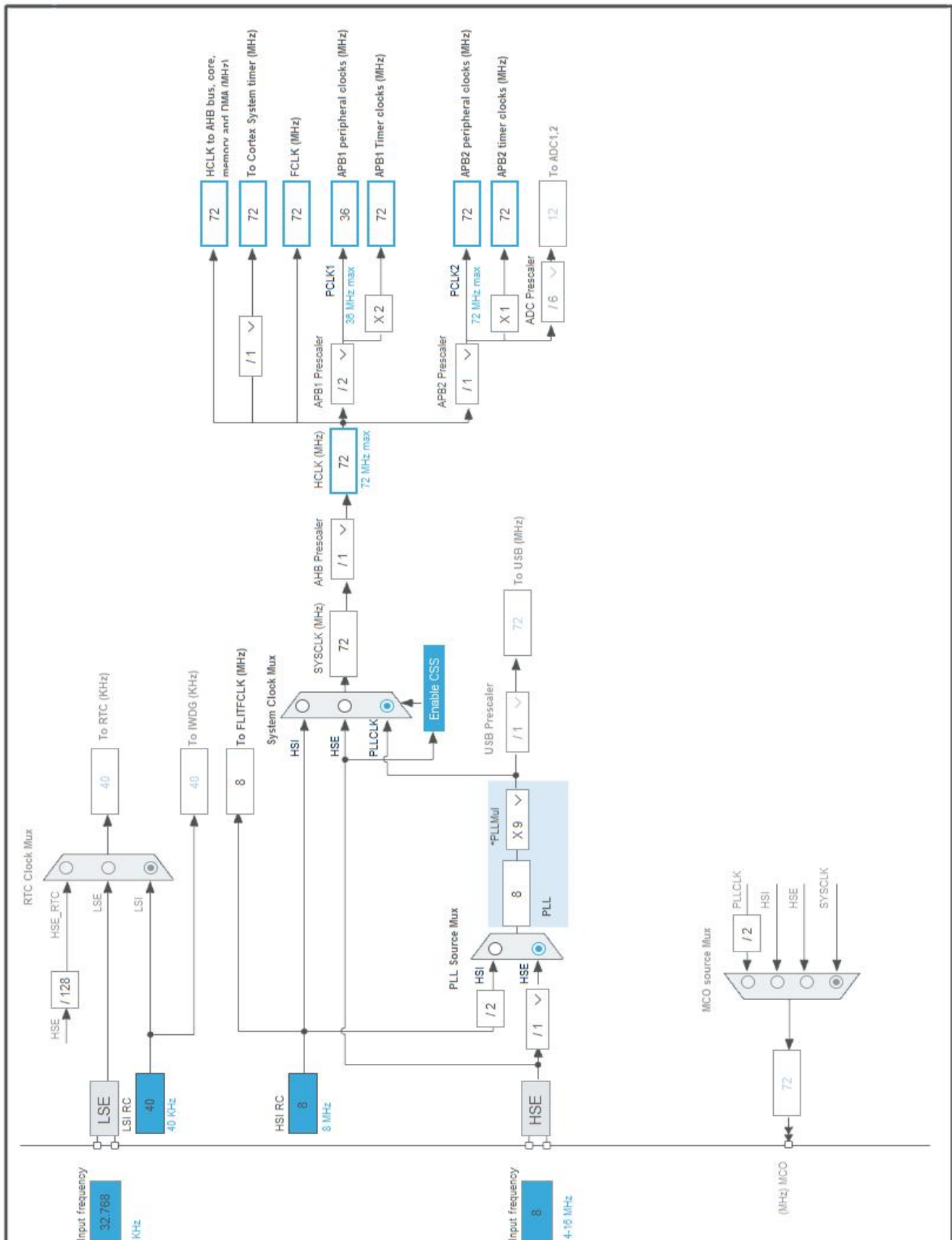


### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
21	PB10	I/O	I2C2_SCL	
22	PB11	I/O	I2C2_SDA	
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Input	Key_8
26	PB13 *	I/O	GPIO_Input	Key_7
27	PB14 *	I/O	GPIO_Input	Key_6
28	PB15 *	I/O	GPIO_Input	Key_5
29	PA8 *	I/O	GPIO_Output	Key_4
30	PA9 *	I/O	GPIO_Output	Key_3
31	PA10 *	I/O	GPIO_Output	Key_2
32	PA11 *	I/O	GPIO_Output	Key_1
33	PA12 *	I/O	GPIO_Input	HCSR505_IO
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
38	PA15	I/O	TIM2_CH1	
39	PB3 *	I/O	GPIO_Output	LED_VCC
40	PB4 *	I/O	GPIO_Output	LED_GND
42	PB6	I/O	USART1_TX	
43	PB7	I/O	USART1_RX	
44	BOOT0	Boot		
45	PB8	I/O	I2C1_SCL	
46	PB9	I/O	I2C1_SDA	
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	Project
Project Folder	D:\Documents\GithubProjects\STM32_BluetoothSmartLight\Project
Toolchain / IDE	MDK-ARM V5.32
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_I2C1_Init	I2C1
4	MX_TIM1_Init	TIM1
5	MX_USART1_UART_Init	USART1
6	MX_TIM2_Init	TIM2
7	MX_I2C2_Init	I2C2

## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	DS5319_Rev17

### 6.2. Parameter Selection

Temperature	25
Vdd	3.3

### 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1



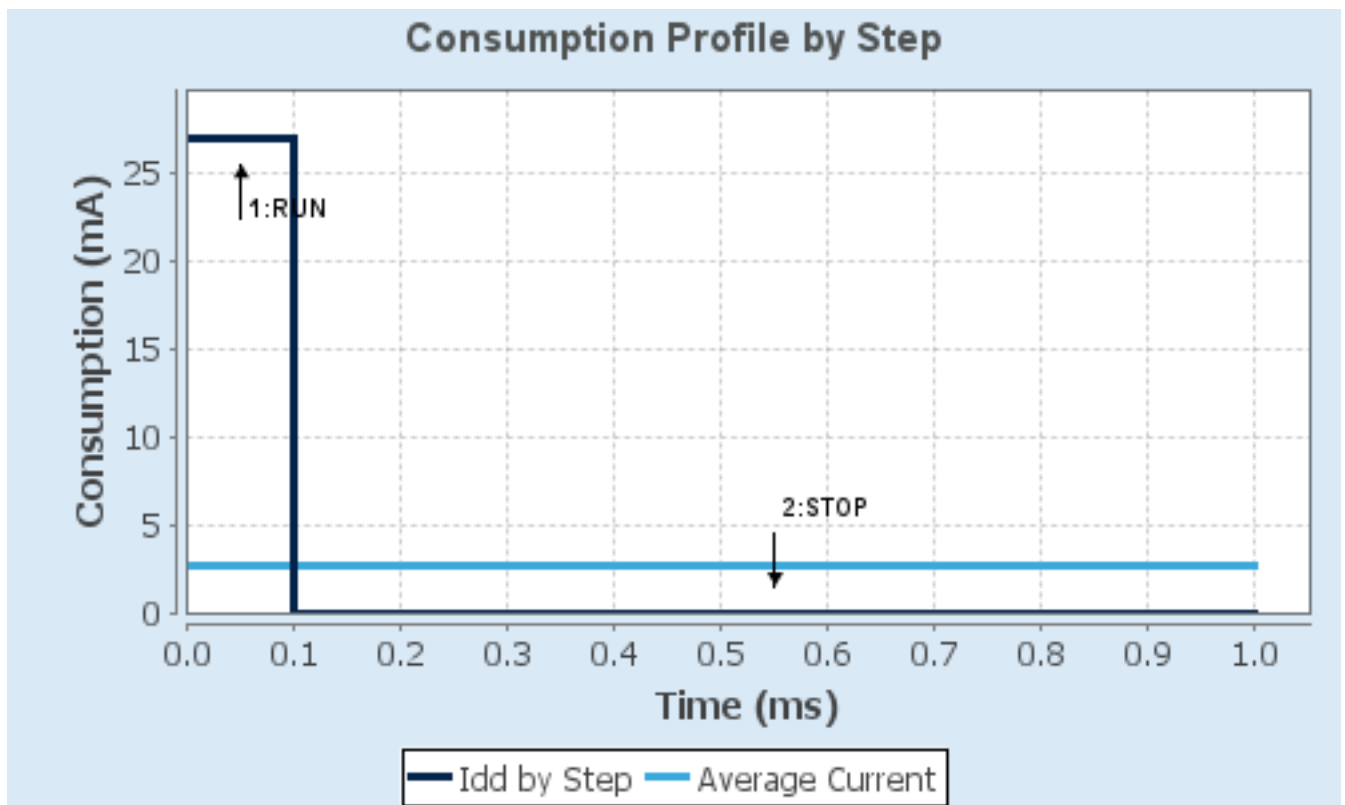
#### 6.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	No Scale	No Scale
<b>Fetch Type</b>	FLASH	n/a
<b>CPU Frequency</b>	72 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL	Regulator LP
<b>Clock Source Frequency</b>	8 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	27 mA	14 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	90.0	0.0
<b>Ta Max</b>	100.1	105
<b>Category</b>	In DS Table	In DS Table

#### 6.5. Results

Sequence Time	1 ms	Average Current	2.71 mA
Battery Life	1 month, 21 days, 17 hours	Average DMIPS	61.0 DMIPS

#### 6.6. Chart



## 7. Peripherals and Middlewares Configuration

### 7.1. I2C1

#### I2C: I2C

##### 7.1.1. Parameter Settings:

###### Master Features:

I2C Speed Mode	Standard Mode
I2C Clock Speed (Hz)	100000

###### Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

### 7.2. I2C2

#### I2C: I2C

##### 7.2.1. Parameter Settings:

###### Master Features:

I2C Speed Mode	Standard Mode
I2C Clock Speed (Hz)	100000

###### Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

### 7.3. RCC

**High Speed Clock (HSE): Crystal/Ceramic Resonator**

**Low Speed Clock (LSE) : Crystal/Ceramic Resonator**

##### 7.3.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
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Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

**RCC Parameters:**

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

## 7.4. SYS

**Debug: Serial Wire**

**Timebase Source: SysTick**

## 7.5. TIM1

**Clock Source : Internal Clock**

### 7.5.1. Parameter Settings:

**Counter Settings:**

Prescaler (PSC - 16 bits value)	<b>71 *</b>
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	65535
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

**Trigger Output (TRGO) Parameters:**

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

## 7.6. TIM2

**Clock Source : Internal Clock**

**Channel1: PWM Generation CH1**

### 7.6.1. Parameter Settings:

**Counter Settings:**

Prescaler (PSC - 16 bits value)	<b>71 *</b>
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	<b>19999 *</b>
Internal Clock Division (CKD)	No Division

auto-reload preload

Disable

### Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection

Reset (UG bit from TIMx\_EGR)

### PWM Generation Channel 1:

Mode

PWM mode 1

Pulse (16 bits value)

**500 \***

Output compare preload

Enable

Fast Mode

Disable

CH Polarity

High

## 7.7. USART1

### Mode: Asynchronous

#### 7.7.1. Parameter Settings:

##### Basic Parameters:

Baud Rate

**9600 \***

Word Length

8 Bits (including Parity)

Parity

None

Stop Bits

1

##### Advanced Parameters:

Data Direction

Receive and Transmit

Over Sampling

16 Samples

**\* User modified value**

## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB8	I2C1_SCL	Alternate Function Open Drain	n/a	High *	
	PB9	I2C1_SDA	Alternate Function Open Drain	n/a	High *	
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
TIM2	PA15	TIM2_CH1	Alternate Function Push Pull	n/a	Low	
USART1	PB6	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PB7	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
GPIO	PB12	GPIO_Input	Input mode	Pull-up *	n/a	Key_8
	PB13	GPIO_Input	Input mode	Pull-up *	n/a	Key_7
	PB14	GPIO_Input	Input mode	Pull-up *	n/a	Key_6
	PB15	GPIO_Input	Input mode	Pull-up *	n/a	Key_5
	PA8	GPIO_Output	Output Push Pull	Pull-down *	High *	Key_4
	PA9	GPIO_Output	Output Push Pull	Pull-down *	High *	Key_3
	PA10	GPIO_Output	Output Push Pull	Pull-down *	High *	Key_2
	PA11	GPIO_Output	Output Push Pull	Pull-down *	High *	Key_1
	PA12	GPIO_Input	Input mode	Pull-down *	n/a	HCSR505_IO
	PB3	GPIO_Output	Output Push Pull	Pull-up *	High *	LED_VCC
	PB4	GPIO_Output	Output Push Pull	Pull-down *	High *	LED_GND

## **8.2. DMA configuration**

nothing configured in DMA service

### 8.3. NVIC configuration

#### 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break interrupt	unused		
TIM1 update interrupt	unused		
TIM1 trigger and commutation interrupts	unused		
TIM1 capture compare interrupt	unused		
TIM2 global interrupt	unused		
I2C1 event interrupt	unused		
I2C1 error interrupt	unused		
I2C2 event interrupt	unused		
I2C2 error interrupt	unused		
USART1 global interrupt	unused		

#### 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true



**\* User modified value**

## 9. System Views

### 9.1. Category view

#### 9.1.1. Current

Middleware				
System Core	Analog	Timers	Connectivity	Computing
DMA		TIM1 ✓	I2C1 ✓	
GPIO ✓		TIM2 ✓	I2C2 ✓	
NVIC ✓			USART1 ✓	
RCC ✓				
SYS ✓				

## 10. Docs & Resources

Type	Link
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip">https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32ibis.zip">https://www.st.com/resource/en/ibis_model/stm32ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f1_svd.zip">https://www.st.com/resource/en/svd/stm32f1_svd.zip</a>
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip">https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32ibis.zip">https://www.st.com/resource/en/ibis_model/stm32ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f1_svd.zip">https://www.st.com/resource/en/svd/stm32f1_svd.zip</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf">https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf</a>
Training Material	<a href="https://www.st.com/resource/en/sales_guide/sg_sc2155.pdf">https://www.st.com/resource/en/sales_guide/sg_sc2155.pdf</a>
Brochures	<a href="https://www.st.com/resource/en/brochure/breveco0518.pdf">https://www.st.com/resource/en/brochure/breveco0518.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstmcsuite.pdf">https://www.st.com/resource/en/flyer/flstmcsuite.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/fldpstpf11120.pdf">https://www.st.com/resource/en/flyer/fldpstpf11120.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/1239988349.pdf">https://www.st.com/resource/en/certification_document/1239988349.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-">https://www.st.com/resource/en/application_note/an1709-emc-design-</a>

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

- Application Notes [https://www.st.com/resource/en/application\\_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf)
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- Application Notes [https://www.st.com/resource/en/application\\_note/an3156-usb-dfu-](https://www.st.com/resource/en/application_note/an3156-usb-dfu-)

protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes [https://www.st.com/resource/en/application\\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3429-stm32-proprietary-code-protection-overview-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3429-stm32-proprietary-code-protection-overview-stmicroelectronics.pdf)

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