3EJ4 LAB FOUR

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Questions for Part 1:

Q1.

- (1). Based on the simulation data obtained in Step 1.2, the low-frequency voltage gains in dB are: Ad1 = 7.38, Ad2 = 70.05, and Ad3 = 0.
- (2). Based on the simulation data obtained in Step 1.2, the overall voltage gain for the differential-mode signal is $Ad = 77.43 \, dB$ or Ad = 7437.8 which is the real magnitude.
- (3). The non-inverting input of the operational amplifier is V2, since at low-frequency the simulated phase of the output voltage Vo is in phase with the phase offset of V2.
- (4). The upper 3-dB frequency fH of the operational amplifier is approximately 6.338 kHz, and this value was determined the frequency when the phase drops by 45 degrees from its initial value.
- **Q2.** The differential-mode gain Ad1 in Q1, 7.38 dB, is around one-tenth of the differential-mode gain Ad in Lab 3, 69.95 dB. The difference is due to feedback received by the emitters of the PNPs in the current mirror, which affects the differential amplifier's collector current at the output. A drop in voltage gain is most likely due to an increase in emitter currents.
- Q3. The input resistance Rin = $81.757 \text{ k}\Omega$ and the output resistance Ro = 461Ω are calculated using the simulated results obtained in Steps 1.2 and 1.3. This corresponds to the expected resistances for an operational amplifier, which are high input and low output.

Q4.

(1). Figure 4.1(a) and (b) shows the plots for the output voltage Vo vs. the time features at 1 KHz for the simulated and observed results from Steps 1.6 and 1.13.

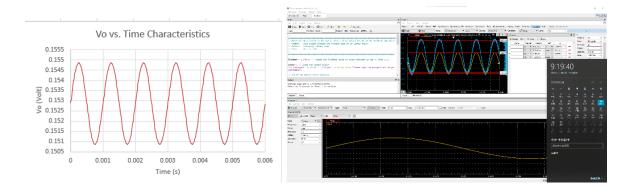


Figure 4.1(a) Figure 4.1(b)

(2). The computed findings have a peak to peak voltage of 40 mV, whereas the measured data have a peak to peak value of roughly 4 V. The simulated findings' AC amplitude, Vp, and DC voltages, Vdc, are 20 mV and 155 mV, respectively, but the real results' Vp and Vdc are 2 V and -15 mV. The differences in Vpp and Vp voltages can be explained by the different input AC voltages at V1. Because the measured peak to peak voltage and AC voltage are 1000 times greater than the simulated peak to peak voltage and AC voltage, the measured peak to peak voltage and AC voltage are 1000 times greater than the simulated peak to peak voltage and AC voltage. Aside than that, both sets of data have the same frequency/period and behave similarly.

Q5.

(1). Figure 5.1 exhibit plots of the simulated and observed findings for the voltage gain magnitude and phase vs. frequency characteristics from Steps 1.7 and 1.13.

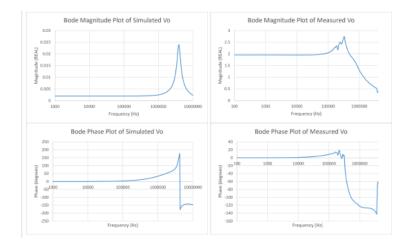


Figure 5.1

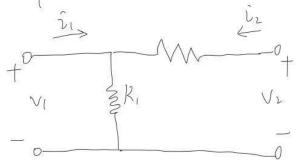
(2). In order to operate this amplifier, its highest operating frequency to provide a constant gain as design is about 100 kHz according to the measurement.

Q6.

A series-shunt feedback configuration is used in the circuit. The input is connected in series to the directional amplifier, whilst the output provides feedback to the differential amplifier, resulting in a shunt connection.

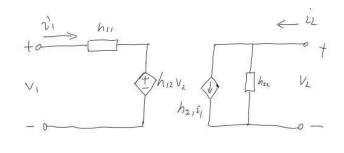
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R7. B network:



use h- parameters

$$\begin{cases} V_1 = h_{11}\hat{i}_1 + h_{12}V_2 \\ i_2 = h_{21}i_1 + h_{21}V_2 \end{cases}$$



 $|2_{11} = h_{11} = \frac{v_1}{\tilde{v}_1}|_{v_2=0} = |2_1|_{1|R_1} = |ook|_{1|ook} = 50ks$

$$R_{21} = \frac{1}{h_{21}} = \frac{v_{1}}{i2} \Big|_{\dot{1}_{1}=0} = \frac{R_{1} + R_{2}}{|R_{1}+R_{2}|} = \frac{l_{00} k}{l_{00}k} = \frac{200 k}{0.5}$$

$$\beta = h_{12} = \frac{v_{1}}{v_{2}} \Big|_{\dot{1}_{1}} = 0 = \frac{R_{1}}{|R_{1}+R_{2}|} = \frac{l_{00} k}{l_{00}k} = 0.5$$

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R8. Dis Connect the B network from A network Vi Vi Vd ParVd Res Exc. Vo

ri'= ki+lin=81757. 32 +50kx = 131.76ka

Y = R | | | R 12 | | 10 = 246 ka | 1 200ka | 1 46/ e = 459 a

AV = Vo = Av - ri + Rec ||Re = 7437.8 x 81.757 200k ||240k = 4596 V/V 1 71's= 11' [HAV'B) = 131-76k [H 4596 X0.5] = 303 MA rif = 10'/(HAV'B) = 459/(H 4596 XOS) =0.20 A' vf = Av' = 4896 = 20/U

1. Ri-rit-Rs=301M - 0 =303 Ma Ro = 1 = -1 = -1 = -0.2 x

D In Conclusion, Avf = 20/V, Ri = JosMa, Ro = 0.20.

Questions for Part 2:

Q9.

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Q9, ~! C=C_1=C_2 And
$$R=R_3=R_4$$
 $V_0=V+(I+\frac{1}{sC_R})+I_2V+\int_{I_2}^{I_2}+sC(I+\frac{1}{sC_R})$
 $=V+(I+\frac{1}{sC_R})+V_4+(2+sC_R)$
 $=V+(3+\frac{1}{sC_R})+cC_R$
 $\Rightarrow \frac{V_+}{V_0}=\frac{s/c_R}{s^2+s(\frac{1}{c_R})+cC_R}$
 $\Rightarrow V_0=\frac{s/c_R}{s^2+s(\frac{1}{c_R})+cC_R}$
 $\Rightarrow V_0=\frac{I+\frac{R_2}{R_1}}{s^2+s\frac{1}{c_R}+cC_R}$
 $\Rightarrow V_0=\frac{I+\frac{R_2}{R_1}}{s^2+s\frac{1}{c$

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[3]0. The characteristic equation of the system is $1-L(s)=1-\frac{1}{(1+\frac{|C_L|}{|C_L|})}\frac{s/cR}{s^2+s\frac{2}{C_L}+|C_L|^2}=0$. To find the pole R, we can convert L(s) into the frequency domain, can find that $L(s)=\frac{s}{(ct_R)^2-w^2+s\frac{3cw}{cR}}$. Then the equation, we see that the value of $|R_L|_{F_L}=2$ will give us $\frac{3W}{RC}$ in both the numerator and denominator, leaving us with $-W^2-(C_R)^2$ or a pole R at $(C_R)^2$ due to the Barkhausen Criterion.

Q11.

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[211. The Settling times for |Z_=120 Ks, 240 ks and 280 ks are approximately 2.51ms, 1.4ms and 0.63ms. The settling time decreases as be value of 12 shorage, This behaviour is are to the loop gain LCS) (thom QG) In cleasing as the value of 12 inchapes, decreasing the amount of time tre oscillator circuit requires to leach saturation |501

Q12.

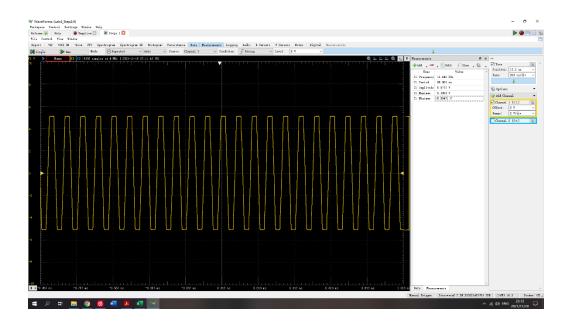


Fig 12.1(Screenshot for 2.9)(measured data)

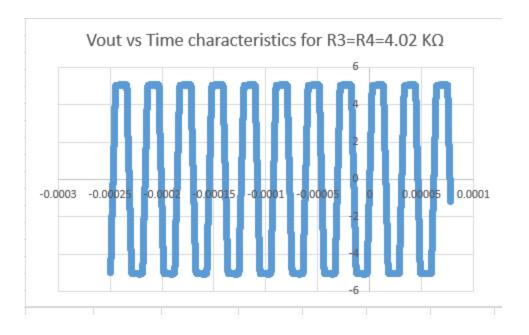


Fig 12.2(Screenshot for 2.10)(measured data)

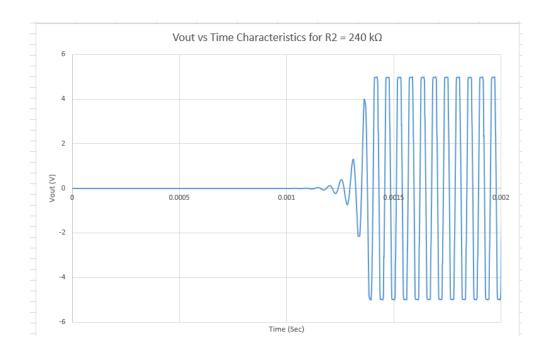


Fig 12.3(simulated data)

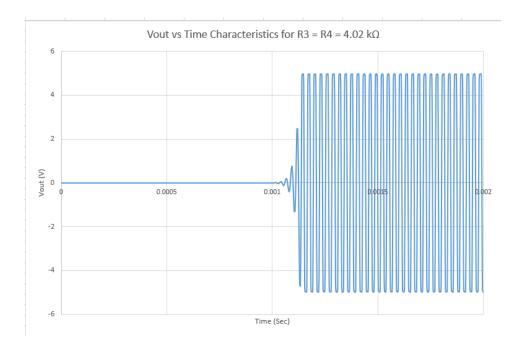


Fig 12.4(simulated data)

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Q12 (2)

The frequencies in Step 2.4 (S/mulated) was found to be approximately 17.5 kHz, while be frequencies in Step 2.9 (measured) was found to be approximately 11.641kHz. The distrince between simulated data and measured data is due to some error in measurement. However, for the frequencies in step 2.6 (Simulated) and 2.10 (measured) were found to be to approximately 33 kHz. This matches the expected results from theory, as the frequency is inversely proportional to be values of 2 and C.

Thus, it I directore the value of R to half its value, then it nearly danbles the treating of the oscillations.