Homework08

Chapter9

- **9.1** :

  a. Device register is the register which is used for data transfer an input/output device.

  b. DDR is the register to store the input or output.

  c. DSR is the register to show the status of input or output.
- **9.2** : Because the porcessor know when the data is ready to be operated.
- **9.9** : It may leads the lost of the previous data.
- **9.10** : It may leads the lost of the previous data.
- **9.14** : The address control logic will control whether to be load.