

Homework04

Chapter4

- **4.3** : The PC don't count anything, it just point to the address where the next instruction is. So the *instruction pointer* is a better name.
- **4.8** :
 - a. 8 bits
 - b. 7 bits
 - c. 10 bits
- **4.9** : Obtain the next instruction from memory.
- **4.19** :
 - a. MAR : x2 , MDR : 01010000
 - b. MDR : 01100010

Chapter5

- **5.1** :
 - ADD : operate instruction, immediate/register
 - JMP : control instruction, base+offset
 - LEA : operate instruction, PC-relative
 - NOT : operate instruction, register
- **5.4** :
 - a. 8 bits
 - b. 5 bits
 - c. 101 in binary
- **5.9** : c