Index

6	ALU, 19, 22, 23, 56, 58, 59, 71, 72, 73, 74 always, 102
64-bit addition, 189	analog watchdog, 395
64-bit logic shift left, 192	APB, 55
64-bit multiplication, 194	application binary interface, 161, 179, 199
64-bit operations, 189	application program status register, 71, 101
64-bit shifted right, 193	APSR, 71, 88, 101
64-bit sign extension, 191	arithmetic
64-bit signed division, 197	add, 76
64-bit subtraction, 190	add with carry, 76
of the out traction, 150	multiply and subtract, 76
A	reverse subtract, 76
\mathbf{A}	subtract, 76
ABI, 155	subtract with carry, 76
access assembly variable, 217	arithmetic logic unit, 56
access C variables, 215	ARM EABI, 157
accumulator-based instruction set, 58	ARM state, 54
ACK, 48, 440, 442, 444, 445, 452	ARM32, 53, 54, 70
ADC, 387	ARM64, 53, 70
continuous mode, 394	Armstrong numbers, 141
digital quantization, 388	ASCII, 47
DMA, 403	assembly function in C programs, 212
external trigger, 396	assembly instruction, 63
freeze mode, 397	assembly program, 2
quantization error, 392	asynchronous transmission, 427
sample-and-hold amplifier, 390	AT commands, 438
sampling time, 390, 391	atoi, 144
scan mode, 395	audio, 479
single mode, 394	AVRCP, 437
software trigger, 396	71 (RC1) 107
trigger, 396	D
add, 76	В
add with carry, 64, 76	Barrel shifter, 61, 73, 74, 79
ADR, 92	base, 27, 221
ADSR, 421	BASEPRI, 14, 88, 260, 261
advanced high-performance bus, 55	baud rate, 429
advanced peripheral bus, 55	BCD encodes, 373
AHB, 55, 263	bias constant, 230
ALIGN, 202	biased exponent, 230
aligned memory accesses, 204	big endianness, 94
alignment, 201	binary executable, 1
double-word alignment, 201	binary interface, 155
halfword alignment, 201	binary numbers, 27, 29
word alignment, 201	binary search, 145
alternative functions, 293	bipolar, 343
and the farite for the first f	1 '

bit, 25	bus, 4, 56
bit mask, 81	bus matrix, 55
check a bit, 82	BX, 106
clear a bit, 82	byte, 25
set a bit, 82	byte order, 84
toggle a bit, 83	byte-addressable, 5
bit mask, 81	,
bit order, 84	C
bit stuffing, 467	•
bitwise logic	calendar time, 371
AND, 80	call a C function from assembly, 216
bit clear, 80	call an assembly subroutine from C, 214
move not, 80	caller, 9, 152, 155, 161, 163, 178
BL, 106	calloc, 8
Bluetooth, 436	carry flag, 34, 35, 40, 71
address, 436	CBNZ, 106
AT commands, 438	CBZ, 106
HC-05, 437	CDC, 479
HC-06, 437	change processor state, 260
paring, 437	change-on-zero, 467
BLX, 106	check a bit, 82
boot loader, 18	circular right rotate, 92
branch	clear a bit, 82
branch if equal, 104	clock arbitration, 442
branch if negative, 104	clock phase, 460
branch if not equal, 104	clock polarity, 460
branch if overflow set, 104	clock synchronization, 442
branch if positive or zero, 104	CMAR, 383
branch if signed greater or equal, 104	CNDTR, 383
branch if signed greater than, 104	collector, 295
branch if signed less than, 104	comment, 2, 20, 60, 61, 62
branch if signed less than or equal, 104	compare and branch on non-zero, 106
branch if unsigned higher, 104	compare and branch on zero, 106
branch if unsigned higher or same, 104	compare and capture register, 319, 325
branch if unsigned lower, 104	compare negative, 65, 72, 87
branch if unsigned lower or same, 104	compound Boolean expression, 109
branch label, 104	compound logical expression, 110
table branch byte, 117	condition flags, 101, 107
table branch half-word, 117	conditional branch instruction, 104
branch and exchange, 65, 106, 152	conditional execution, 107
branch and link, 65, 106, 152	context switch, 493, 496, 498, 499
branch indirect with link, 106	continue statement, 115
branch instruction, 104	control register, 410, 452, 487, 489
branch with link and exchange, 65	control structures, 123
break statement, 116	coordinated universal time, 371
bubble sort, 147	Cortex-A, 54
buffered output, 411	Cortex-M, 54
bulk transfers, 468	Cortex-M0, 54, 275

Cortex-M1, 54	dividing by zero, 272
Cortex-M4, 54, 70, 275	DMA, 381, 403
Cortex-R, 54	DMA and interrupt enable register, 332
count digits, 136	double precision, 230
count the number of 1-bits, 132	double-word, 25
counter overflow, 341	do-while loop, 114
counter underflow, 341	duty cycle, 322
CPAR, 383	duty ratio, 357
CPS, 260	•
CPSID, 261	E
CPSIE, 65, 261, 490, 491, 500	L
CRC, 469	EABI, 166, 214, 216
	electromagnetic interference, 299
D	ELF, 3, 23, 514
D	embedded assembly in C programs, 213
DAC, 407	EMI, 299
buffered output, 411	END, 67
control register, 411	end of packet, 469
data output register, 410	endianess, 84
resolution, 408	endianness, 94, 96, 119, 255
sampling rate, 408	ENDP, 67
trigger, 410	endpoint, 465, 468, 469, 470, 471, 472, 474, 477,
Darlington array, 354	480
data alignment, 201	enumeration, 475
data comparison, 87	EOP, 468, 469, 470
data memory, 5, 7, 8, 9, 17, 18, 19, 20, 21, 22, 23,	EPSR, 71, 88
55, 70, 129, 159, 188, 207, 318, 363, 366, 426	EQU, 68, 303
data memory barrier, 65	equal, 102
data minus, 466	event generation register, 324
data plus, 466	exception, 252
data structure padding, 203	exception handling, 272
data symbols, 1	executable and linkable format, 3
data synchronization barrier, 65	executable file, 1
DCB, 68	executable interface, 3
DCD, 68	execution program status register, 71
DCQ, 68	execution view, 4
DCW, 68	export, 163, 214
De Morgan's laws, 110	extern, 215, 217
debouncing, 305	external trigger, 397
decimal, 27, 221	external voltage reference, 393
decode, 282	
deformalized, 235	F
delay, 267	-
differential signals, 466	factorial numbers, 131, 182
digital input filter, 330	FAULTMASK, 14, 88, 260, 261
digital quantization, 388	FHSS, 436
direct memory access, 381	FILL, 68
directives, 3, 62, 66, 67, 69	find maximum, 134

fixed-point numbers, 222	global static variable, 206
accuracy, 224	goto, 123
addition, 225	goto statement, 125
division, 227	GPIO, 293
multiplication, 226	data input register, 302
Qm.n format, 223	data ouput register, 302
range, 224	input data register, 294
resolution, 224	open drain, 297
subtraction, 226	output data register, 294
flash memory, 7	pull down, 294
floating-point numbers, 228	pull up, 294
addition, 240	push pull, 295
bias constant, 230	slew rate, 298
biased exponent, 230	strong pull down, 295
double precision, 229, 230	strong pull up, 295
fraction field, 230	weak pull down, 295
IEEE 754, 228	weak pull up, 295
multiplication, 244	greatest common divisor, 165
normalized notation, 229	
overflow, 234	Н
range, 236	11
rounding rules, 238	half stepping, 349
rounding to even, 239	half-word, 25
rounding up, 240	Hamming distance, 132
sign bit, 230	harmonics, 299, 420
single precision, 229	Harvard architecture, 4, 7, 10, 17, 23
special values, 233	heap, 8, 148, 159, 207
subnormal numbers, 235	hexadecimal, 27
truncation, 239	HID, 479, 480
underflow, 234	HID descriptor, 481, 483
floating-point unit, 222	HSI, 339
flowcharts, 126	
for loop, 112	I
FPU, 412	•
fraction field, 230	I ² C, 439
freeze mode, 397	ACK, 440
frequency hopping, 436	NACK, 440
FT232R, 430	open drain, 439
full stepping, 345, 346, 347, 348, 349, 351	SCL, 439
function, 151	SDA, 439
	start, 440
G	stop, 440
9	ICPSC, 331
GAVDP, 437	if-then statement, 108
GCD, 165	if-then-else statement, 111
general-purpose integer registers, 13 get, 69	immediate number, 58, 61, 76, 88, 91, 92, 93, 277 282, 283, 285, 490
global, 214	import, 163, 215

include, 69	K
initialized data segment, 8	
injected channel, 400	K state, 467
inline assembly, 212, 213	keypad reverse scanning, 311
input capture, 329	keypad scanning algorithm, 308
prescaler, 331	
instruction memory, 5, 7, 8, 13, 14, 18, 20, 21, 23,	L
53, 55, 56, 276	
instruction synchronization barrier, 65	label, 2, 60, 62, 117, 126
interface class, 472	last-in-first-out, 153
internal reference voltage, 393	LCD, 355
interrupt, 249	bias, 357, 358
active bit register, 254	common terminals, 362
clear pending register, 254	data memory, 363
controller type register, 254	display memory, 363
enable and disable, 255	duty ratio, 357
external interrupt, 252, 269	encoding, 366
interrupt levels, 258	LDR, 92
interrupt number, 254	LDRB, 65, 98, 116, 142, 143, 144, 164, 167, 168, 171,
pending bit, 255	174, 184, 200, 201, 213, 214, 220, 503, 504, 510
preemption priority, 257	LDRSB, 65, 97, 98, 200, 201, 503, 510
priority, 251	leap second, 372
set enable register, 254	LIFO, 153
set pending register, 254	linkable interface, 3 little endianness, 94, 255
software interrupts, 271	load
software trigger, 255	load byte, 98
stacking, 253	load half-word, 98
unstacking, 253 interrupt clear enable register, 254	load multiple words, 98
interrupt crear enable register, 254	load signed byte, 98
interrupt priority register, 257	load signed half-word, 98
interrupt priority register, 237	load word, 98
interrupt service routine, 249	load constant into registers, 91
interrupt transfers, 468	load register exclusive, 65
intra-procedure-call register, 156	load register with byte, 200, 201
Intra-Procedure-Call register, 156	load register with double-word, 201
IP, 257	load register with signed byte, 200
IPSR, 71, 88	load register with signed halfword, 201
ISER, 254, 255, 256, 257, 379	loading effects, 411
isochronous transfers, 468, 474, 479	loading view, 4
IT instruction, 105	load-store instruction set, 58, 59
itoa, 170	local static variable, 206, 208
	logic shift left, 64, 279
Ī	logic shift right, 64, 193
,	lookup table, 412
J state, 467	loop structure, 124
Java executables, 1	
IEOC 400	

JEOC, 400

M	negative flag, 71 NMI, 14, 260
machine program, 1, 3, 4, 17	noise filtering, 330
main stack, 14, 250, 254, 273, 487, 497, 498, 499,	non-preemptive, 249
501	non-return-to-zero inverted, 466
major opcode, 279	non-static variable, 207
malloc, 8	non-system interrupts, 254
mask, 81	non-volatile, 4, 7, 211
matrix transpose, 172	normalized presentation, 235
memory	NOT, 88
data memory, 4	not any number, 233
instruction memory, 4	note equal, 102
main memory, 4	NRZI, 466
memory address index formats, 97	NULL, 48, 49, 52, 143, 149, 164, 168, 171, 218, 436
memory address modes, 95	NULL terminator, 48
memory addressing modes, 95	NVIC, 56, 250, 253, 254, 255, 256, 259, 260, 262,
pre-index, 96	270, 432, 463, 498
micro-stepping, 351	Nyquist–Shannon, 408
minor opcode, 278, 279	Try quist offurnion, 100
MISO, 457	0
mnemonic, 2, 60, 61, 72	O
MOSI, 457	object codes, 3
move 16-bit immediate value to bottom	OCM, 317
halfword, 91	OCREF, 317
move 16-bit immediate value to top, 91	octal, 27
move from general register to special register, 88	one's complement, 32
move from special register to general register, 88	opcode, 60, 277, 278, 279, 283, 284, 285
move not, 88	open collector, 439
move the bitwise inverse of 8-bit immediate	open drain, 295, 296, 297, 302, 303, 307, 310, 319,
value, 91	439
MSC, 479	operands, 2, 11, 22, 32, 37, 43, 44, 45, 47, 53, 56, 58,
MSP, 250	59, 60, 61, 62, 76, 77, 79, 87, 93, 189, 223, 227,
multiply and subtract, 76	245, 276, 277, 278, 279, 283
multiply-accumulate, 64	output compare mode, 317
multiply-subtract, 64	over-capture flag, 332
musical	overflow, 37, 40
ADSR, 421	overflow on addition, 37
amplitude, 420	overflow on subtraction, 38
duration, 421	overflow clear, 102, 104
pitch, 420	overflow flag, 34, 71
timbre, 420	overflow set, 102
tone, 420	,
	P
N	1 1 204
NA CIC 440 444 445	packed, 204
NACK, 440, 444, 445	packed data structure, 203
NaN, 233	padding, 203
negative, 102	palindrome string, 142

parity, 137	Q
even parity, 137	ON N. 202
odd parity, 137	QNaN, 233
parity bit, 428	quantization error, 392
part per million, 466	Quiet NaN, 233
pass arguments, 157	_
pass by reference, 158, 159	R
pass by value, 158, 159, 164	ma dia fra ayan ay interference 200
PC-relative addressing, 18, 93, 98, 99, 290	radio frequency interference, 299 radix, 221
perfect number, 139	read-only section, 4
peripheral interrupt, 254, 255, 256, 257, 259	read-write section, 4
permutation, 185	real number, 25, 221, 224, 231, 247
PHDC, 479	real-time clock, 371
piconet, 436	recursive function, 180
plus, 102, 104	register allocation, 12
polling, 250, 252, 306, 311, 312	registers, 11
positive or zero, 102	general purpose registers, 11
post-index format, 95	live range, 128
pot, 399	register reuse, 128
potentiometer, 399	scratch registers, 155, 161
power dissipation, 387	special purpose registers, 11
PPM, 466 preempt priority, 257, 258	virtual registers, 213
	regular channel, 400
preemptive, 249 pre-index format, 95	resolution, 387
prescaler, 316	reverse bits, 84
PRIMASK, 14, 88, 260, 261, 491, 500	reverse byte order, 84
privileged state, 487, 489, 491	reverse subtract, 64, 76
PROC, 67	RFCOMM, 437
procedure, 151	RFI, 299
process stack, 14, 254, 487, 497, 498, 499, 500, 501	rise time, 446
processor exception, 254	ROR, 199
program counter, 14	rotate
program status register, 71	rotate right, 64, 74
program status registers, 88	rotate right with extend, 74
pseudo instruction, 18, 63, 92, 93, 99, 143	rotate right with extend, 64
PSR, 71, 72, 88, 253, 268, 273, 491, 495, 497, 499,	round to even, 239
500	round to the nearest, 238
pull down, 294, 295	round toward negative infinity, 238
pull up, 294	round toward plus infinity, 238
push button, 304	round toward zero, 238
push pull, 295, 296, 297, 299, 302, 303, 307, 310,	rounding up, 240
319, 335	Round-robin, 493
PVD output, 271	round-robin scheduler, 501
PWM, 321	routine, 151
control register, 324	RRX, 199
mode, 324	RS-232, 430
	RS-422, 430

RS-485, 430	SNaN, 233
RTC, 271, 365, 371, 372, 374, 375, 377, 378, 379,	SOF, 470
380	software trigger, 397
alarm time comparison, 377	software trigger interrupt register, 254, 255
RTC alarm, 377	SPACE, 68
RTC alarm event, 271	SPI, 457
RTC tamper and time stamp events, 271	SPI mode, 460
	SSAT, 64, 71, 76, 78, 79
S	stack, 8, 9, 13, 58, 65, 152, 153, 154, 155, 156, 157,
5	161, 166, 178, 179, 180, 182, 187, 188, 199, 212
S suffix, 72, 84, 282, 285	213, 253, 261, 273, 274, 414, 487, 488, 489, 490
sample-and-hold amplifier, 388, 390	491, 493, 495, 496, 497, 498, 499, 500, 501, 502
sampling rate, 387	ascending stack, 154
sampling time, 391	descending stack, 154
saturation, 78	empty stack, 154
saturation flag, 71	full stack, 154
scanning algorithm, 309	stack frame pointer, 496
SCB, 258, 260, 272	stack-based instruction set, 58
SCLK, 457	stacking, 253
SE0, 467	start-of-frame, 470
SE1, 467	static variable, 206
selection structure, 124	statically-allocated, 4
sequence structure, 124	step angle, 345
servo motor, 343	stepper motor, 343
set a bit, 82	full stepping, 347
shift	half stepping, 349
arithmetic shift right, 64, 74	micro-stepping, 351
logical shift left, 74	wave stepping, 347
logical shift right, 74	stepwise refinement, 125
shift and rotate, 74	STIR, 254, 255
SHP, 258	store
sign and magnitude, 31	store lower byte, 98
sign and zero extension, 86	store lower half-word, 98
sign extension, 200, 395	store multiple words, 98
Signaling NaN, 233	store register byte, 201
signed division, 46, 74	store register halfword, 201
signed greater or equal, 102	store word, 98
signed greater than, 102	strcat, 167, 185, 186, 218
signed integer, 29	string
signed less than, 102	itoa, 170
signed less than or equal, 102	remove a character, 174
signed long multiply-accumulate, 64	reverse a string, 184
signed saturate, 76, 78	string comparison, 49, 168
single precision, 230	string concatenation, 167
single-ended signal, 467	string permutation, 185
sinusoidal, 420	strlen, 214, 216
slave select line, 457	strong pull down, 295, 308
slew rate, 298	strong pull up, 295, 308

strrchr, 52	upcounting, 314
strstr, 52	toggle a bit, 83
structured programming, 123, 124, 125	token packet, 469
subroutine, 151	transmission bus, 4, 7
pass arguments through stack, 178	trap, 272
pass by reference, 158	truncation, 239
pass by value, 158	two's complement, 33
passing arguments, 157	•
subtract, 76	U
subtract with carry, 64, 76	C
successive-approximation, 387	UART, 427
suffix S, 107	UEV, 324
supervisor call, 272	ULN2803, 354
swap, 164, 184	ultrasonic distance sensor, 336
switch statement, 117	ultrasonic waves, 337
synchronous protocol, 458	unaligned memory accesses, 204
system control block, 258, 260	unaligned memory layout, 204
system exception, 254	unconditional branch instruction, 104
system handler priority, 258	uninitialized data segment, 8
system interrupt, 254	uninitialized variables, 4
system tick timer, 261	unipolar, 343
SysTick, 68, 210, 211, 261, 262, 263, 266, 268, 273,	unique numbers, 175
274, 498	Unix Epoch, 371, 380
	unprivileged state, 487, 489
T	unsigned decimal, 29
-	unsigned division, 46, 74, 197
table branch byte, 117	unsigned higher, 102
table branch half-word, 117	unsigned higher or same, 102
TBB, 117	unsigned long multiply-subtract, 64
TBH, 117	unsigned lower, 102
temporal locality, 12	unsigned lower or same, 102
test, 87	unsigned numbers, 28
test equivalence, 87	unsigned saturate, 76, 78
test equivalent, 65	unstacking, 253
Thumb, 53, 54, 70, 275, 276, 277, 278, 503, 505	update event, 324, 327, 340, 341, 397, 416
Thumb state, 54	update interrupt flag bit, 324
Thumb-2, 53, 70, 275, 276	USAT, 64, 71, 76, 78, 79
time division duplex, 436	USB, 465
timer, 313	address field, 469
center-aligned counting, 314	bulk transfers, 468
compare and capture register, 319	bus layer, 466
downcounting, 314	class layer, 479
input capture, 329	CRC, 469
output compare mode, 317	data field, 469
overflow, 314	descriptors, 471
prescaler, 316	device layer, 468
PWM, 324	differential signals, 466
underflow, 314	end of packet, 469

video, 479

virtual decimal place, 222 volatile, 4, 5, 7, 206, 210, 211

endpoint, 468, 474 volatile variable, 210 enumeration, 475 von Neumann, 4, 23 functions layer, 471 GET_DESCRIPTOR requests, 477 W HID, 480 wait for event, 65 interrupt transfers, 468 wait for interrupt, 65 isochronous transfers, 468 I state, 467 wave stepping, 346, 347 K state, 467 weak, 215, 217 weak pull down, 295 NRZI, 466 weak pull up, 295 packet identification field, 469 while loop, 113 power supply, 472 product ID, 471 word, 25 SET_ADDRESS request, 477 X speed, 466 start-of-frame, 470 xPSR, 14 SYNC, 468 transfer type, 468 Y vendor ID, 471 UTC, 371, 372 year 2038 problem, 371 \mathbf{V} Z variable live range, 13

zero flag, 71

zero-initialized data section, 4, 7