	STM32L1	STM32L4
I2C	I2C1, I2C2	I2C1, I2C2, I2C3
	Standard mode (up to 100 kHz), Fast mode (up to 400 kHz)	Standard mode (up to 100 kHz), Fast mode (up to 400 kHz), Fast mode Plus (up to 1 MHz)
	I2C clock is APB1 clock (PCLK1).	I2C clock is derived from one of the three following sources: system clock (SYSCLK), HSI16, APB1 (PCLK1).

	<pre>typedef struct {
	    __IO uint16_t CR1;
	    __IO uint16_t CR2;
	    __IO uint16_t OAR1;
	    __IO uint16_t OAR2;
	    __IO uint16_t DR;
	    __IO uint16_t SR1;
	    __IO uint16_t SR2;
	    __IO uint16_t CCR;
	    __IO uint16_t TRISE;
	} I2C_TypeDef;</pre>
		<pre>typedef struct {
		    __IO uint32_t CR1;
		    __IO uint32_t CR2;
		    __IO uint32_t OAR1;
		    __IO uint32_t OAR2;
		    __IO uint32_t TIMINGR;
		    __IO uint32_t TIMEOUTR;
		    __IO uint32_t ISR;
		    __IO uint32_t ICR;
		    __IO uint32_t PECR;
		    __IO uint32_t RXDR;
		    __IO uint32_t TXDR;
		} I2C_TypeDef;</pre>

	I2C->DR	I2C->RXDR or I2C->TXDR