ECE 271 Microcomputer Architecture and Applications Lab 8: Timer Input Capture in Assembly Instructor: Prof. Yifeng Zhu Spring 2015

Goals

- 1. Understand the basic concept of input capture function of a timer
- 2. Handle different events in the interrupt service routine
- 3. Handle timer counter overflow and underflow
- 4. Use a timer to measure the timestamp of a signal edge (rising or falling edge) external to the microprocessor

Pre-Lab Assignment

- 1. Read Chapter 15 of the Textbook.
- 2. Complete the pre-assignment.

In-Lab Requirements

- 1. Measure the period of a 1Hz square signal by using TIM4 Input Capture and calculate the accuracy
- 2. Measure distance using the ultrasonic sensor and calculate the accuracy
- 3. Something cool such as printing distance to the LCD display

Post-Lab Assignment

- 1. Complete the post-lab report
- 2. Write your report in Readme.md and submit it

This lab repeats Lab 7. However, you are required to complete the lab in assembly.

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Lab 8 Timer Input Capture Pre-Lab Assignment

Student Name:	

1. Configure RCC_AHBENR to enable the clock of GPIO Port B

Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	6	8		9	5	4	3	2	1	0
AHBENR	_	ΝΞ	Reserved		AESEN	Reserved	DMA2EN	DMA1EN				_	erve				FLITFEN		Reserved	CRCEN		Res			OPGEN	GPIOPFEN	JPHEN	OPEEN	JPDEN	JPCEN	OPBEN	OPAEN
Mask				-		ш.																										
Value																																

Write your assembly code below to enable the GPIO B clock:

2. Configure RCC_APB1ENR to enable the clock of Timer 2 and Timer 4

Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	6	8	7	9	2	4	3	2	1	0
APB1ENR	COMPEN	Reserved	DACEN	PWREN	R	ese	erve	ed	USBEN	I2C2EN	I2C1EN	USART5EN	USART4EN	USART3EN	USART2EN	Reserved	SPI3EN	SPIZEN		Reserved	WWDGEN	Reserved	LCDEN	Re	serv	ved	TIM7EN	TIMGEN	TIMSEN	TIM4EN	TIM3EN	TIM2EN
Mask																																
Value																																

Write your assembly code below to enable the clock of Timer 2 and 4:

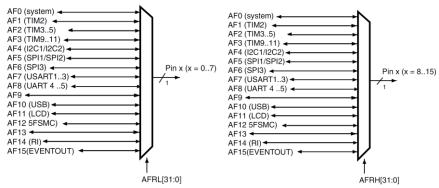
3. Configure PB 6 as Alternative Function Mode

GPIO Mode: Input (00, reset), Output (01), AlterFunc (10), Analog (11)

Register	31	29	27 26	25 24	23	21 20	19		15 14	13	11	8	7	5	3	1
GPIOB MODER	MODER15[1:0]	MODER14[1:0]	MODER13[1:0]	MODER12[1:0]	MODER11[1:0]	MODER10[1:0]	MODER9[1:0]	MODER8[1:0]	MODER7[1:0]	MODER6[1:0]	MODER5[1:0]	MODER4[1:0]	MODER3[1:0]	MODER2[1:0]	MODER1[1:0]	MODER0[:0]
Mask																
Value																

GPIOB Mode Register MASK Value = 0x_______ (in HEX)
GPIOB Mode Register Value = 0x_______ (in HEX)

4. Configure and Select the Alternative Function for PB 6



GPIOx_AFRL[31:00] defines the alternate function for pins 0 to 7, and GPIO_AFHL for pins 8 to 15.

Register	31	30	53	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	9	6	œ	7	9	2	4	3	7	-	0
GPIOB AFR[0]	AF	RL	7[3:	:0]	AF	RL6	5[3:	0]	AF	RL!	5[3:	0]	AF	RL4	1[3:	0]	ΑI	FRL	3[3	:0]	AF	RL2	2[3:	0]	AF	'RL	1[3	:0]	AF	RL	0[3	:0]
MASK																																
VALUE							·																			·					·	

GPIOB Alternative Function Register [0] MASK = 0x_______ (in HEX)

GPIOB Alternative Function Register [0] = 0x (in HEX)

5. Complete the following table to configure the Input Capture for Channel 1 of Timer 4

	Г	П	Т	Т	Т	Т		1			П	- 1			I	_		-	lui	1	1		T .	T	T		1			
Register	31	30	29	28	27	07	22 42	23	22	27	20	19	18	16	15	4	13	12	1	10	စ	8	7	9	2	4	က	7	~	0
TIMx_CR1										R	esei	rve	d									KD :0]	ARPE		MS :0]	DIR	OPM	URS	SIGN	CEN
Value																														
TIMx_CR2											R	ese	erve	d									TI1S		MMS [2:0]		CCDS	Re	serv	ved
Value																														
TIMx_SMCR							Res	erv	⁄ed	[ETP	ECE	E S[ГР 1:0]	I	ETF	[3:0]	MSM	T	S[2:	0]	Reserved	SM	1S[2	2:0]
Value																														
TIMx_DIER		Reserved															COMDE	CC4DE	CC3DE	CC2DE	CC1DE	NDE	Reserved	븯	Reserved	CC4IE	CC3IE	CC2IE	CC1IE	OIE
Value																		ш.		Ш										
TIMx_SR		Reserved CC CO CC CC O CC CC O CC CC O CC CC CC															5	Keserved	土	Reserved	CC41F	CC3IF	CC2IF	CC11F	UIF					
Value																						מ	צ		2					
TIMx_EGR												Re	eserv	ved										1G	Reserved	CC4G	CC3G	CC2G	CC1G	ne
Value																			ı		ı		ı		2					
TIMx_CCMR1 Input Capture mode							Res	erv	æd	l					ŀ	C2F	[3:0)]	PS	C2 SC :0]	C(S [C2 1:0]	ı	C1F	[3:0)]	IC PS [1:	SC	C(S [C1 1:0]
Value																														
TIMx_CCMR2 Input Capture mode							Res	orv	ned.						I	C4F	[3:0)]	PS	24 SC :0]		C4 1:0]	ı	C3F	[3:0)]	PS	3 SC :0]		C3 1:0]
Value							NES	CI V	eu																					
TIMx_CCER						F	Res	erv	ve	d					CC4NP		CC4P	CC4E	CC3NP		CC3P	CC3E	CC2NP		CC2P	CC2E	CC1NP		CC1P	CC1E

Value																		0	Dr.v.	2	0	0	0	erv	0	0	0	erv	0	0	0	erv	0	0
TIMx_CNT	C	N-	Γ[3	2:1	[6]		IM5					erv	/ed	0	n t	he	<u>;</u>				'		•	•	(NT	[15:0)]	•	•		•	•	•
Value											,																							
TIMx_PSC					•		D -						•				•							•	I	PSC	15:0)]	•					•
Value							Re	se	rve	ea																								
TIMx_ARR	А	RF	₹[3	2:1	16]		IM5					er۷	/ed	0	n t	the)								Α	RR	[15:0)]						
Value																																		
TIMx_CCR1	С	CF	1[3	32:	16]		TIM othe					er	ve	d c	on	th	е								С	CR1	[15:	0]						
Value		CCR4[32:16] (TIM5 only, reserved on the other timers)																																
TIMx_CCR2	С																е								С	CR2	[15:	0]						
Value		other timers) CCR4[32:16] (TIM5 only, reserved on the																																
TIMx_CCR3	С	CCR4[32:16] (TIM5 only, reserved on the other timers)														е								С	CR3	[15:	0]							
Value																																		
TIMx_CCR4	С	CF	84[3	32:	16]		ΓIM othe					er	ve	d d	on	th	е								С	CR4	[15:	0]						
Value																																		
TIMx_DCR									Re	se	rv	ec	d										D	BL[4	:0]			Reser	ved		DE	8A[4	:0]	
Value																																		
TIMx_DMAR							Re	se	rve	ed															Dl	MAI	3[15	:0]						
Value																			L															
TIM2_OR																		R	es	er	ved													ITR1_RMP
Value																																		
TIM3_OR																		R	es	erv	ved													ITR2_RMP
Value																																		

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Lab 8 Timer Input Capture In-Lab Demo

Part 1: Measure the period of 1 Hz square signal

- Basic requirement: In the debug environment, show the period you have measured. Record the accuracy for the post-lab assignment.
- Something cool: for example, show time measurements on LCD.

Part 2: Interface with Ultrasonic distance sensor

- Basic requirement: In the debug environment, show the distance you have measured. . Record the accuracy for the post-lab assignment.
- Something cool: for example, show distance measurements on LCD.

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Lab 8 Timer Input Capture Post-Lab Assignment

Write your answers to Readme.md and submit it to Gitlab server:

- 1. Does the timer counter count up or down in your lab? If counting up, how did you handle the counter overflow? (If counting down, how did you handle the counter underflow?)
- 2. What is the accuracy when measuring the period of 1Hz square wave?
- 3. What is the accuracy of the distance you have measured?
- 4. What is the most challenge issue you had in this lab?
- 5. Do you have any suggestions or comments of this lab?
- 6. Do you have any comments of the textbook?