

YIJU LI

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Dear Hiring Manager,

I am writing to express my interest in the FPGA Design Intern position at Sensoride. As a student at the University of California, San Diego, majoring in Computer Engineering and Mathematics, I am confident that my skills and experience make me a strong candidate for this role.

I have hands-on experience in system design and verification using modelsim and proficiency in hardware description languages, including VHDL and systemverilog. Additionally, my experience with Altera's Quartus design suite has allowed me to develop a strong understanding of the FPGA design flow, from RTL design to bitstream generation.

I am eager to apply my skills and knowledge to real-world projects and contribute to the growth and success of Sensoride. I am particularly drawn to Sensoride's cutting-edge technology and innovative approach to solving complex problems. I believe that this internship would provide me with an unparalleled opportunity to expand my technical skills and gain invaluable industry experience.

Thank you for considering my application. I look forward to the opportunity to discuss my qualifications further.

Sincerely,

Yiju Li