Lab Report

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Contents

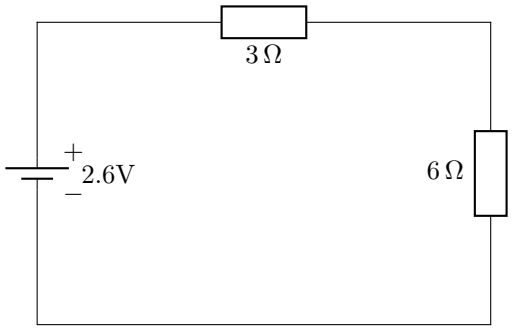
Chapter 1

Theoretical part

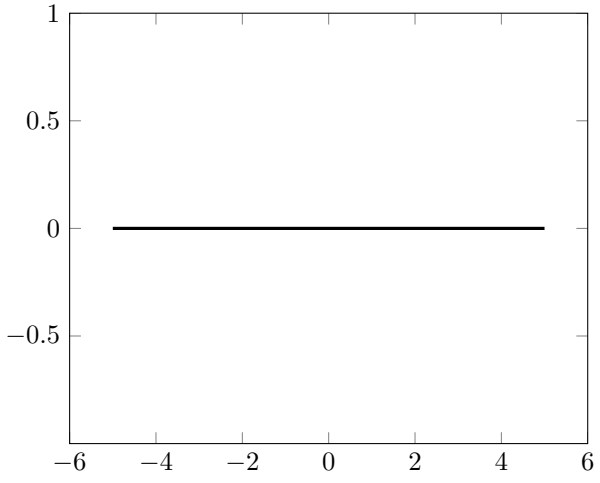
1.1 Circuit calculation

R1	3
R2	6
V1	2.6
UR1	0.86
UR2	1.73

1.2 Circuit diagram



1.3 Circuit plot



Chapter 2

Practical part

2.1 Work with GEDA programs

2.1.1 Work with gschem

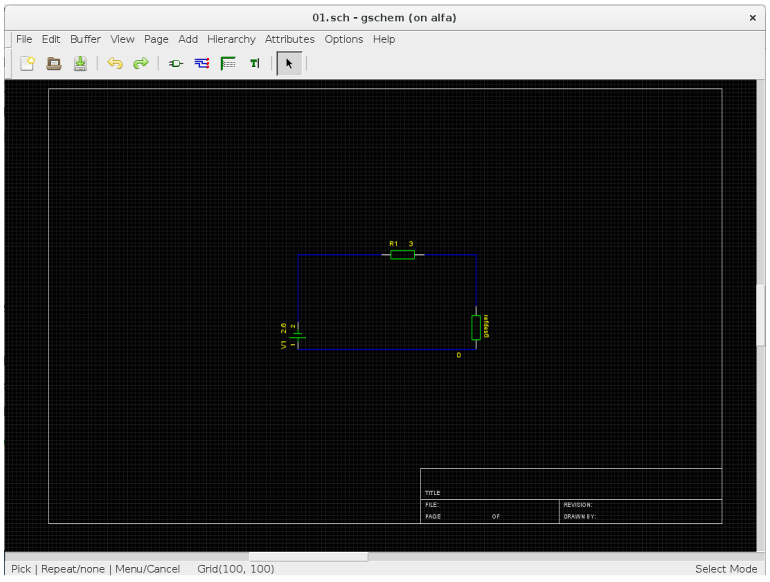


Figure 2.1: Circuit within the gEDA schematics environment.

2.1.2 Work with gnetlist

```
* Spice netlister for gnetlist
R2 2 0 6
R1 1 2 3
V1 0 1 2 . 6
.END
```

2.1.3 Work with ngspice

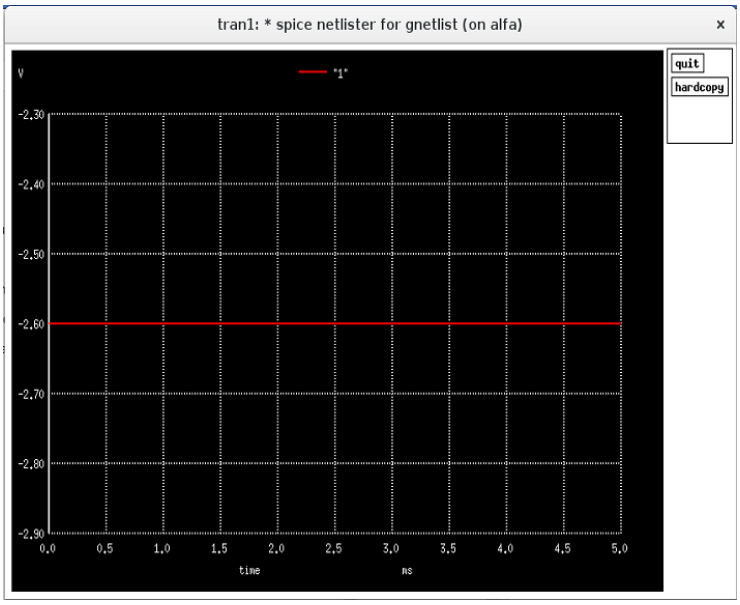


Figure 2.2: Simulation of voltage on resistor R1.

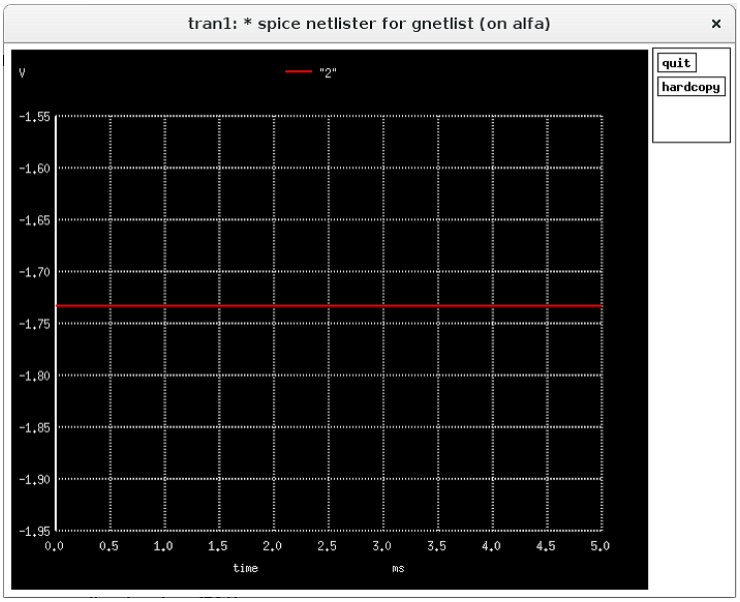


Figure 2.3: Simulation of voltage on resistor R2.

2.2 Work with QUCS programs

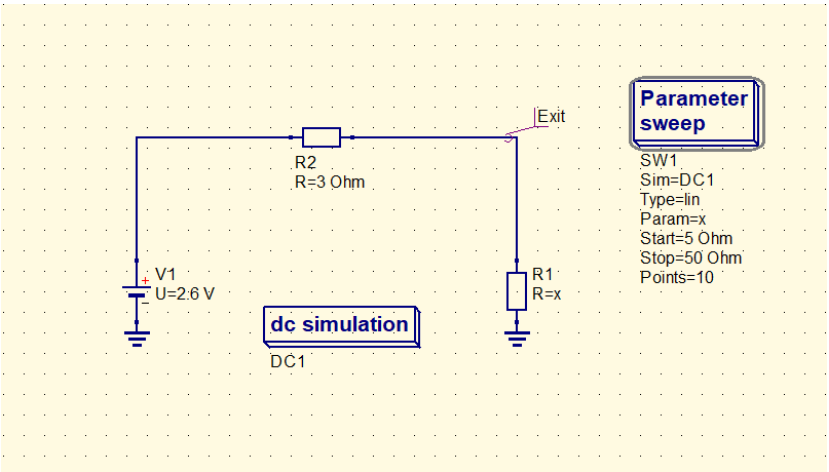


Figure 2.4: Circuit within the QUCS schematics environment.

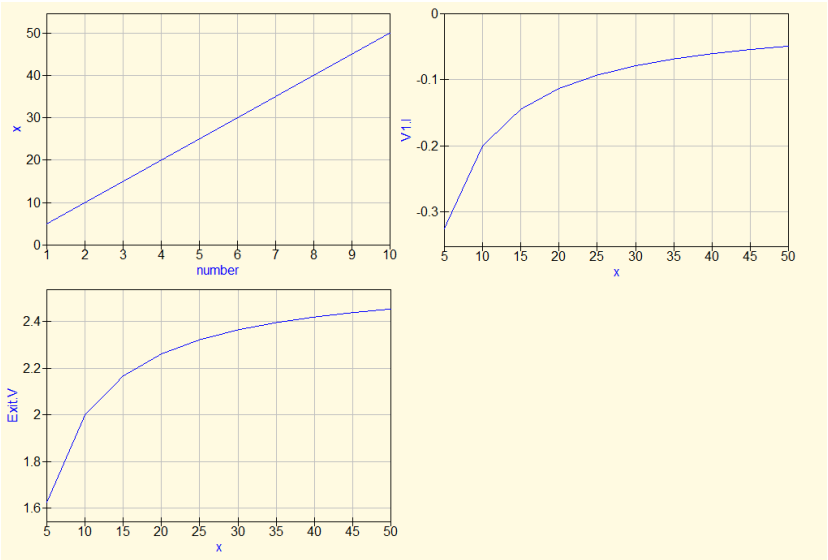


Figure 2.5: Plot with Cartesian coordinates, tabular view of currents flowing from points V1.1 and x.