



Benchmarking MPI for Deep Learning and HPC Workloads

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Benchmarking in the Data Center: Expanding to the Cloud
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Outline



Introduction

Open MPI + UCX

Benchmarking for MPI-Based Deep Learning

RMA and Collective Communication Design

Micro-Benchmarks

Application Results

Benchmarking for MPI-Partitioned Communication

MPI Partitioned Point-to-Point Communication

Overhead

Perceived Bandwidth

Sweep3D Communication Pattern

Conclusion And Future Work

Introduction



- ▶ HPC is used to solve large complex problems in many domains
 - ▶ Communication is one of the main bottlenecks in applications
 - ▶ There has been a recent popularity of systems with accelerators

Introduction

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 - ▶ Communication is one of the main bottlenecks in applications
 - ▶ There has been a recent popularity of systems with accelerators
- ▶ The Message Passing Interface (MPI)
 - ▶ Popular parallel programming model in HPC
 - ▶ Provides multiple communication APIs
 - ▶ Point-to-point
 - ▶ Partitioned point-to-point
 - ▶ RMA
 - ▶ Collective Communication (`MPI_Allreduce`, `MPI_Bcast`, etc.)

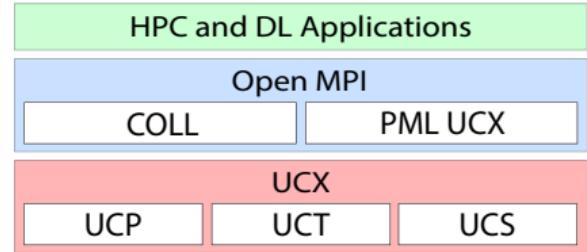
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 - ▶ Communication is one of the main bottlenecks in applications
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 - ▶ RMA
 - ▶ Collective Communication (`MPI_Allreduce`, `MPI_Bcast`, etc.)
- ▶ MPI based Deep Learning on HPC systems
 - ▶ As the complexity of DL models grow we move towards using the aggregate power of HPC systems

Open MPI + UCX

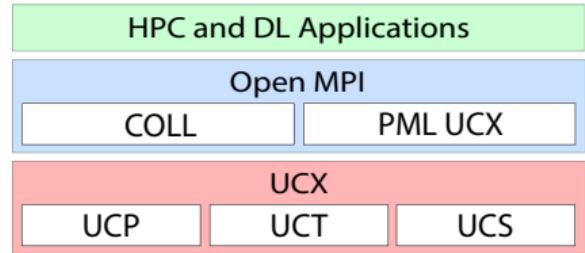


- ▶ UCX provides abstract communication primitives to best utilise hardware
 - ▶ Point-to-point implemented upon RMA
Put/Get operations



Open MPI + UCX

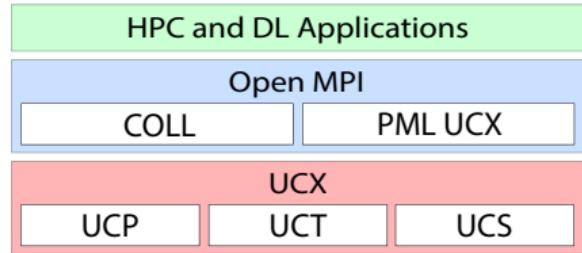
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- ▶ Open MPI is an open source MPI implementation
 - ▶ Point-to-point communication directly relies on UCX for data transfers
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Research Goals

- ▶ Improve the performance of GPU MPI communication for Deep Learning
- ▶ Obtain a better understanding of the MPI Partitioned Interface

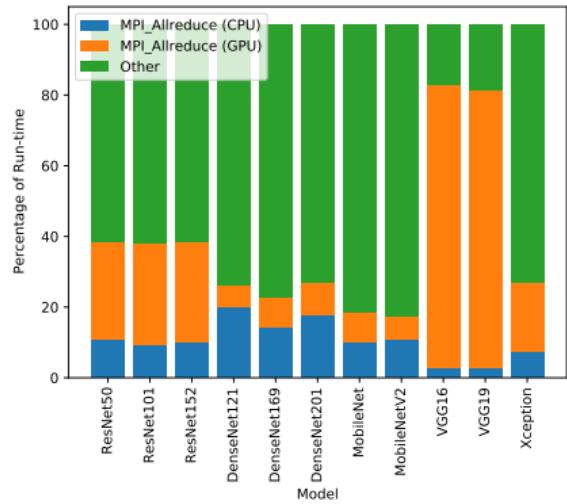
Benchmarking for MPI-Based Deep Learning

MPI-based Deep Learning



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- ▶ Distributed Deep Learning using Horovod is possible with models from:



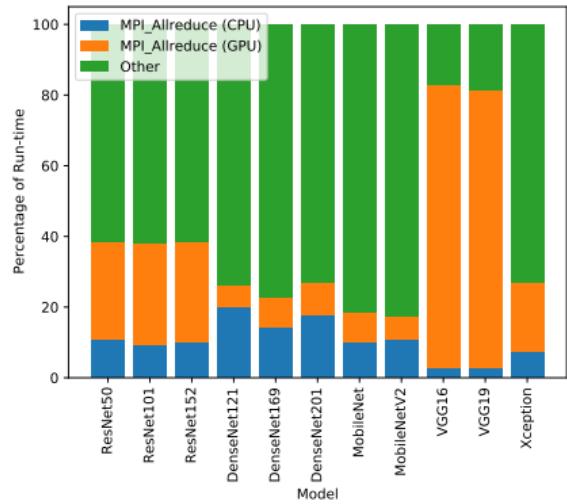
Impact of MPI_Allreduce on a single IBM AC922 node

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- ▶ Distributed Deep Learning using Horovod is possible with models from:
 - ▶ TensorFlow
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 - ▶ MXNet



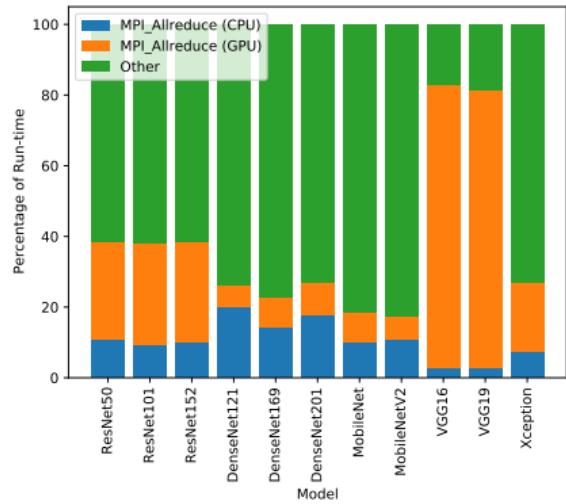
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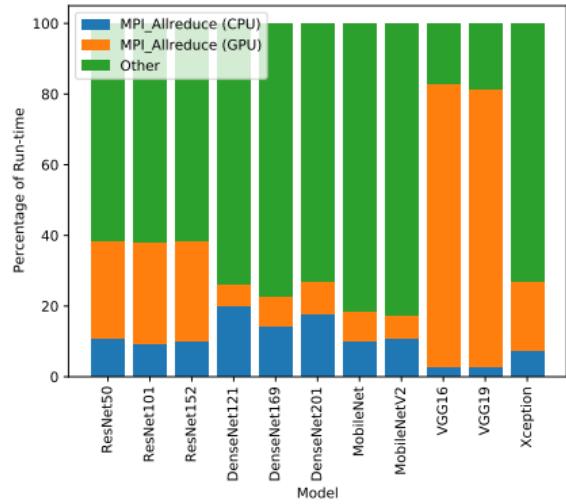
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 - ▶ 17-83% of training time was spent in `MPI_Allreduce`



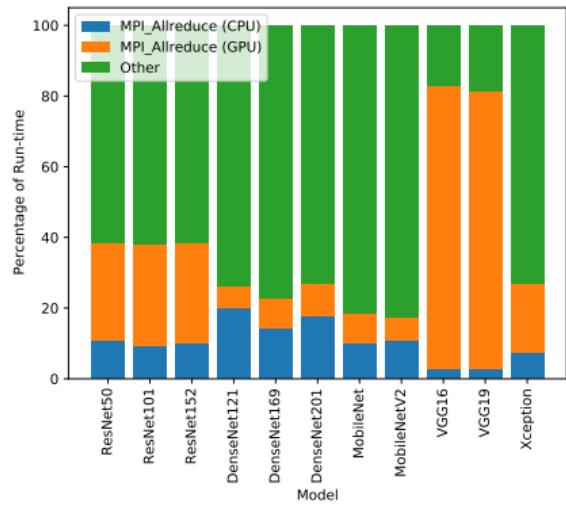
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 - ▶ 17-83% of training time was spent in MPI_Allreduce
 - ▶ Up to 80% of runtime was spent in a GPU based MPI_Allreduce



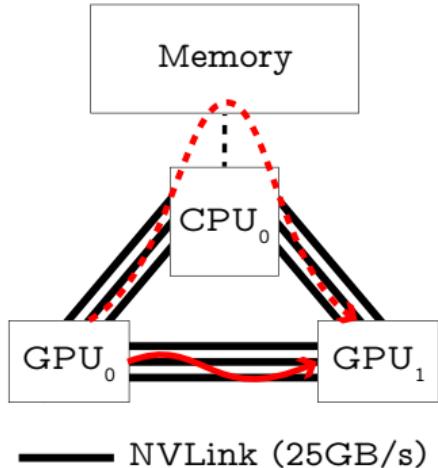
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Multi-Path Copy Motivation



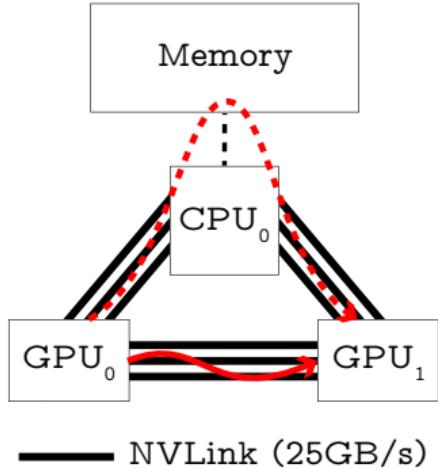
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- ▶ MPI sends data directly from GPU_0 to GPU_1
 - ▶ Uses a zero copy put operation in UCX
 - ▶ (As shown by the solid red line)



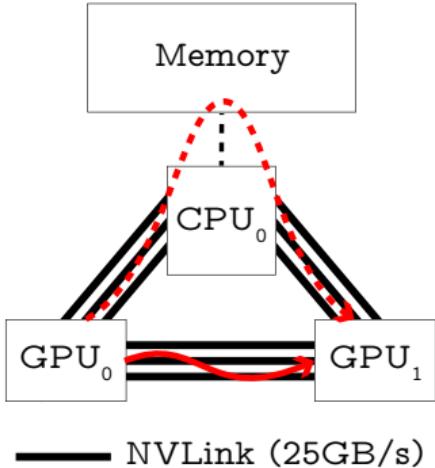
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- ▶ A large amount of unused potential bandwidth

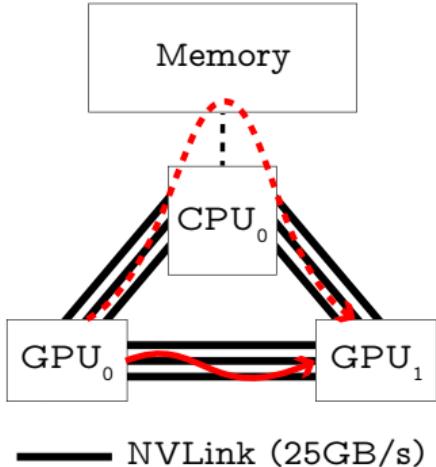


Multi-Path Copy Motivation



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Research Question

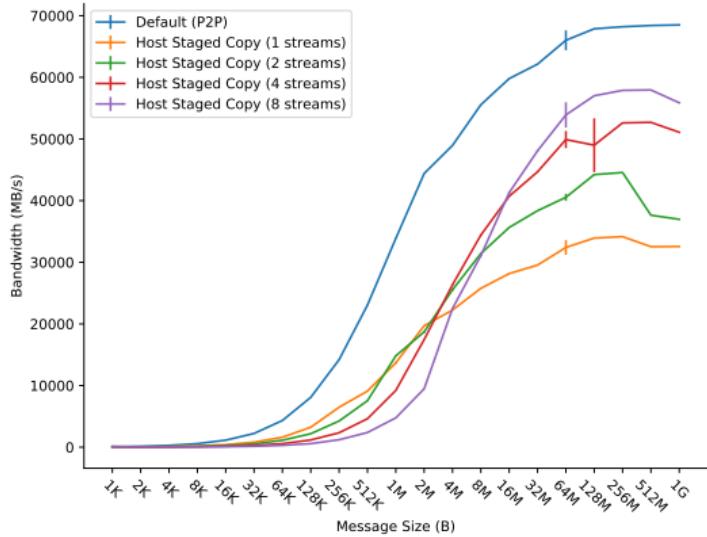
Can we design a mechanism to use all communication paths?

Multi-Path Copy Motivation



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- We used the `ucx_perftest` micro-benchmarks to assess the viability of our design idea

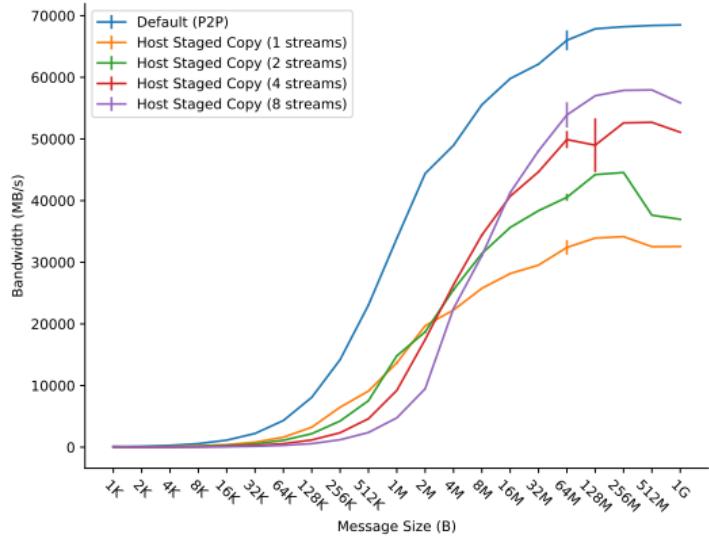


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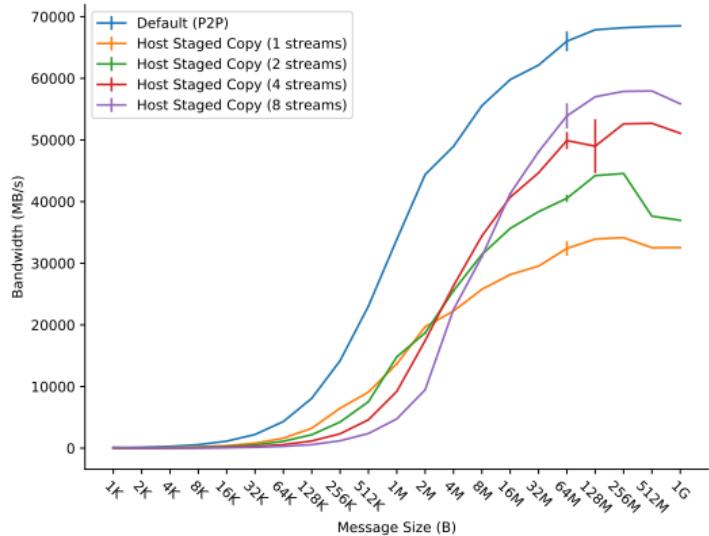


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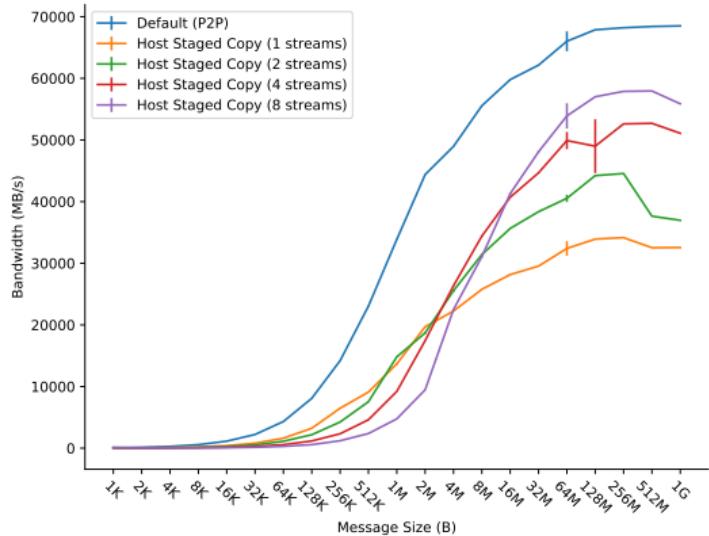


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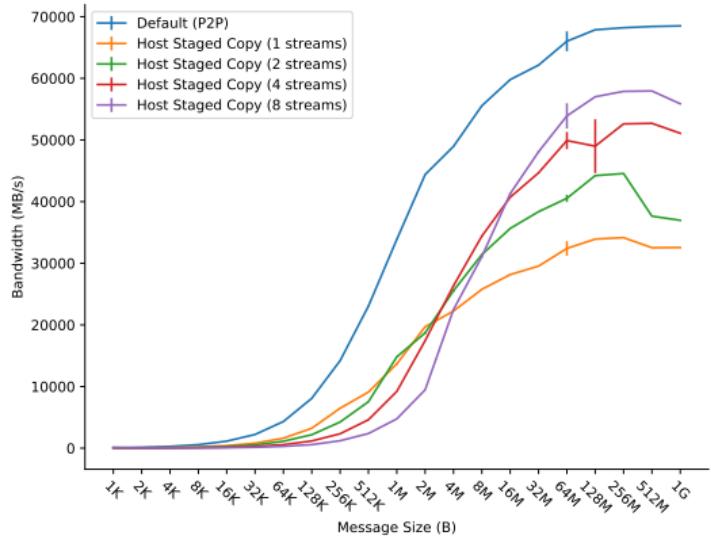


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 - ▶ Up to 53GB/s of unused bandwidth

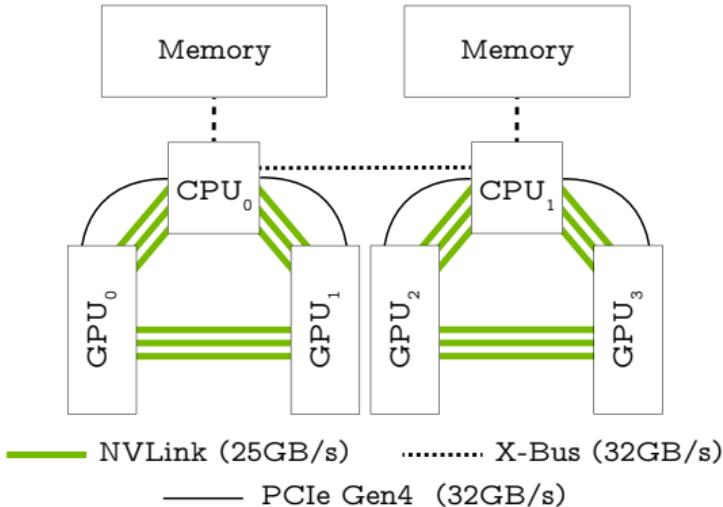


Hierarchical Allreduce with Multi-Path Copy



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- The proposed MPI_Allreduce algorithm has three steps:

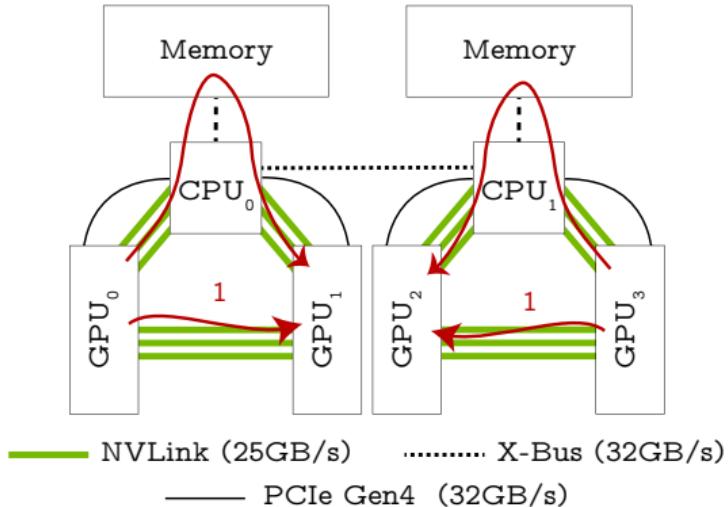


Hierarchical Allreduce with Multi-Path Copy



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- The proposed MPI_Allreduce algorithm has three steps:
 1. Intra-socket multi-path reduce

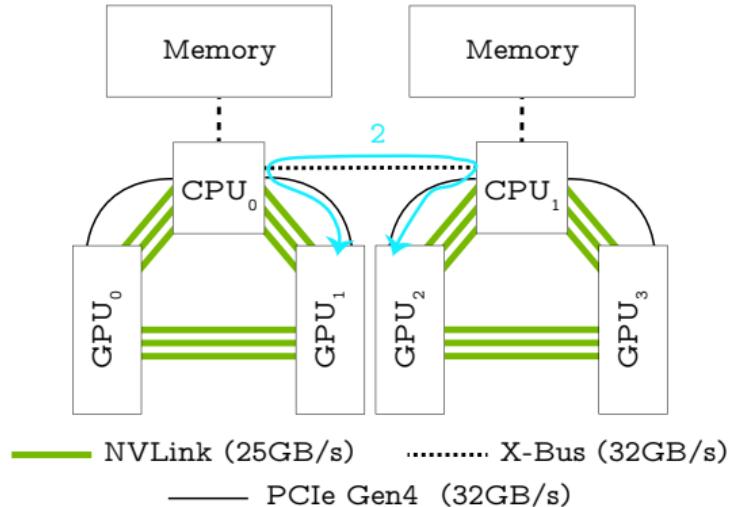


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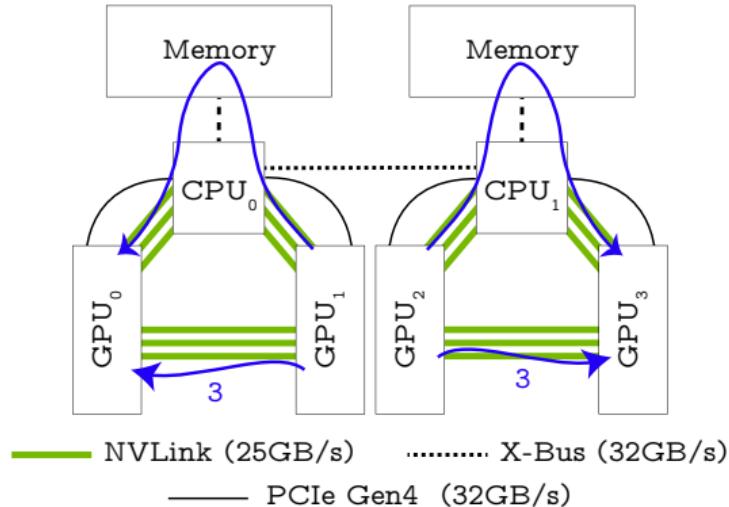


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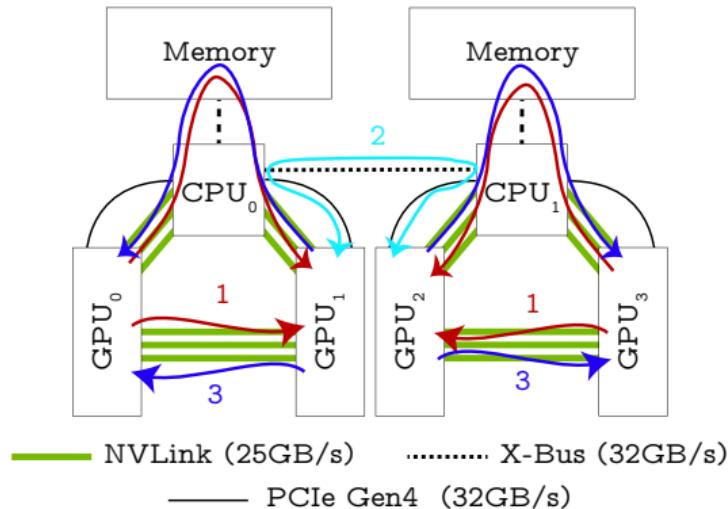


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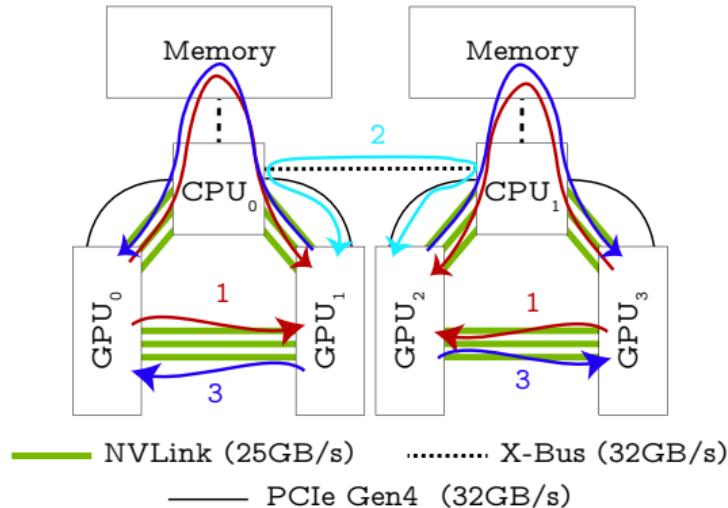


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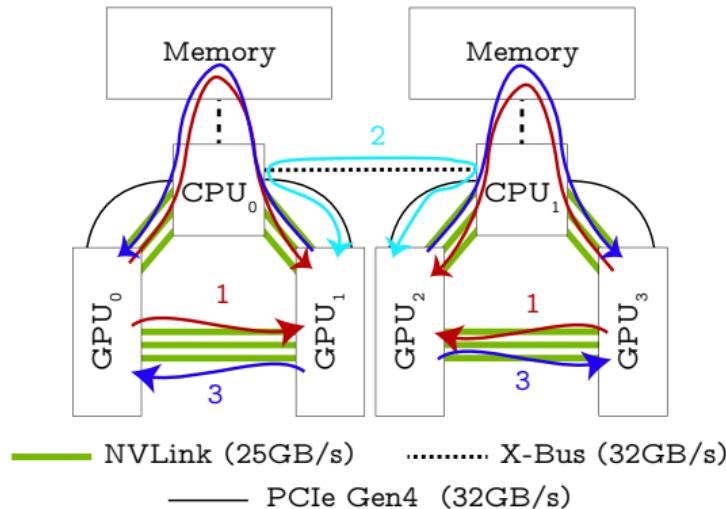


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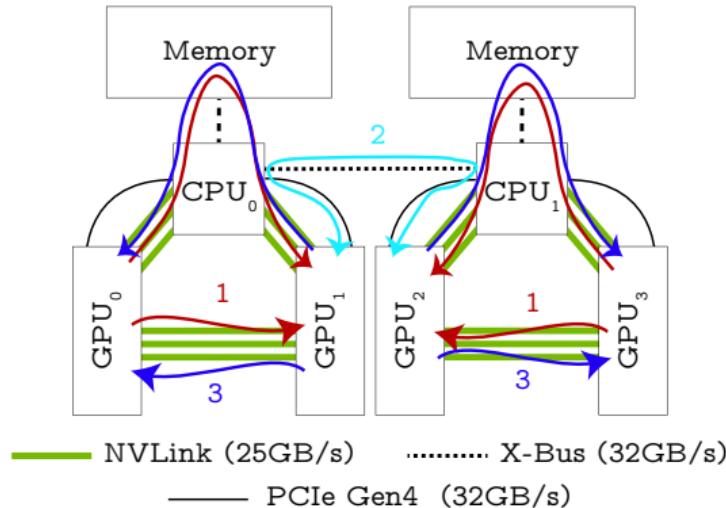


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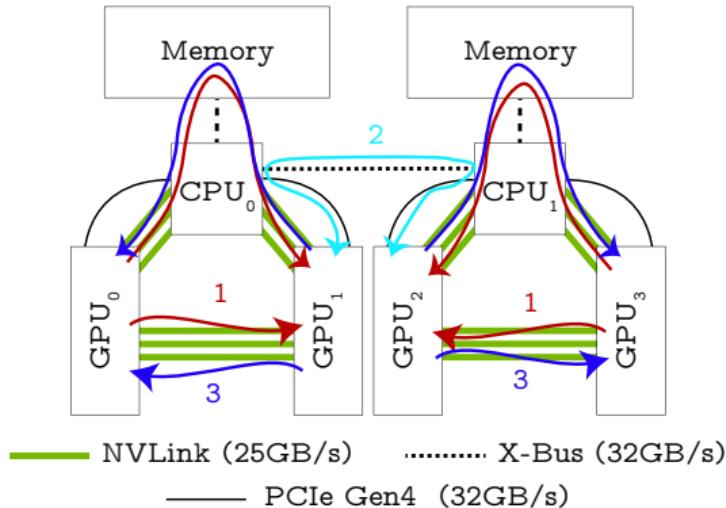


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 - ▶ Inter-socket communication dynamically switches between PCIe and NVLink
 - ▶ Dynamically send data using Multi-path or Peer-to-Peer copies via the host links
 - ▶ Minimise intra-socket congestion



Experimental Setup



► Hardware:

- ▶ IBM AC922
- ▶ 32 Core, 128 Thread Power9 CPU
- ▶ 256GB RAM
- ▶ Four V100-SMX2-32GB



Experimental Setup

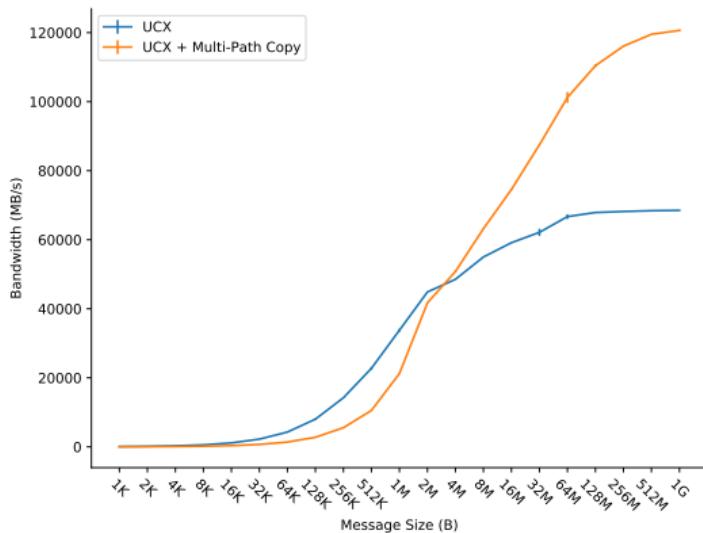
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 - ▶ UCX 1.8.0
 - ▶ Open MPI + HPC-X v2.7
 - ▶ Spectrum-MPI 10.3.1
 - ▶ MVAPICH2-GDR 2.3.5
 - ▶ NCCL 2.5.6
 - ▶ Horovod 0.20.3
 - ▶ TensorFlow 1.15.2



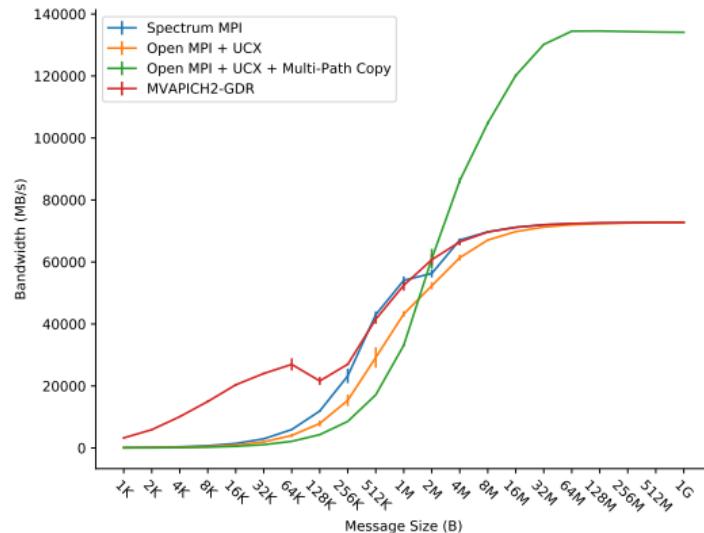
UCX Put and MPI Point-to-Point Results



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UCX Put Bandwidth

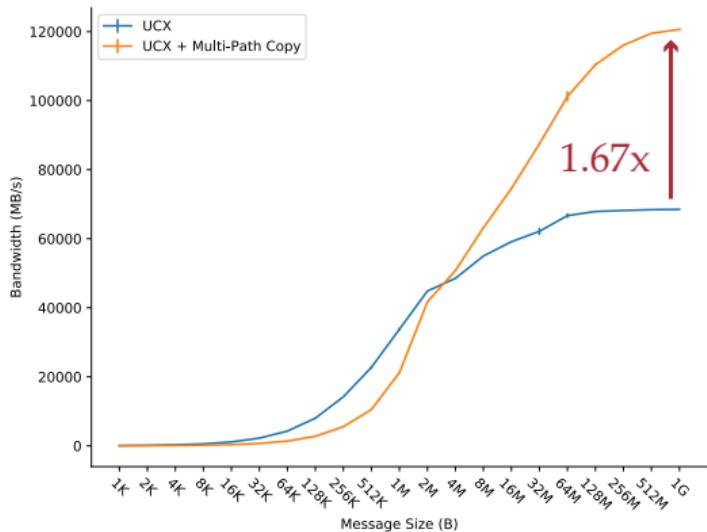


MPI Unidirectional Bandwidth

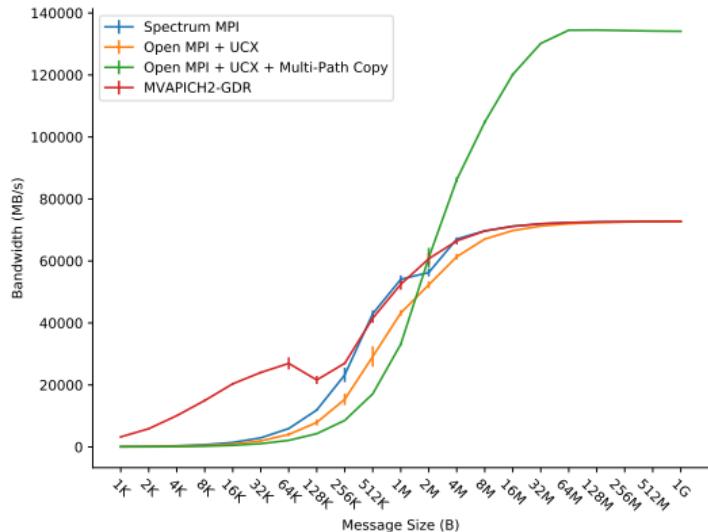
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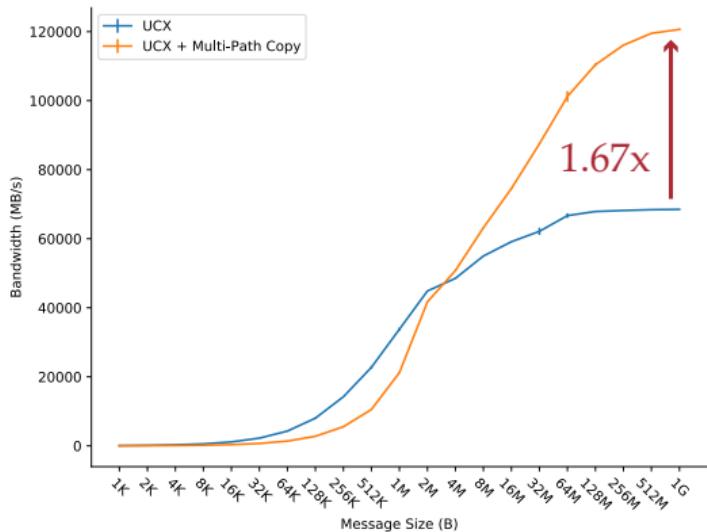


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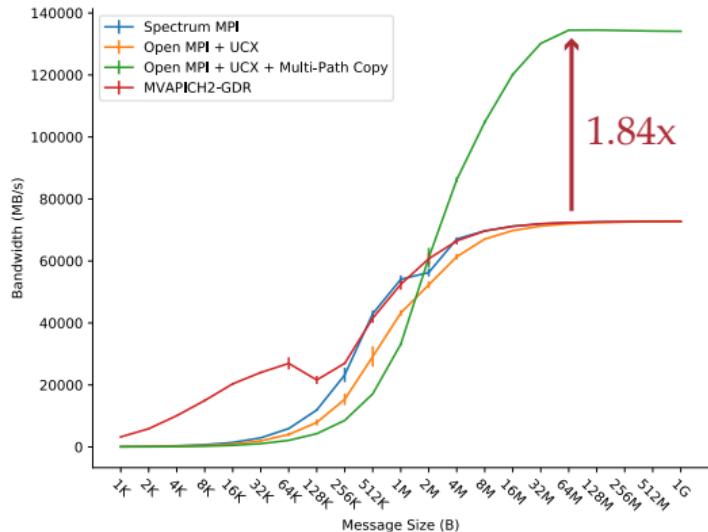
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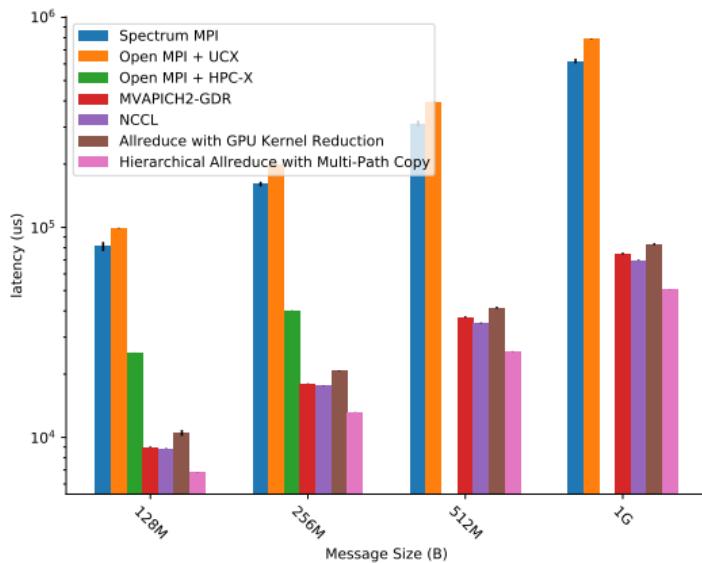


MPI Unidirectional Bandwidth

MPI_Allreduce OSU Microbenchmark Results



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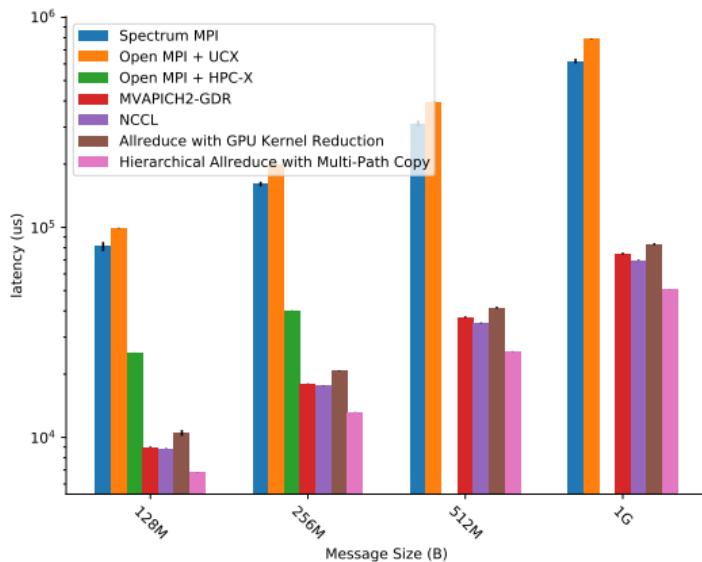


MPI_Allreduce latency on 4 GPUs for very large message sizes

MPI_Allreduce OSU Microbenchmark Results



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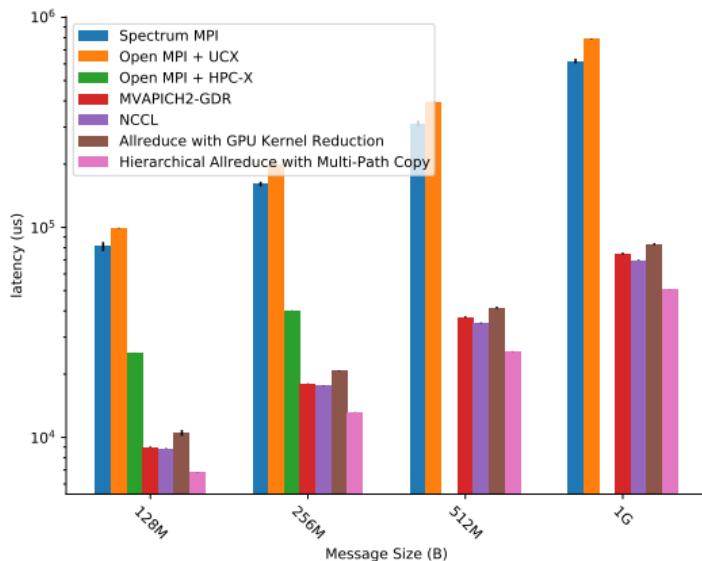
- ▶ Much lower latency than Open MPI + HPC-X

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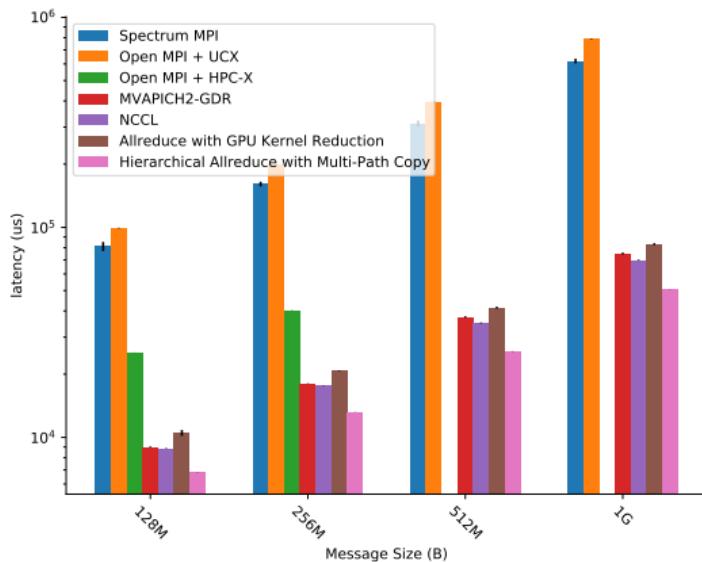
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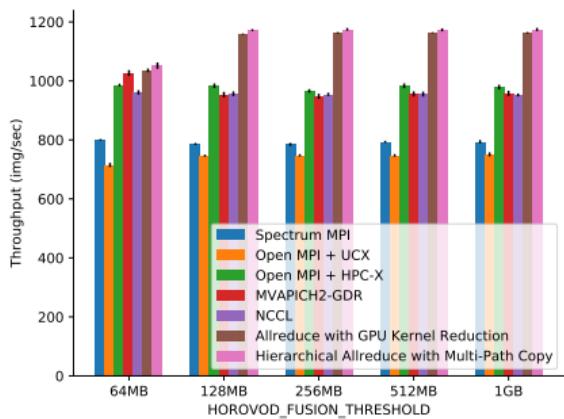
- ▶ Much lower latency than Open MPI + HPC-X
- ▶ At 1GB we see speedup of:
 - ▶ 1.47x over MVAPICH2-GDR
 - ▶ 1.38x over NCCL

Application Results

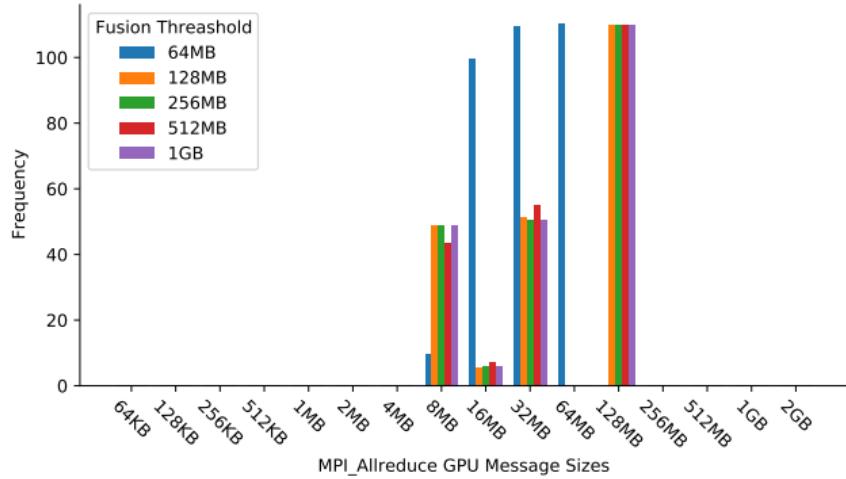
- ▶ ResNet50 up to 1.56x speedup



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Synthetic Horovod + TensorFlow benchmarks for ResNet50



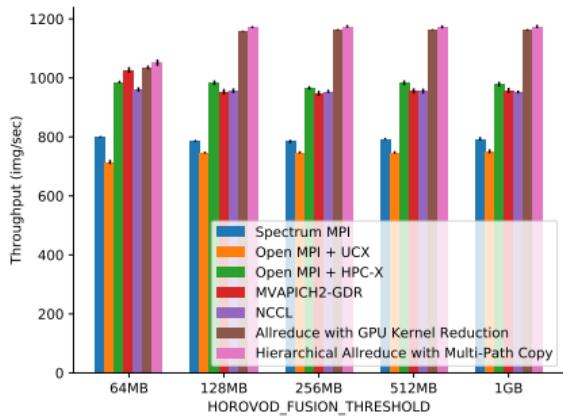
GPU Message Sizes for different
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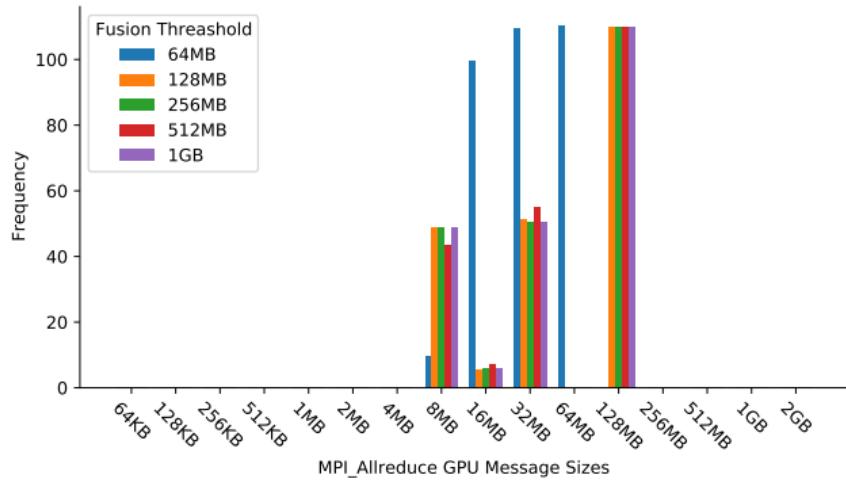


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- ▶ ResNet50 up to 1.56x speedup
- ▶ Modifying fusion threshold increases message sizes to 128MB



Synthetic Horovod + TensorFlow benchmarks for ResNet50



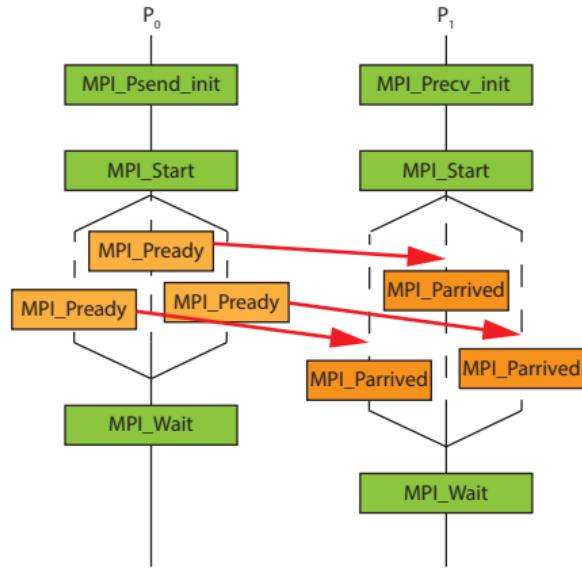
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Benchmarking for MPI-Partitioned Communication

MPI Partitioned Point-to-Point Communication

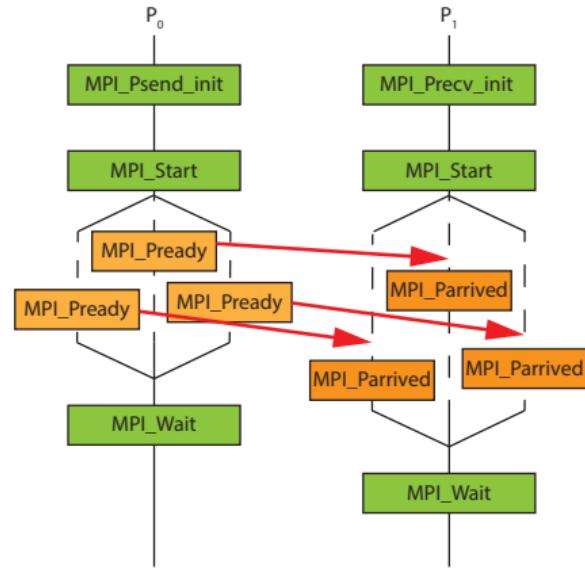


- ▶ MPI_Psend_init/MPI_Precv_init is used to initialize communication between processes



MPI Partitioned Point-to-Point Communication

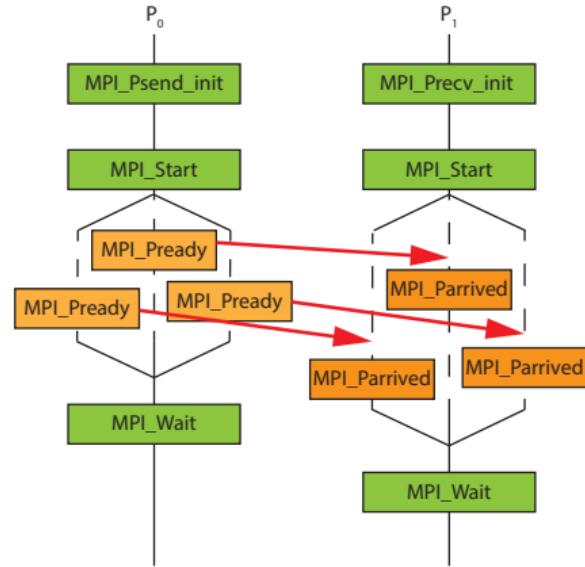
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 - ▶ MPI Partitioned does not accept wildcards



MPI Partitioned Point-to-Point Communication

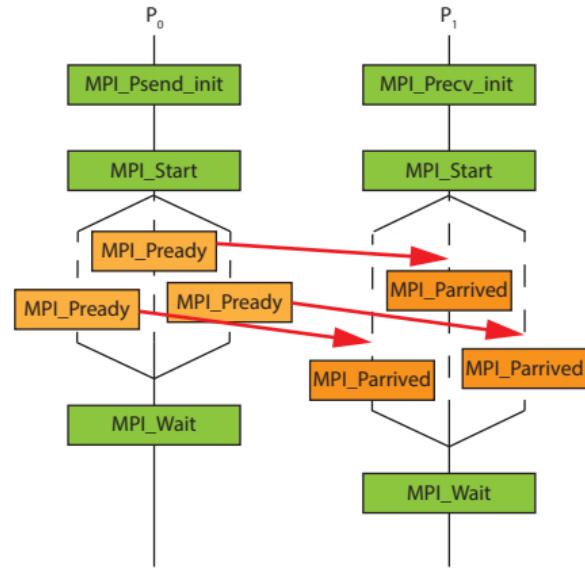


- ▶ MPI_Psend_init/MPI_Precv_init is used to initialize communication between processes
 - ▶ Message matching occurs here
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- ▶ MPI_Start is called to start communication



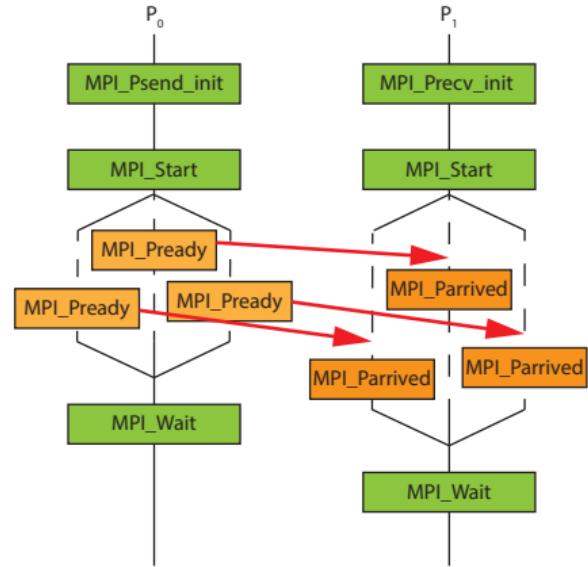
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- ▶ MPI_Start is called to start communication
- ▶ A parallel for loop is launched



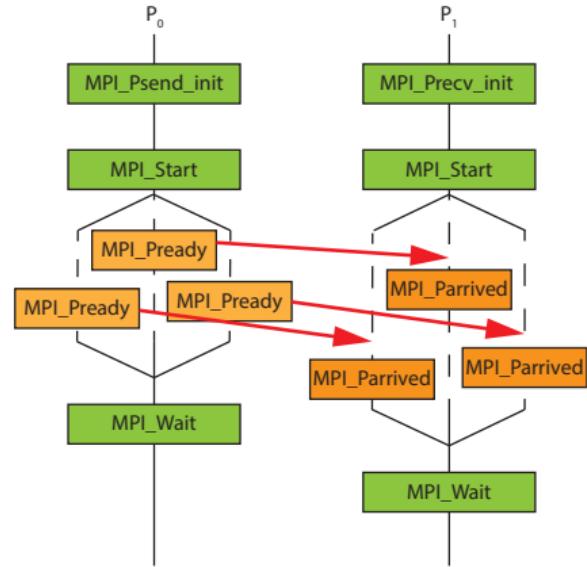
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 - ▶ Work is Computed
 - ▶ Once data is ready, MPI_Pready is called
 - ▶ Optionally, MPI_Parrived to check if incoming data has arrived



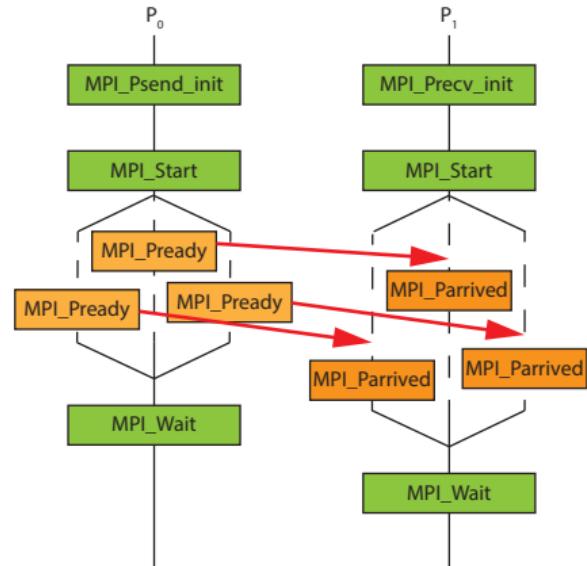
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- ▶ MPI_Waitall is called to complete communication



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- ▶ MPI_Waitall is called to complete communication
- ▶ A good implementation does not have the serialization issues of MPI Point-to-Point



Motivation



- ▶ Commonly used benchmarks do not support MPI Partitioned
 - ▶ Sandia Micro Benchmarks (SMB)
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- ▶ What are appropriate partition sizes for application developers to use?

Experiment Setup



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- ▶ Niagara Supercomputer at SciNet¹
 - ▶ 2x 20 Core Intel Skylake at 2.4GHz
 - ▶ EDR InfiniBand Network
 - ▶ GNU/Linux - CentOS 7.6
 - ▶ Open MPI (master branch)
 - ▶ UCX v1.11.0
 - ▶ MPIPCL

¹SciNet is funded by: the Canada Foundation for Innovation; the Government of Ontario; Ontario Research Fund - Research Excellence; and the University of Toronto. This research was enabled in part by support provided by the Digital Research Alliance of Canada

Overhead



- ▶ What is the cost of using MPI
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 - ▶ We measure each individual data transfer
 - ▶ Compare it to MPI Point-to-Point

$$Overhead = \frac{t_{part}}{t_{pt2pt}}$$

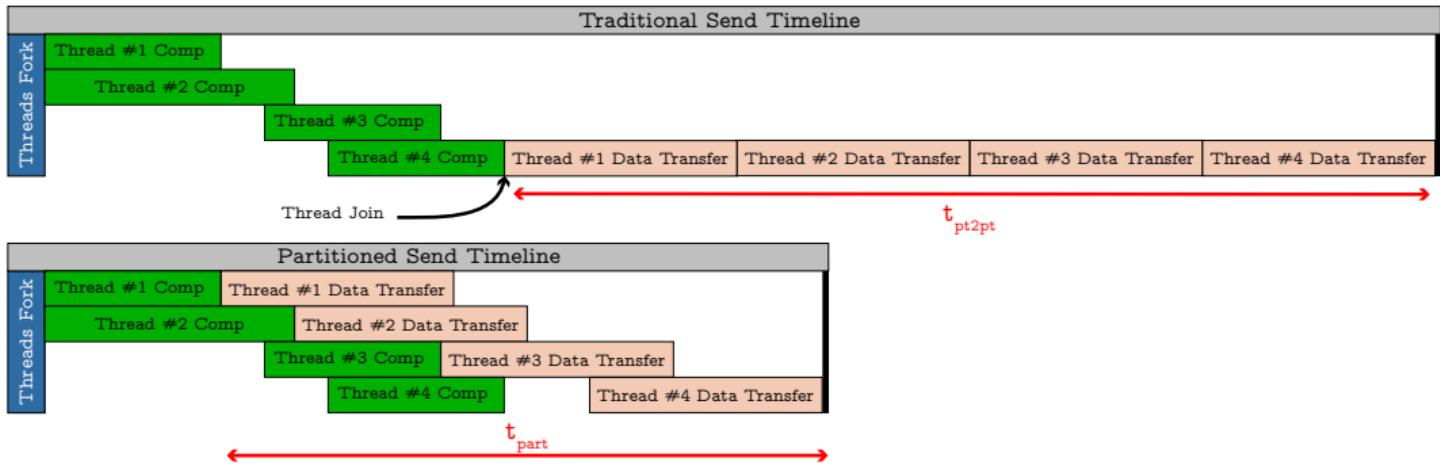
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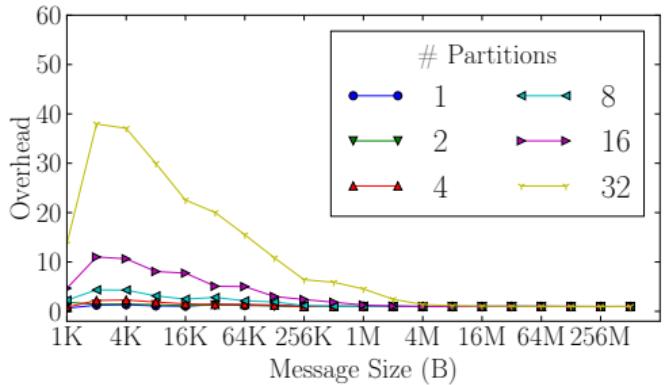
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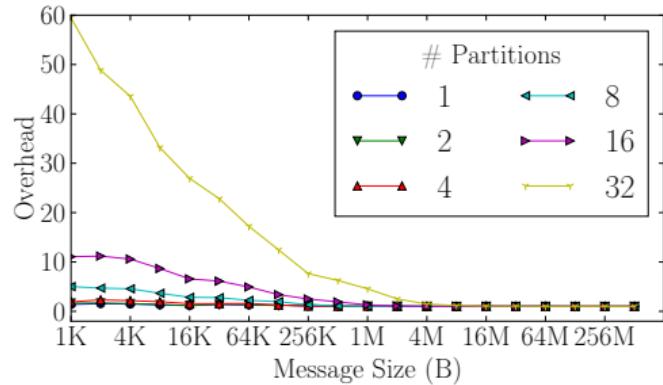
Overhead Results



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(a) Cold Cache

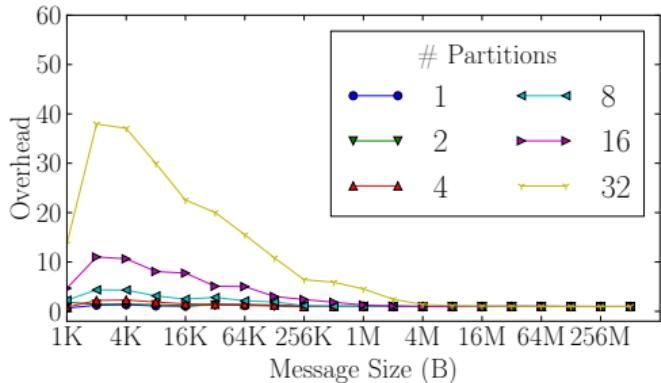


(b) Hot Cache

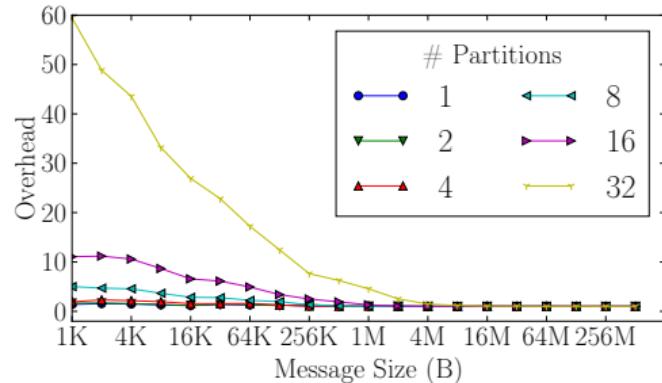
Overhead of Partitioned Point-to-Point Communication Relative to Point-to-Point Communication for 10ms of Compute

Overhead Results

- Partition count correlates with overhead



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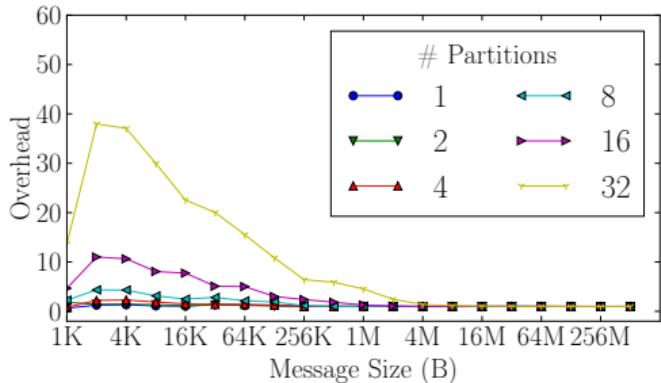
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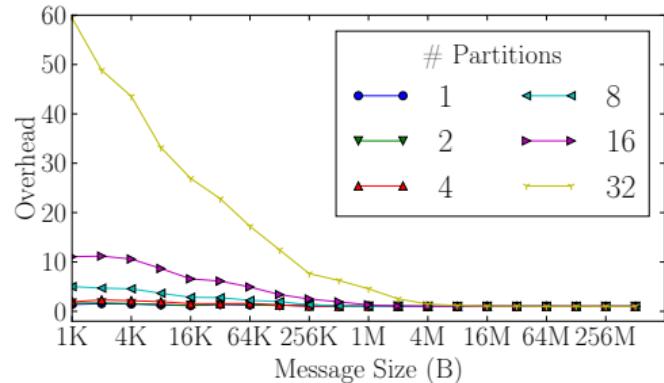


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- ▶ Partition count correlates with overhead
- ▶ Overheads mostly impact small messages



(a) Cold Cache



(b) Hot Cache

Overhead of Partitioned Point-to-Point Communication Relative to Point-to-Point Communication for 10ms of Compute

Perceived Bandwidth



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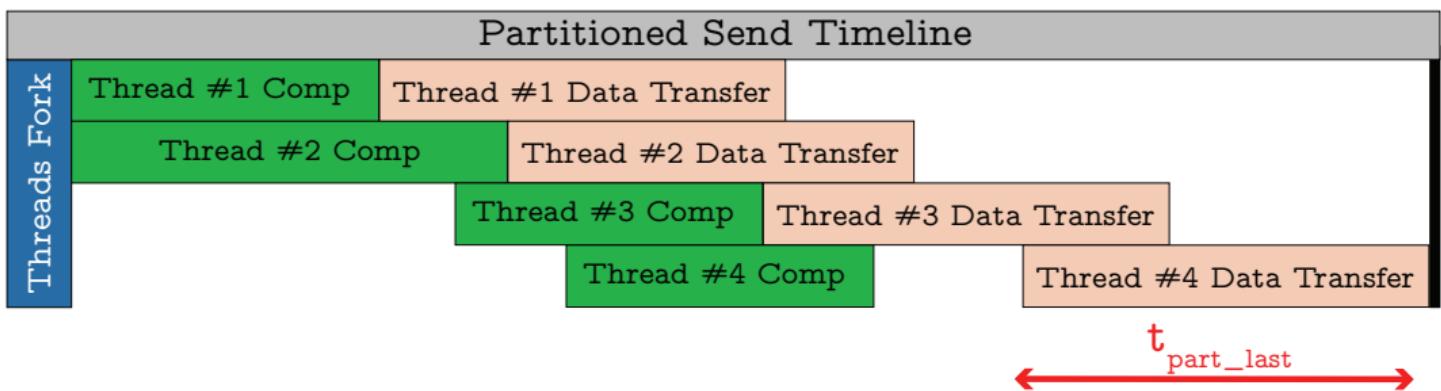
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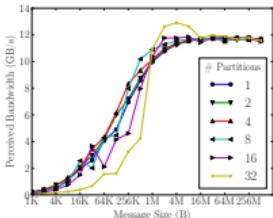
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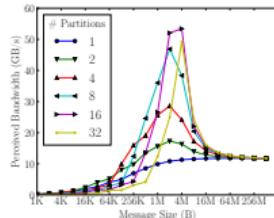
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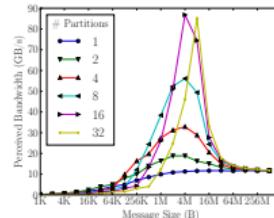
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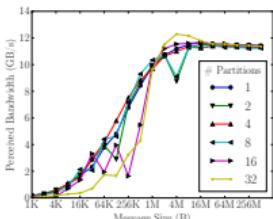
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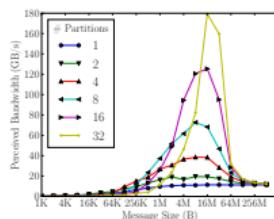
(b) 10ms Comp with 4% Noise



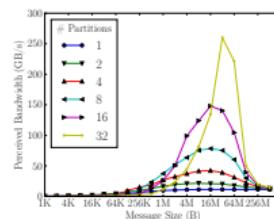
(c) 10ms Comp with 10% Noise



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(f) 100ms Comp with 10% Noise

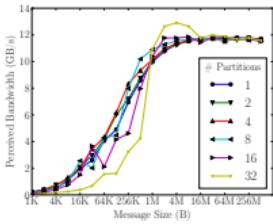
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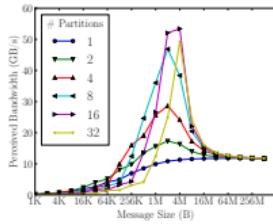


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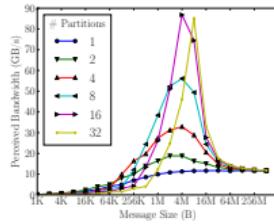
- With 0% noise, we see our traditional bandwidth curve



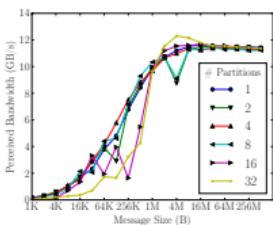
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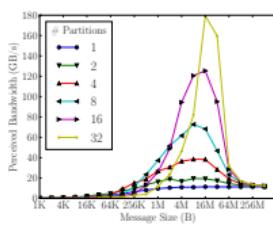
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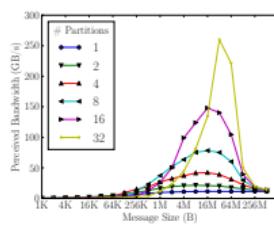
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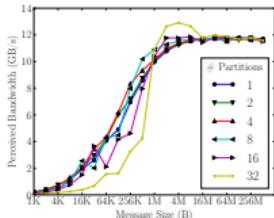
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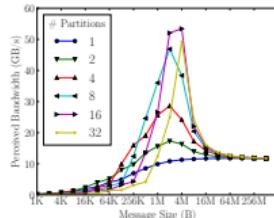


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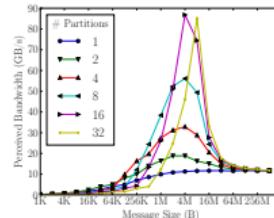
- With 0% noise, we see our traditional bandwidth curve
- Peak bandwidth is obtained for medium sized messages



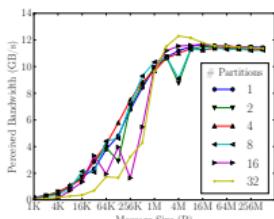
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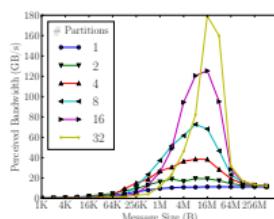
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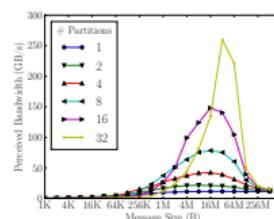
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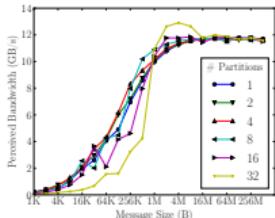
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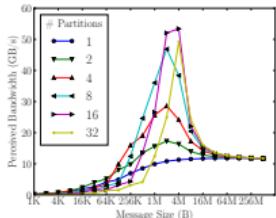


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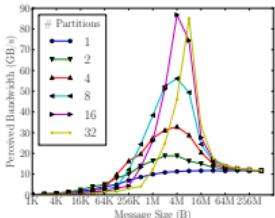
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- Peak bandwidth is obtained for medium sized messages
- Actual network bandwidth is saturated for large messages, thus perceived bandwidth drops



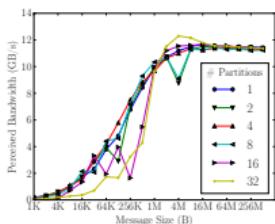
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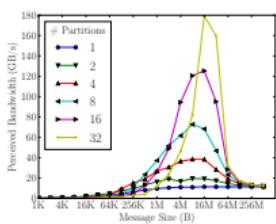
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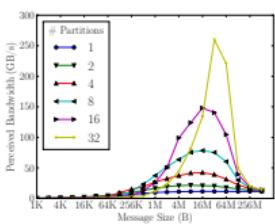
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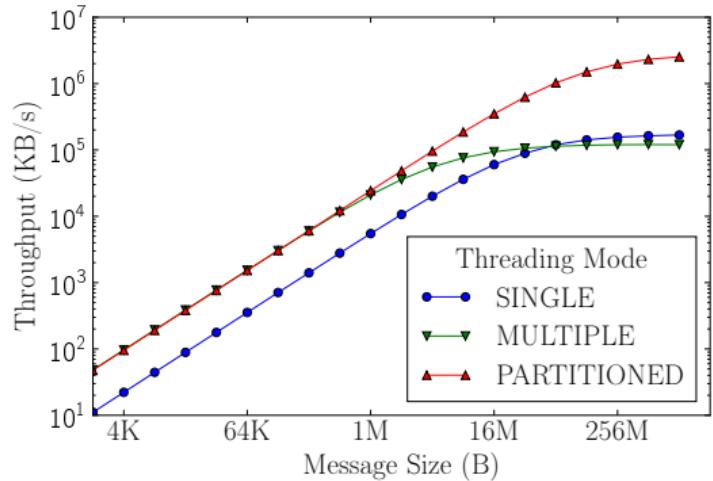
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Sweep3D Communication Pattern



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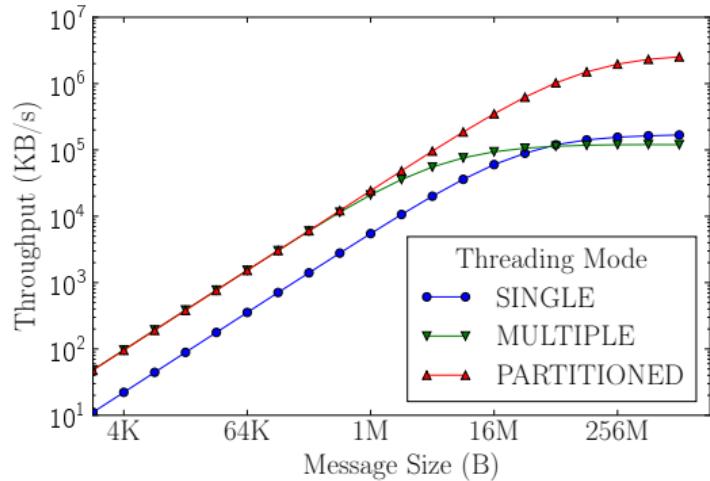
Sweep3D communication throughput for 16 partitions, 10ms compute, and 4% Single Noise with a Hot Cache

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- ▶ Sweep3D communication pattern has lots of dependencies



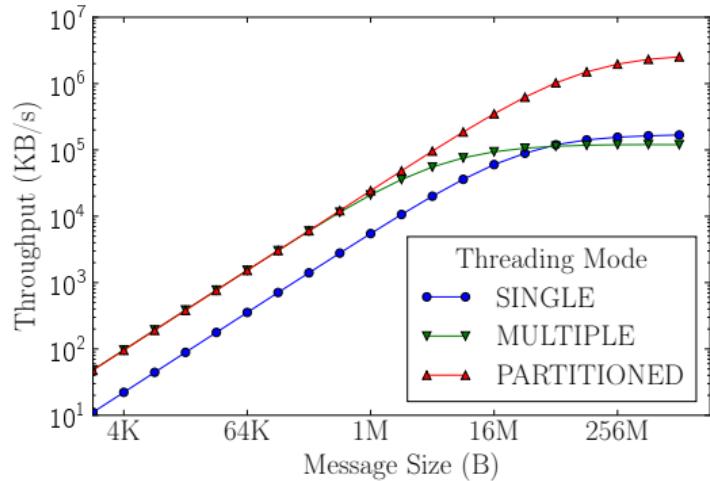
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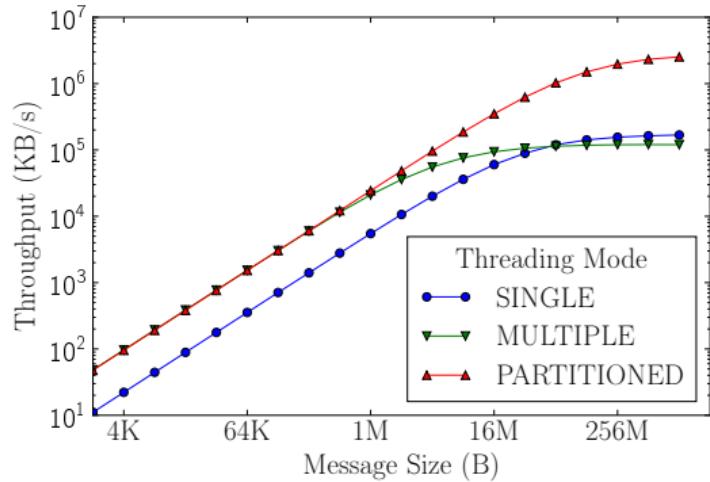
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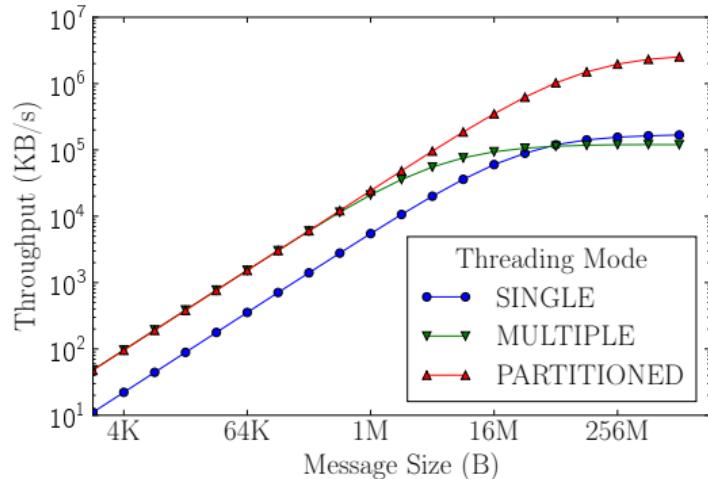
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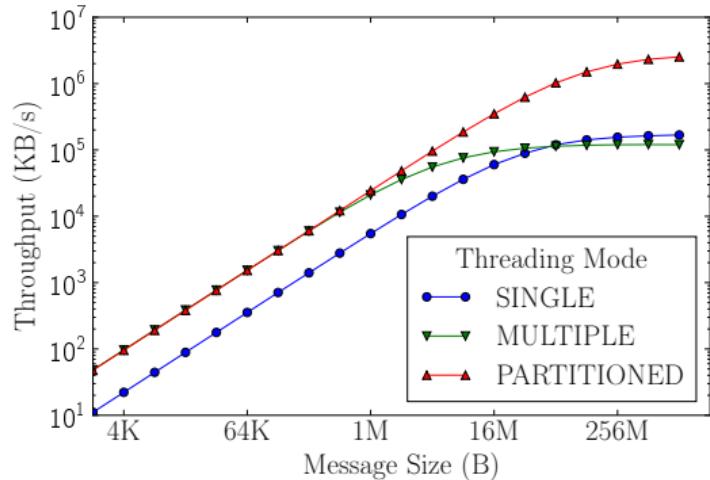
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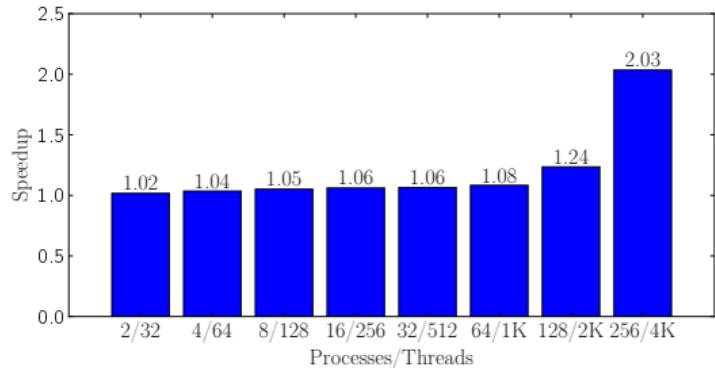
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Sweep3D communication throughput for 16 partitions, 10ms compute, and 4% Single Noise with a Hot Cache

Potential Application Improvements



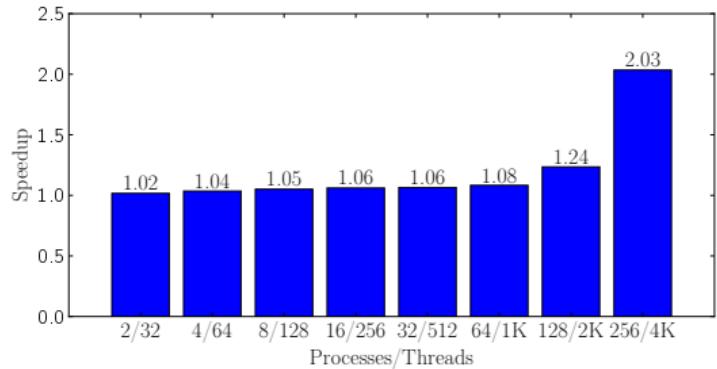
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Potential Application Improvements



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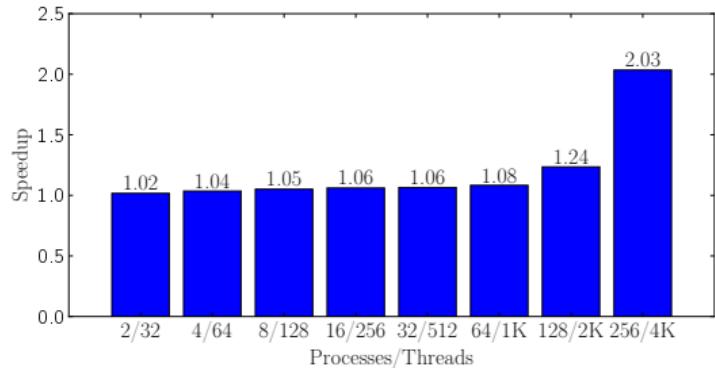


Expected Speedup From Porting SNAP-C to
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Potential Application Improvements



- ▶ The Sweep3D communication pattern showed potential for if it were ported to MPI Partitioned
- ▶ SNAP uses a Sweep3D communication
 - ▶ We profiled SNAP's communication
 - ▶ Projected the potential speedup



Expected Speedup From Porting SNAP-C to MPI Partitioned

Conclusion And Future Work

- ▶ Benchmarking for MPI-Based Deep Learning



Conclusion And Future Work



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Thank You!

Acknowledgements



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numérique** du Canada

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