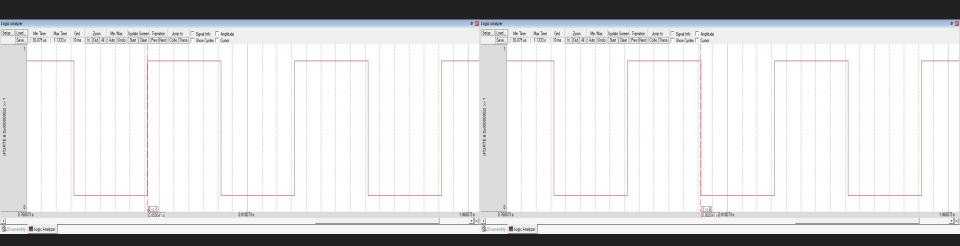
Mini Project 1

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From the line 15 of os.h file(shown above), we can see the system tick of 1ms is 80000. So to get 20 Hz (period = 1/20 = 50ms), TEST_PERIOD = 50 * 80000 = 4000000

After setting the TEST_TIMER to 1, we can get the following signal by adding PORTE.1 to logic analyzer.



0.900041 - 0.850041 = 0.05s

Frequency = 1/0.05 = 20 Hz

I have uploaded the video to youtube.

If the link does not work, I have also uploaded the video to github. :)

https://youtu.be/Vq0GncBpklM

Survey Completion

Thank you for your feedback on Mini Project 1.