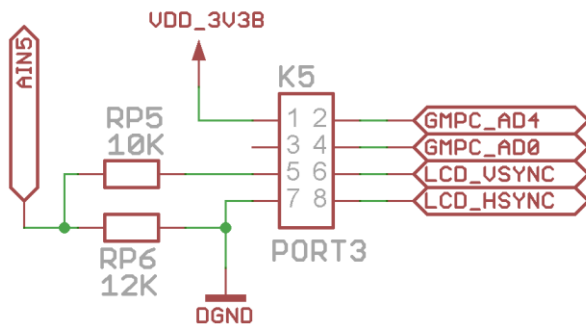
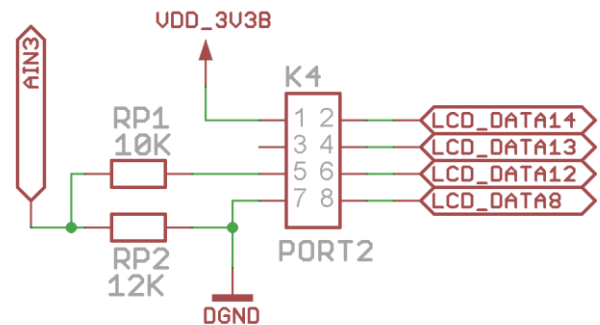
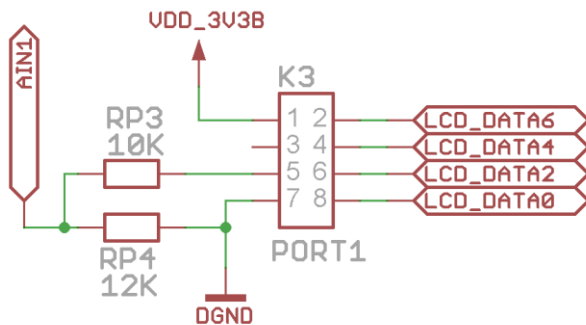
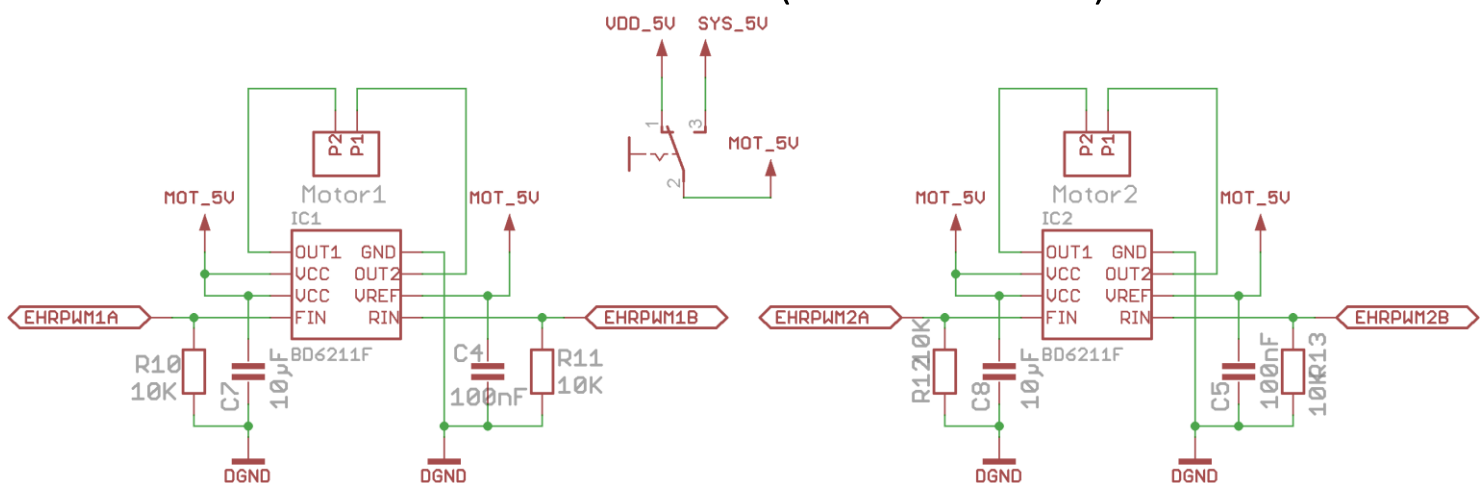


# Datenblatt des EGR-Capes

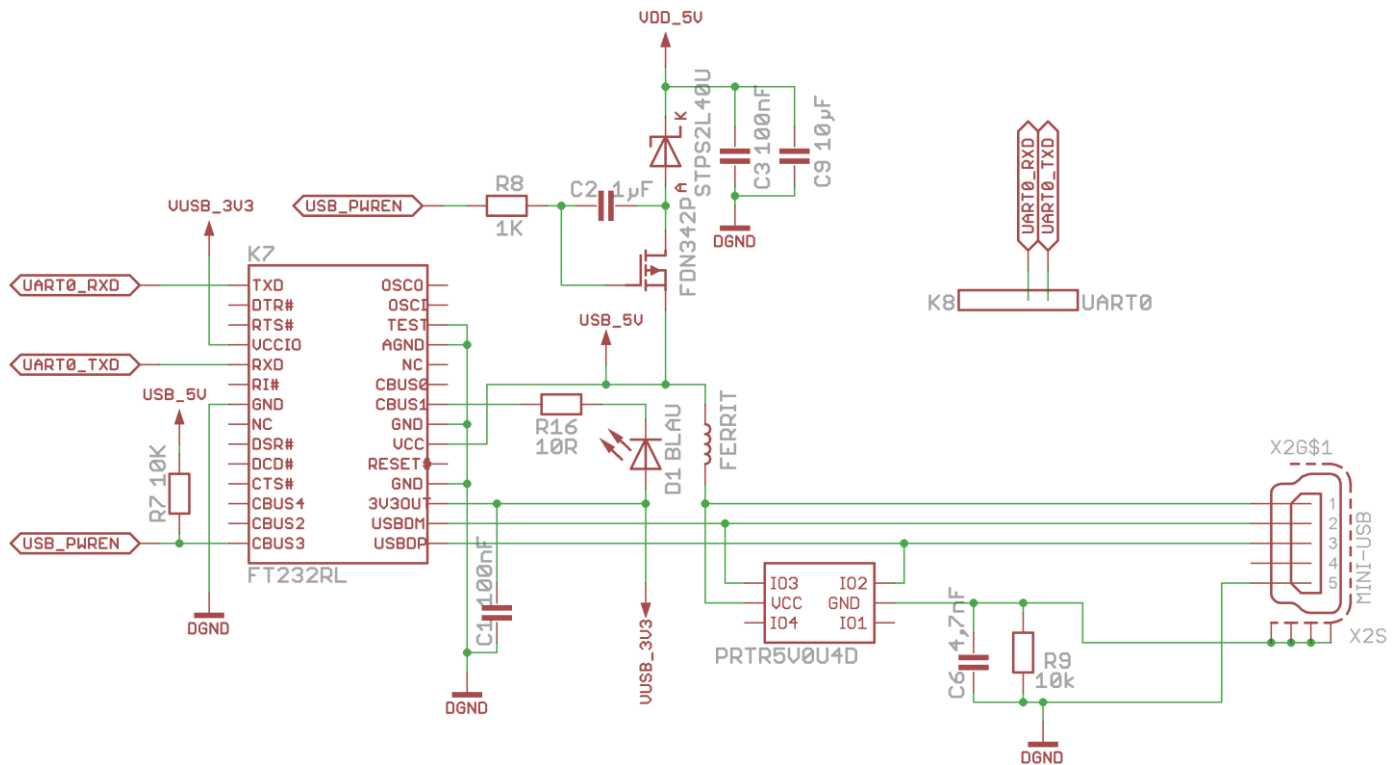
## MODULPORTS (MA-VIN KOMPATIBEL)



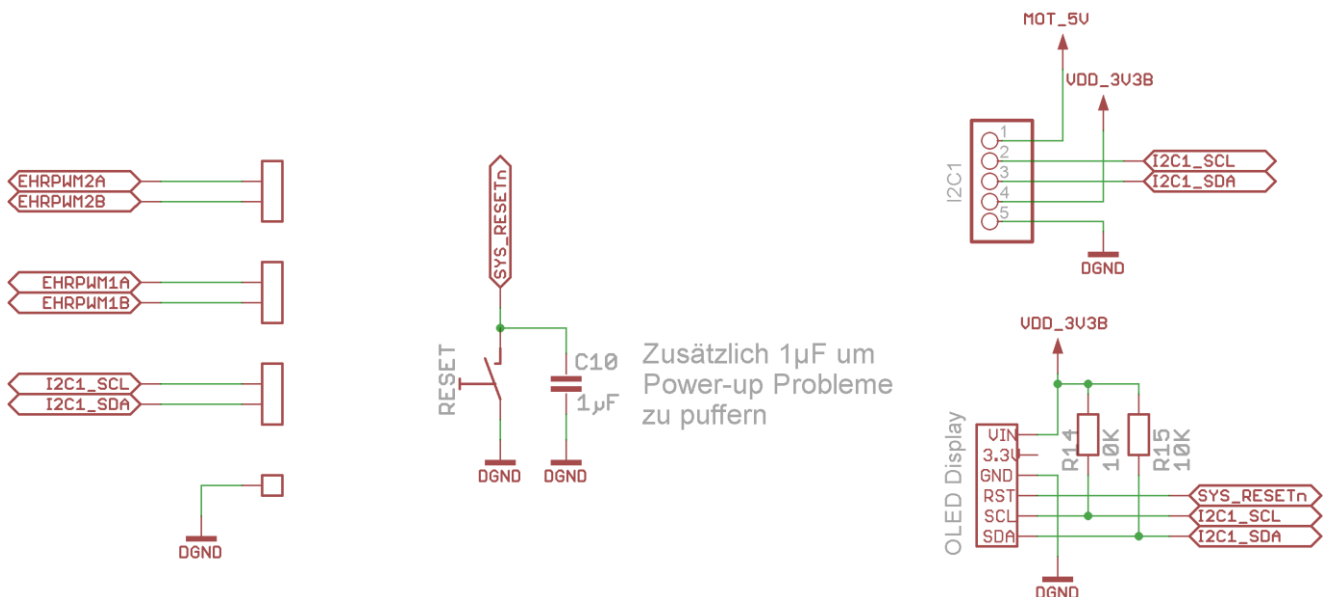
## MOTOR-TREIBER (H-BRÜCKEN)



## USB-FTDI-SCHALTUNG



## OLED-DISPLAY / I<sup>2</sup>C / SYSTEM-RESET



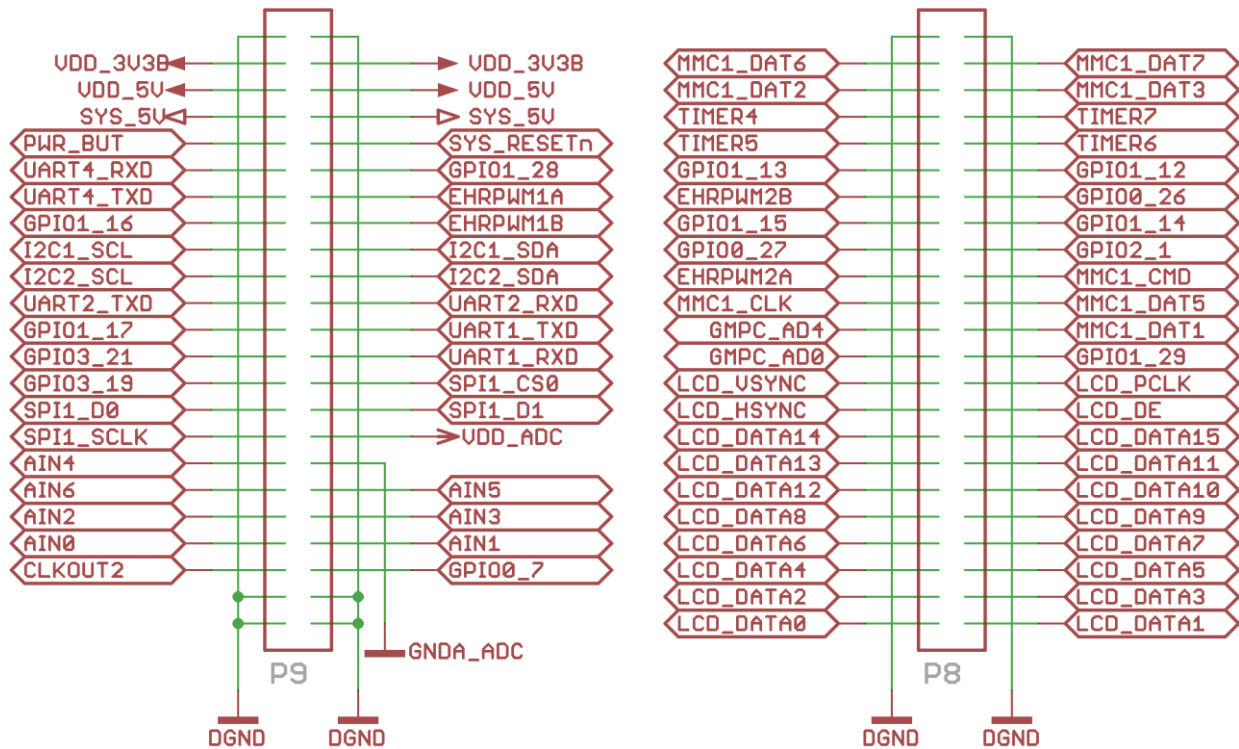
# Register

Modul steck- platz	Pin	Pin Name	Signal Name							
			Mode							
			0	1	2	3	4	5	6	7
Port1	2	LCD_DATA6	lcd_data6	gpmc_a6	Pr1_edio_...	eQEP2_index	Pr1_edio_...	Pr1_pru1_...	Pr1_pru1_...	gpio2_12
Port1	4	LCD_DATA4	lcd_data4	gpmc_a4	Pr1_mii0_txd1	eQEP2A_in		Pr1_pru1_...	Pr1_pru1_...	gpio2_10
Port1	6	LCD_DATA2	lcd_data2	gpmc_a2		ehr-pwm2_...				gpio2_8
Port1	8	LCD_DATA0	lcd_data0	gpmc_a0		ehr-pwm2A				gpio2_6
Port2	2	LCD_DATA14	lcd_data14	gpmc_a18	eQEP1_index	mcasp0_axr1	uart5_rxd		uart5_ctsn	gpio0_10
Port2	4	LCD_DATA13	lcd_data13	gpmc_a17	eQEP1B_in	mcasp0_fs_r	mcasp0_axr3		uart4_rtsn	gpio0_9
Port2	6	LCD_DATA12	lcd_data12	gpmc_a16	eQEP1A_in	mcasp0_clk_r	mcasp0_axr2		uart4_ctsn	gpio0_8
Port2	8	LCD_DATA8	lcd_data8	gpmc_a12	Ehr-pwm1_tripzo...	mcasp0_clkx	uart5_txd		uart2_ctsn	gpio2_14
Port3	2	GPMC_AD4	gpmc_ad4	mmc1_dat4						gpio1_4
Port3	4	GPMC_AD0	gpmc_ad0	mmc1_dat0						gpio1_0
Port3	6	LCD_VSYNC	lcd_vsync	gpmc_a8						gpio2_22
Port3	8	LCD_HSYNC	lcd_hsync	gpmc_a9						gpio2_23

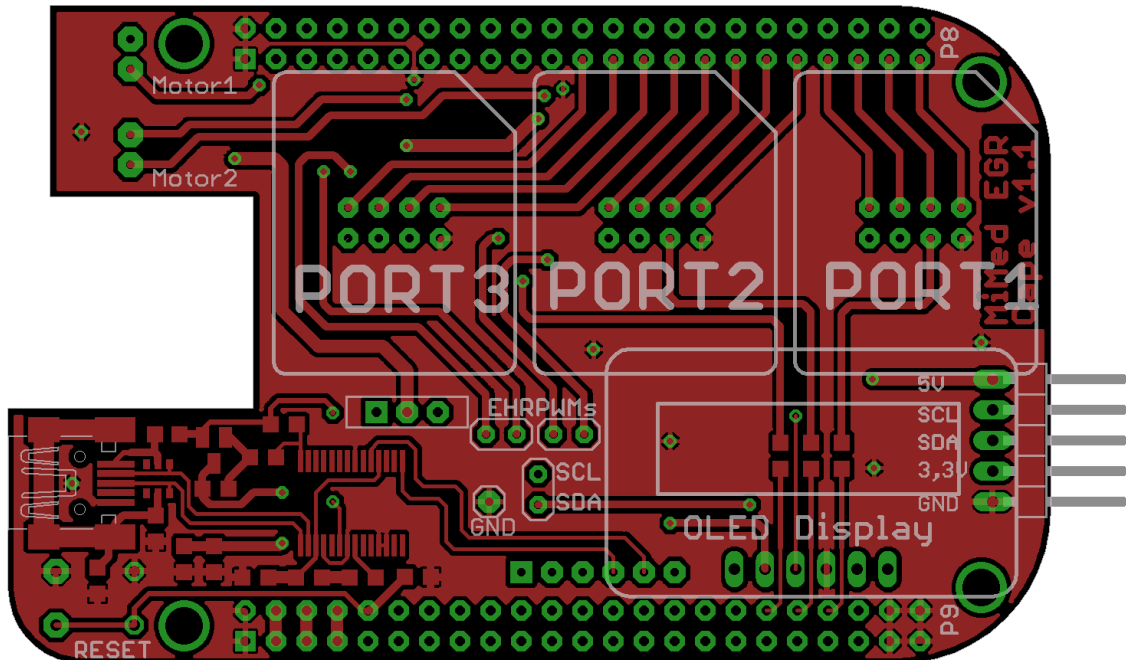
Modul steckplatz	Pin	Pin Name	Signal Name							
			Mode							
			0	1	2	3	4	5	6	7
<b>MOTOR 1</b>	1	GPMC_A2	gpmc_a2	mii2_txd 3	rgmii2_td 3	mmc2_d at1	gpmc_a1 8		<b>ehr- pwm1A</b>	gpio1_18
<b>MOTOR 1</b>	2	GPMC_A3	gpmc_a3	mii2_txd 2	rgmii2_td 2	mmc2_d at2	gpmc_a1 9		<b>ehr- pwm1B</b>	gpio1_19
<b>MOTOR 2</b>	1	GPMC_AD8	gpmc_ad 8	lcd_data2 3	mmc1_d at0	mmc2_d at4	<b>ehr- pwm2A</b>			gpio0_22
<b>MOTOR 2</b>	2	GPMC_AD9	gpmc_ad 9	lcd_data2 2	mmc1_d at1	mmc2_d at5	<b>ehr- pwm2B</b>			gpio0_23

Modul steckplatz	Pin Name	Signal Name								
		Mode								
		0	1	2	3	4	5	6	7	
I2C1_SCL		spi0_cs0	spi0_cs0	mmc2_sd wp	I2C1_SCL	ehr- pwm0_syn ci	pr1_uart0_ txd			gpio0_5
I2C1_SDA		spi0_d1	spi0_d1	mmc1_sd wp	I2C1_SDA	ehr- pwm0_trip zone	pr1_uart0_ rx			gpio0_4

## Pin Header P9 und P8



## Cape Layout - Oberseite



## Cape Layout - Unterseite

