

Lab 0: Preparing for ECE342 Labs

Introduction

In this lab, you will review concepts from ECE241 and ECE243 and learn new things such as SystemVerilog and testbenches. The material in this lab is self-taught. There will be no dedicated lab session but you are expected to complete the tutorials linked in this lab before starting lab 1. The table below lists the tutorials you should complete and the suggested order of completion.

Tutorial name	Description	Complete by
SystemVerilog Tutorial	Introduction to SystemVerilog, an updated and more robust version of Verilog.	Lab 1
ModelSim Guide	Review of Modelsim.	Lab 1
Testbench tutorial	Introduction to testbenches, as a replacement for .do files.	Lab 1
Creating Generic Hardware	Introduction to parametrized modules and generate blocks.	Lab 2

You will find each of these tutorials on Quercus.