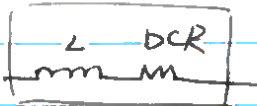



2022/06/15

Page 14 of LTC78w tells about "SENSE"

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Two current sensing schemes

Inductor

DCR: Inductor DC resistance sensing
Low value resistance sensing. 

I_{LM} - tri-level logic input \Rightarrow sets the max current limit of the controller.

- ① I_{LM} grounded - threshold voltage of the current comparator - 30mV
- ② I_{LM} is floated - 75mV. $\rightarrow V_{sense(max)}$
- ③ I_{LM} is $INTV_{cc}$ - 50mV \rightarrow

$Sense^-$ & $Sense^+$ \Rightarrow current comparator

Common mode voltage range 0V \sim 28V (abs max)

enabling the LTC78w regulator $V_{out} = 24V$ nominal.

$Sense^+$ \rightarrow High impedance, low current $\pm 1\mu A$

$sense^- \rightarrow < INTV_{cc} - 0.5V$ current $< 1\mu A$

$> INTV_{cc} + 0.5V$ current $\sim 700\mu A$

$INTV_{cc} - 0.5V > > INTV_{cc} + 0.5V$ current $1\mu A \sim 700\mu A$

R_{sense} - for low resistance sensing - chosen based on required output current

$V_{sense(max)} =$ Comparator max. threshold voltage

Project Definition

Preparatory task

Main task

Scope of for extension

Background knowledge

Resource

Reference (主要资料)

Signed: Student

Mission statement

★ Low Value Resistor Current Sensing

$R_{sense} \rightarrow$ based on required output current

$$R_{sense} = \frac{V_{sense(max)}}{I_{max} + \frac{\Delta I_L}{2}} \rightarrow \text{The current comparator has a max threshold } V_{sense(max)} \text{ determined by } I_{LIN}$$

$V_{sense(max)}$ sets $I_{L peak} \rightarrow I_{max} = I_{peak} - \frac{1}{2} \Delta I_L$
 \downarrow
 30mV, 50mV, 75mV.

★ Inductor DCR Sensing

(Heavy load)

\rightarrow For applications requiring highest ~~for~~ efficiency at high load current

Because for high current, sense resistor will cost several points of efficiency.

if $(R_1 || R_2) \cdot C_1$ time constant $\gg \frac{L}{DCR}$ (satisfied)

$$\text{thms } V_{C1} = V_{DCR \text{ Inductor}} \times \frac{R_2}{R_1 + R_2}$$

Using inductor ripple current value, the target sense resistor value.

$$R_{sense(Lequivalent)} = \frac{V_{sense(max)}}{I_{max} + \frac{\Delta I_L}{2}}$$

$$R_D = \frac{R_{sense(Lequivalent)}}{DCR_{max \text{ at } I_L(max)}}$$

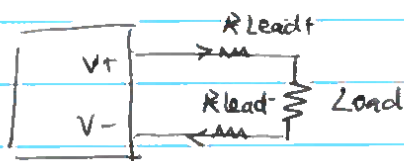
$$C_1 = 0.1 \mu F \sim 0.47 \mu F \quad R_1 || R_2 \text{ around } 2k$$

$R_1 \rightarrow$ Max power loss in R_1 is related to duty cycle.

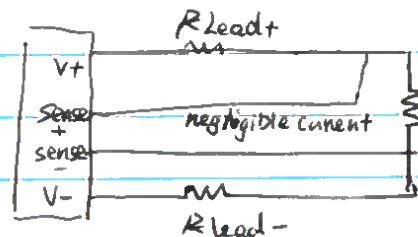
★ Sense: — a technique used in power supplies to produce the correct voltage

\downarrow for a load (wiki)

$V_{sense} \rightarrow$ Output voltage over load?



power supply without sense



power supply with sense connection

sense \rightarrow a measurement of the voltage at the resistor

\downarrow no current flow in the sense wires because of high input resistance without having impact (no voltage drop since no current)

Control $V+$, $V-$ power supply (Feedback)

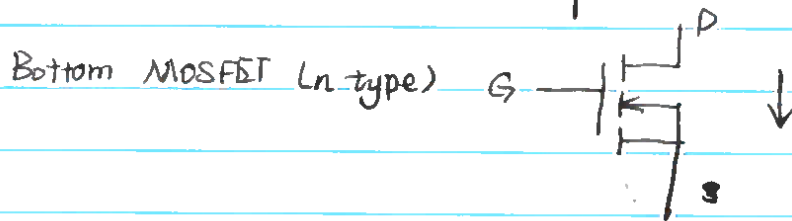
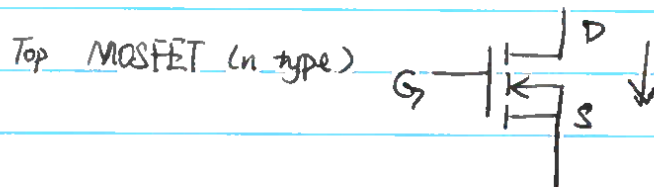
$$\left(\frac{499}{82} + 1\right) \times 0.8 = 5.668 \text{ V not } 6.2 \text{ V output / my simulation } \sim 3.7 \text{ V}$$

$$\left(\frac{x}{82} + 1\right) \times 0.8 = 3.3 \text{ V}$$

$$\frac{x}{82} + 1 = \frac{3.3}{0.8}$$

$$x = \left(\frac{3.3}{0.8} - 1\right) \times 82 = 256.25$$

ignore LTspice simulation different.



$$V_{GS1} = V_{TG} - V_{SW}$$

$$V_{GS2} = V_{BS} - GND = V_{BG}$$