

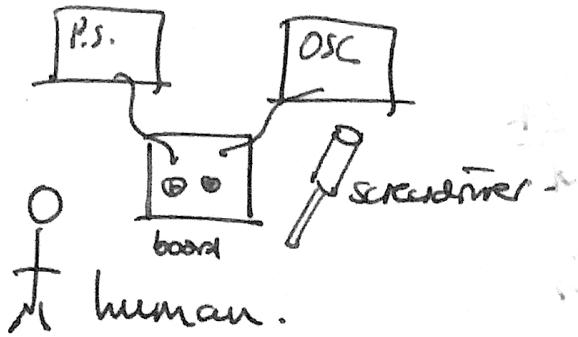
Minutes of 6/13 meeting:

- 1, Architecture of a remote lab: Could Service -> Odroid-C4 (Single board computer) -> { NanoIOT33 (Microcontroller), Oscilloscope, Power Supply, PCB Board}.
- 2, User controllable parameter: Oscilloscope, Power Supply, Variable elements on PCB Board (includes resister, capacitor and inductor).
- 3, Variable elements: Digital potentiometer (resistor); Varactor(capacitor).
Decision that need to be made:
 - A, decide to use which device to achieve this;
 - B, Tuning range for user(safe range);
 - C, Available to buy.
- 4, Safe range for variable elements: control theory. (manufacturing variations, EMC problem, etc).
- 5, Check on similar remote lab project on GitHub – Code for NanoIOT33.
- 6, Finish Mission Statement this week.

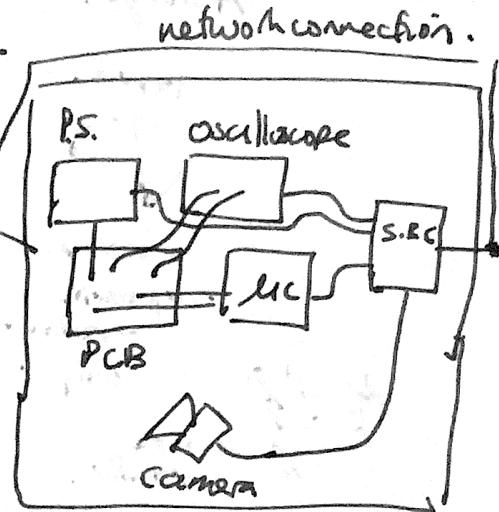
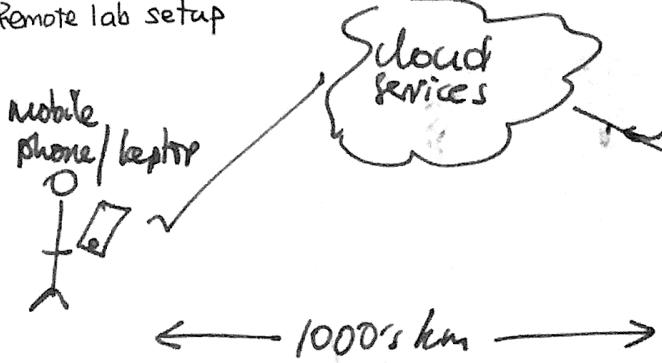
Goal of this week (6/13-6/19):

- 1, Mission Statement
- 2, Laboratory passport signed
- 3, Buck Converter - Study (circuit operation, data sheets, parameter selection, layout)
- 4, Variable elements - Resistor - device selection (data sheet)

- 1) GitHub - Logbook record
 - other resources
- 2) Weekly Meeting
 - A summary of week work
 - Questions (come with several solutions)
which one is optimal & why.
(Evaluation)



Remote lab setup

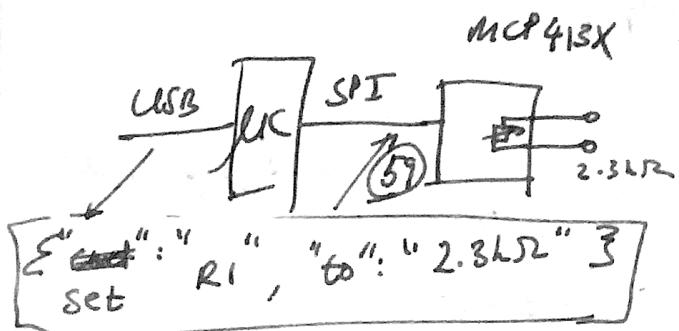


JSON format message.

```
{ "cmd": "→", "params": [ "R": "→", "V": "→" ] }
```

digital potentiometer.

- Odroid C4
- nano IOT 33



7bit/8bit.

5, 10, 50, 100 pF. ① What capacitance tuning range do you need?

variable ② What is available capacitance to buy?

→ physical tunable capacitor

→ Varactor

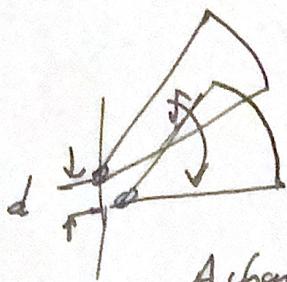
→ ?? tunable capacitor

STPTIC-82C4

10kΩ.

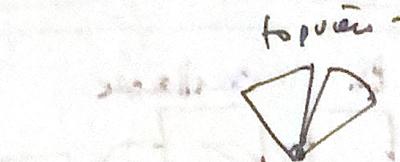
$$\frac{2.3k}{10k} \times 255 = 59$$

$$0-255 = 256 \text{ (8bit)}$$

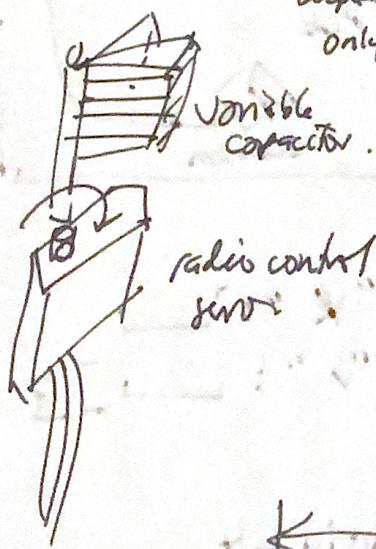


$$C = \frac{\epsilon A}{d}$$

A changes, d is fixed



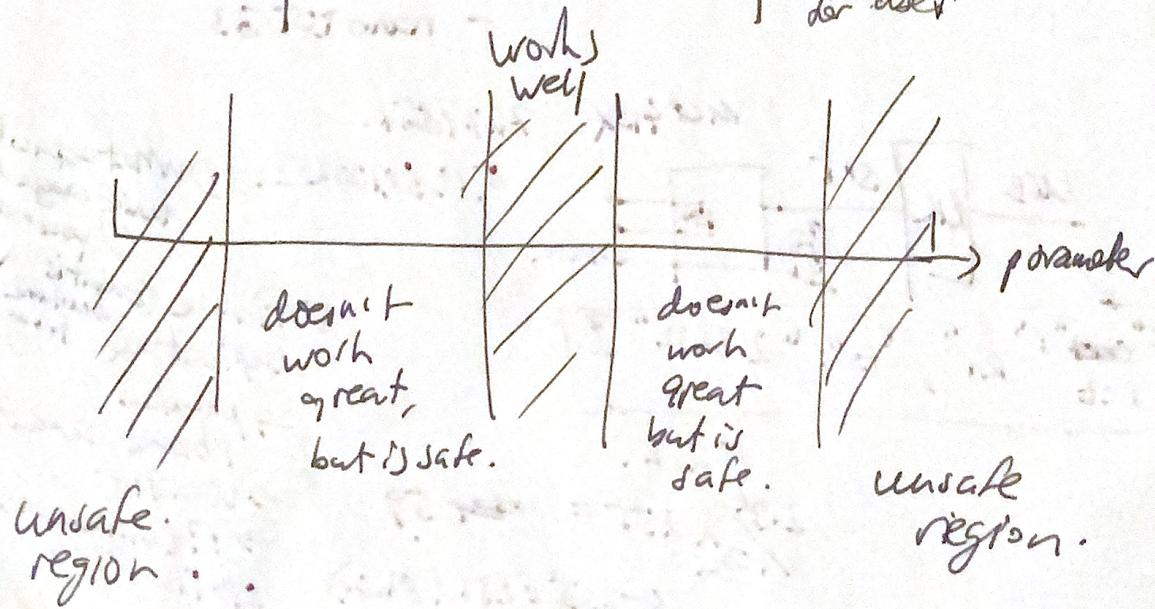
Overlap
≈ plastic
capacitance
only

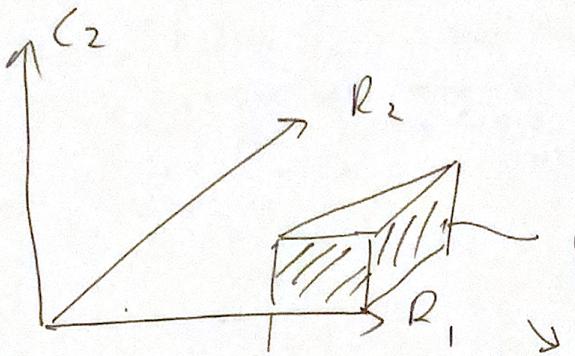


Collin "Canard"

How do you
achieve that?

allowed range
for user





*N-dimensional
space of
possible
parameters.*

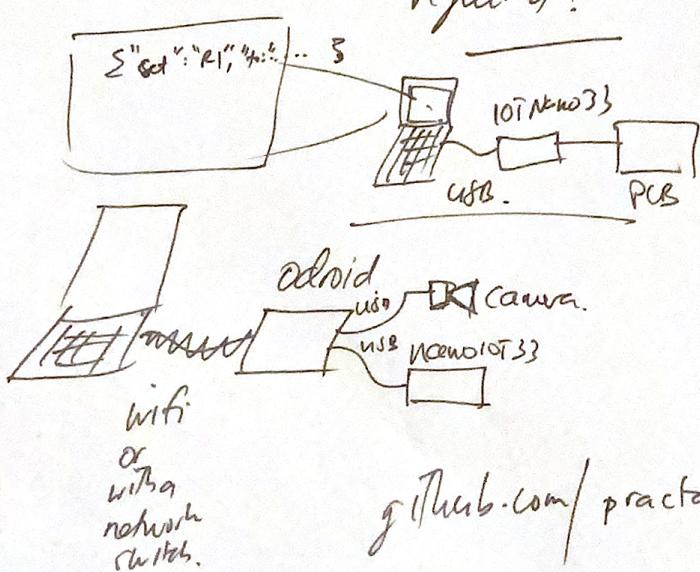
→ EMC problem
- avoid by good design
(but not require to do
emc evaluation)

example in 3D.

on chip power supply - must handle
manufacturing variations.

millions of chips ⇒ lots of variation!

→ "analogue" student provides variation.
→ same fundamental understanding
required!



[github.com/practical/penduino/blob/master/
Sketch/Sketch.ino.](https://github.com/practical/penduino/blob/master/Sketch/Sketch.ino)