

Logbook → Remote lab - Buck converter

2022/6/14

LTSpice runs the circuit

$$V_{out} = 3.76V \quad 3.792 \quad 3.805$$

$$R_L = 3 \quad 4 \quad 5$$

Different from pdf → 6V around output

⇒ Frequency different.

LTC7800 determine the switching frequency.

P_{in} , FREQ → $R_{freq} = 100k \Rightarrow Freq_{typical} = 2.1 MHz$

★ But Week 1 pdf simulation graph - $R_{freq} = 33k \Rightarrow 500kHz$

(Page 22 datasheet)

frequency? can change $\frac{V_{out}}{V_{in}}$ ratio?

Normally $\frac{V_{out}}{V_{in}} = \text{Duty}$ → what determine duty cycle in
continuous mode LTC7800?
dis-continuous mode

$$V_{out} = V_{in} \times \frac{2}{1 + \sqrt{1 + \frac{4k}{D^2}}} \quad \text{where } k = \frac{RL}{R_{sTs}} \quad \times \text{ can't happen}$$

LTC7800 → Typical on-time 45ns (minimum on-time

$$\begin{matrix} m & \mu & n & p \\ & & \downarrow & \\ & & 10^{-9} & \end{matrix}$$

2.2, 2.2 MHz

As the peak sense voltage decrease ↓
the minimum on-time gradually increase up to 70ns

↓ Forced Continuous Application.

$$T_{on(min)} < \frac{V_{out}}{V_{in}(f)}$$

Page 24 of LTC7800 datasheet:

Design example $V_{in} = 12V$ $V_{IN} = 22V$ (max)
(normal)

$$V_{out} = 3.3V \quad I_{max} = 5A \quad V_{sense-max} = 75mV$$

$$f = 1MHz \rightarrow 55k\Omega \text{ to FREQ - GND}$$

Inductance value → ripple current 30%

Highest value of ripple current occurs at V_{in-max}

$$\Delta I_L = \frac{V_{out}}{(f) \times L} \times \left(1 - \frac{V_{out}}{V_{in(nom)}} \right)$$

Inductor ripple current

$$1 - \frac{3.3}{12} = 0.725$$

$$V_L = L \times \frac{di_L}{dt} \Rightarrow \Delta I_L = \frac{V_L}{L} \times \Delta T$$

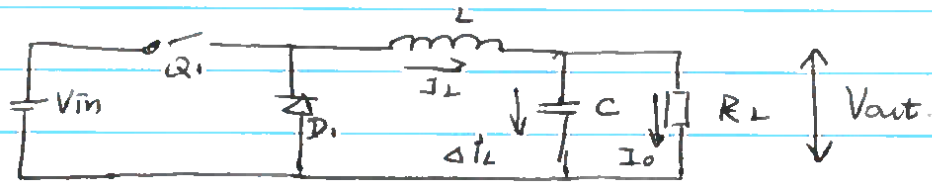
$$\Delta I_L = \frac{(V_{in} - V_o) - V_o}{L} \times T_{on}$$

current ripple

$$= \frac{V_{in} - V_o}{L} \times T_{on}$$

\downarrow
 $\frac{1}{f} \times D$

Typical Buck Converter



$$\Delta I_L = I_L(T_{on}) - I_L(T_o)$$

$$I_o = \frac{I_L(T_{on}) + I_L(T_o)}{2}$$

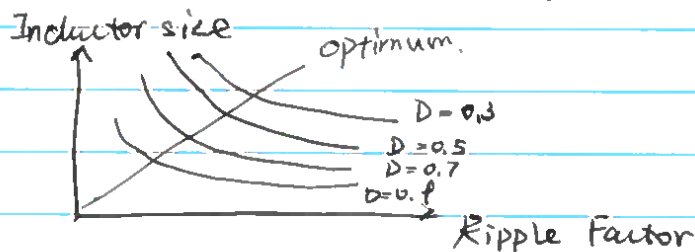
$$\text{ripple factor} = \frac{\Delta I_L}{I_o} < 2$$

continuous mode.

$$\Delta I_L = \frac{V_{in} - V_o}{L} \times D \cdot T_s$$

$$\text{ripple factor} = \frac{\Delta I_L}{I_o} = \frac{(V_{in} - V_o) \cdot D \cdot T_s}{L I_o}$$

$$= \frac{V_{in} (1 - D) \cdot D \cdot T_s}{L I_o}$$



High ripple factor - Low Inductor.

Low ripple factor - Large Inductor.

For $D = 0.1$, ripple factor (gwd) = $0.33 \sim 0.66$

$D = 0.3$

$0.28 \sim 0.56$

$D = 0.5$

$0.25 \sim 0.48$

$D = 0.7$

$0.2 \sim 0.4$

\Downarrow
30% ripple design

$$0.3 = \frac{V_{in} (1 - D) \cdot D \cdot T_s}{L I_o}$$

$$\frac{V_{out}}{V_{in}} = D$$

$$\text{Ripple factor} = \frac{\Delta I_L}{I_o} = \frac{V_{out} \times (1 - D) \cdot \frac{1}{f}}{L}$$

$$\Delta I_L = \frac{V_{out}}{f \times L} \times (1 - \frac{V_{out}}{V_{in}}) \star \text{make sense}$$

⇒ Inductor chosen First principle = 30% ripple current

1.5 μ H inductor - (32%) ripple current

$$\text{Ripple factor} = \frac{V_{out}}{f \times L} \left(1 - \frac{V_{out}}{V_{in}}\right) \times \frac{1}{I_o}$$

$$I_{L\text{-peak}} = I_{DC\text{-max}} + \frac{1}{2} \Delta I_L = 5A + \frac{1}{2} \times 5 \times 32\%$$

$$= 5 + 2.5 \times 0.32$$

$$= 5.8A$$

2 6 p
m μ n. p

$$T_{on} = \frac{V_{out}}{V_{in\text{-max}}} \times \frac{1}{f} = \frac{3.3V}{22V} \times \frac{1}{1MHz} = 1.5 \times 10^{-7} = 150 \text{ ns}$$

Solved: Duty cycle $D = \frac{V_{out}}{V_{in}}$ (modify V_{out} or $V_{in} \rightarrow$ get Duty get T_{on})

Always operate in continuous mode

$$V_{out} \text{ is set by } V_{FB} = V_{out} = 0.8 \times \left(1 + \frac{R_B}{R_A}\right) V$$

What is I_{sense} & V_{sense} ? How do they related to Duty cycle?

I_{ZIM} - foldback Current Limit

$\left. \begin{matrix} \text{Sense-} \\ \text{sense+} \end{matrix} \right\}$ inputs to the differential current comparator

When $> INTV_{cc} - 0.5V$ Sense- supply power to the current comparator

→ normally connect to DCR sensing network

current sensing resistor

ITH pin voltage → offset of sense- & sense+ with R_{sense}

Set the current trip threshold.

$I_{ZIM} = \left\{ \begin{matrix} SGND \\ FLOAT \\ INTV_{cc} \end{matrix} \right\}$ sets the max current sense threshold to three different levels for the comparators

★ Forced Continuous Mode (Burst mode, Phase Skipping)

PLLIN/MODE Pin connected to SGND ⇒ Burst Mode Operation

→ $INTV_{cc} \rightarrow$ Continuous operation

~~INTV~~ $INTV_{cc} > DC \text{ voltage} > 1.2V \Rightarrow$ Pulse Skipping
-1.3V

LC Mode
(Need to do actions further)