FDSPP26A — $0 \sim 12V$ — 33A) (on

    $0 \sim 1A$ at $1V$ source voltage

    $1.2V \sim 1.5V$ Gate voltage

for LM741 & FDSPP26A.

$$\overset{=1}{=} \quad V_{ref} = \frac{1 + [1.2 \sim 1.5]}{2}$$

$$= \frac{2.2 \sim 2.5}{2}$$

$$= 1.1 \sim 1.25 V \Leftarrow$$

by simulation, the feed V_{in} from Rsense

    V_{ref} should be $0.6 \sim 1.8 V$ to get $0 \sim 0.1 A$ ~~output~~ I_d.

Constant Resistance

$$I_{ref} = V/R$$
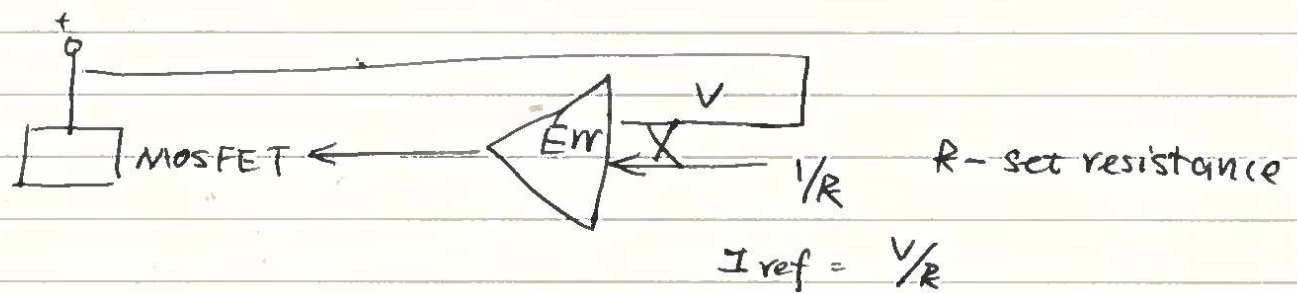
Constant Resistance Mode
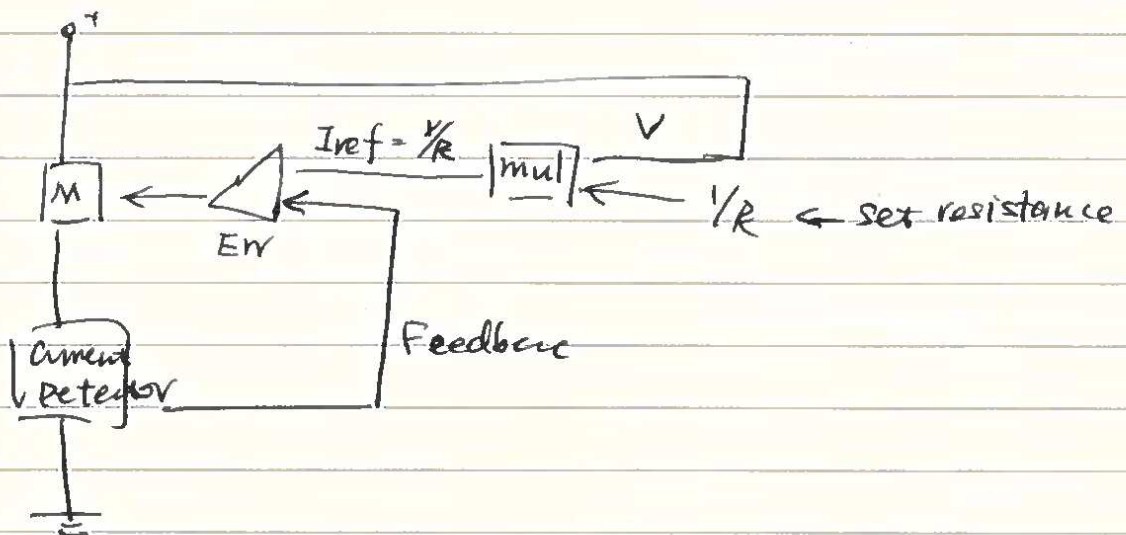
for example, we want this electronic load act as
a constant resistor with 2Ω resistance.

→ 1V source $\frac{1}{2} = \frac{1}{2}A$ — 0.5A ⎤ Sense → 0.5V

→ 2V source $\frac{2}{2} = 1A$ ⎦ resistor. → 1V.

Multiplexer → $\frac{1}{2}$ → R

Vcontrol → $\underline{0.6 \sim 1.8V}$ → 1.2V
              $0.2 \sim 2.6V$.
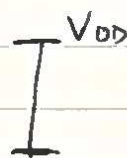
Comparator.

~~A~~ — 0.5+

Amplifier → only amplifying the AC part of signal & DC part of sig
              which amplification on DC part ~~have trouble to~~
              ~~have~~ require additional control to balance
              → V— → Vin from current sense amplifier.
                        has DC part
              →
              V+ → Vref from set source voltage

Precision, Amplifier → reduce.

MAX44280. I $^{V_{DD}}$

Common voltage.

~~Vin +Vo + ... ~~

Comparator   0.01V

$V_{in} = 1V + 1mVsine$ 0.989 ~ 0.969 V   20mV → 10mVsine + 0.999 v

$V_{out} = -13.6V \sim -12V$

$\updownarrow$ ~~1.58V~~ 1.6V

Vref = 1V (0.9841 v) $-12.8V$ with $\pm0.8V$

$gain\_openloop = 80$

V_{in1} V_{out1}

15V   20k   20k   10k   ~~13.10~12V~~

12.05V ~ 13.438V

$\updownarrow$ 1.387V

$gain (-1)$