CPU VDD_CPUX_1V3 VDD_SYS_1V2 VDD_SYS0 L10 U1D VCC_IO_3V3 VCC_IO_3V3 U1C VDD CPUX0 VDD_CPUX1 VDD_CPUX2 VDD_SYS1 VDD_SYS2 J15 PA0/UART2_TX/JTAG_MS0/PA_EINT0 PA1/UART2_RX/JTAG_CK0/PA_EINT1 PA2/UART2_RTS/JTAG_DO0/PA_EINT2 VCC-PD H7 P7 SDC1 VCC PG VDD_CPUX3 VDD_CPUX4 VDD_CPUX5 VDD_SYS3 VDD SYS4 PA3/UART2_CTS/JTAG_DI0/PA_EINT3 PA4/UART0_TX/PA_EINT4 F5 H6 H6 PA6/UARTO_TX/PA_EINT4
PA5/UARTO_RX/PWM0/PA_EINT5
PA6/SIM_PWREN/PWM1/PA_EINT6
PA7/SIM_CLK/PA_EINT7 PG0/SDC1_CLK/PG_EINT0
PG1/SDC1_CMD/PG_EINT1
PG2/SDC1_D0/PG_EINT2
F3 VDD_SYS5 UARTO_TX \\\
UARTO_RX \\\ R7 VDD_CPUX6 VDD_CPUX7 VDD SYS6 K12 ->> PG2/USER_KEY1 [6] VDD_SYS7 PG3/SDC1_D1/PG_EINT3 PG4/SDC1_D2/PG_EINT4 VDD_CPUX8 VDD_CPUX9 VDD_SYS8 VDD_SYS9 PAO/SIM_FWREIN/FWM1/F/
PA7/SIM_CLK/PA_EINT7
PA8/SIM_DATA/PA_EINT8
PA9/SIM_RST/PA_EINT9 **GPIO** PD3/RGMII_RXD0/MII_RXD0/RMII_RXD0 PD3/RGMII_RXD0/MII_RXD0/RMII_RXD0
PD4/RGMII_RXCK/MII_RXCK/RMII_NULL
F19 PG5/SDC1_D3/PG_EINT5 PG6/UART1_TX/PG_EINT6 VDD_CPUX10 VDD_CPUX11 VDD_SYS10 PD5/RGMII RXCTL/MII RXDV/RMII CRS D ->> PG6/UART1_TX [6] PA10/SIM_DET/PA_EINT10 PA11/TWI0_SCK/DI_TX/PA_EINT11 VDD_SYS11 PD6/RGMII_NULL/MII_RXERR/RMII_RXER PD7/RGMII_TXD3/MII_TXD3/RMII_NULL PG7/UART1_RX [6] PG8/UART1_RTS [[6] I2C0_SCK [6] I2C0_SDA [6] I2C0_SDA] PA13/SPI1_CS PA14/SPI1_CLK PA15/SPI1_MOSI PG7/UART1_RX/PG_EINT7 VDD_CPUX12 VCC_IO_3V3 PG8/UART1_RTS/PG_EINT8 E3
PG9/UART1_CTS/PG_EINT9 M3 PA12/TWI0_SDA/DI_RX/PA_EINT12 PA13/SPI1_CS/UART3_TX/PA_EINT13 VCC_IO0 G13 G14 VCC_IO1 VCC_IO2 H13 PD8/RGMII_TXD2/MII_TXD2/RMII_NULL PD9/RGMII_TXD1/MII_TXD1/RMII_TXD1 PD8/RGMII_TXD2/MII_TXD2/RMII_NULL
PD9/RGMII_TXD1/MII_TXD1/RMII_TXD1
PD10/RGMII_TXD0/MII_TXD0/RMII_TXD0
PD11/RGMII_NULL/MII_CRS/RMII_NULL
PD12/RGMII_TXCK/MII_TXCK/KMII_TXCK
K17
PD13/RGMII_TXCTL/MII_TXEN/RMII_TXEN PG10/PCM1_SYNC/PG_EINT10 PG11/PCM1_CLK/PG_EINT11 D1 [3] VDD_CPUFB>> VDD_CPUXFB GND_CPUXFB PA14/SPI1_CLK/UART3_RX/PA_EINT14 PA15/SPI1 MOSI/UART3 RTS/PA EINT15 D15 C14 B13 PG12/PCM1_DOUTPG_EINT12 PG13/PCM1_DIN/PG_EINT13 √C PG12/USB0_OTGID [4] VCC_IO3 H14 VCC_IO4 J14 PA16/SPI1_MISO >> PA16/SPI1_MISO/UART3_CTS/PA_EINT16 VDD_CPUS J7 VDD CPUSO PA17/SPDIF_OUT/PA_EINT17 PA18/PCM0_SYNC/TWI1_SCK/PA_EINT18 PD13/RGMII_TXCTL/MII_TXEN/RMII_TXEN PD14/RGMII_NULL/MII_TXERR/RMII_NULL L17 K18 L18 L19 R1 0R VDD_CPUS1 VCC_IO5 **POWER** PA19/PCM0_CLK/TWI1_SDA/PA_EINT19 PA20/PCM0_DOUT/SIM_VPPEN/PA_EINT20 PD15/RGMII_CLKIN/MII_COL/RMII_NULL PD16/MDC H3-BGA3XX_12 BGA347P0_65B14X14 PA21/PCM0_DIN/SIM_VPPPP/PA_EINT21 PD17/MDIO H3-BGA3XX_12 BGA347P0_65B14X14 GND PE0/CSI_PCLK/TS_CLK CSI MCLK HSYNC VSYNC U1G PE3/CSI_VSYNC/TS_DVLD SDC0_D1 SDC0_D0 SDC0_CLK PF0/SDC0_D1/JTAG_MS0 PF1/SDC0_D0/JTAG_DI0 PE4/CSI_D0/TS_D0 PE5/CSI_D1/TS_D1 -≪UBOOT [6] UBOOT JTAG_SEL0 D10 C8 C11 D20 F18 [5] [5] [5] [5] PF2/SDC0 CLK/UART0 TX PE6/CSI_D2/TS_D2 PE7/CSI_D3/TS_D3 **USB** JTAG_SEL TEST SDC0_CMD SDC0_D3 PF3/SDC0_CMD/JTAG_DO0 I_D3 I_D4 C20 PF4/SDC0_D3/UART0_RX G18 PF5/SDC0_D2/JTAG_CK0 AA6 PE8/CSI_D4/TS_D4 PE9/CSI_D5/TS_D5 SYS -NCC SYS 3V3 SDC0 R3 47K AP RESET# [6] VCC-USB RESET PE10/CSI_D6/TS_D6 -PE11/CSI_D7/TS_D7 -A11 B12 C7 C6 C5 KEYADC [6] KEYADO CSI_SCK CSI_SDA PE12/CSI_SCK/TWI2_SCK PE13/CSI_SDA/TWI2_SDA PE14 -VCC_IO_3V3 VDD_EFUSE RESET C2||4.7uF||GND A5 USB_DM0 VDD_EFUSEBP PLLTEST PE15 C3 AVCC_3V3 VCC_PLL NC H3-BGA3XX_12 X24MOUT BGA347P0_65B14X14 0402CS USB DM1 X24MIN VCC_HDMI_3V3 B6 USB_DP1 VCC_RTC_3V3
VCC_RTC
RTC_VIO

W4 C4 100nF USB_DM2 W2 MBIAG W1 MICIN1P MICIN1N MBIAS C5||4.7uF||GND **HVCC** RTC_VIO USB DP2 HDMI HGND MICIN1F P3 U4 V5 ->> HTX0P ->> HTX0N USB_DM3 USB_DP3 U1B X32KFOUT Y2 MICINYN

AA2 MICIN2P

MICIN2N

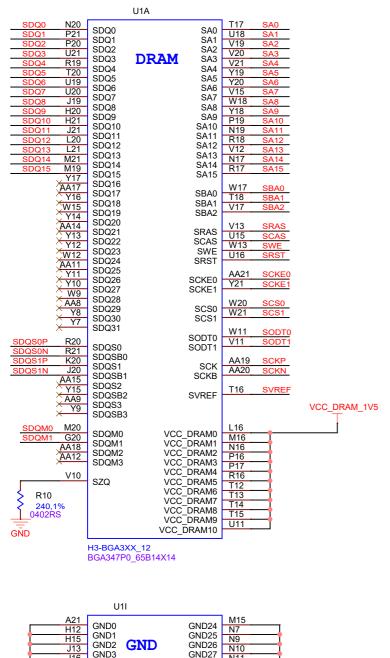
MICIN2N

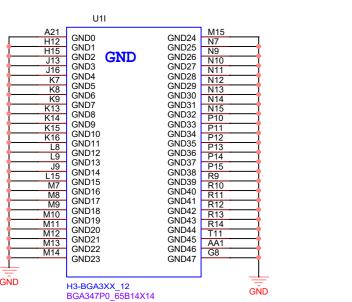
LINEOUTR

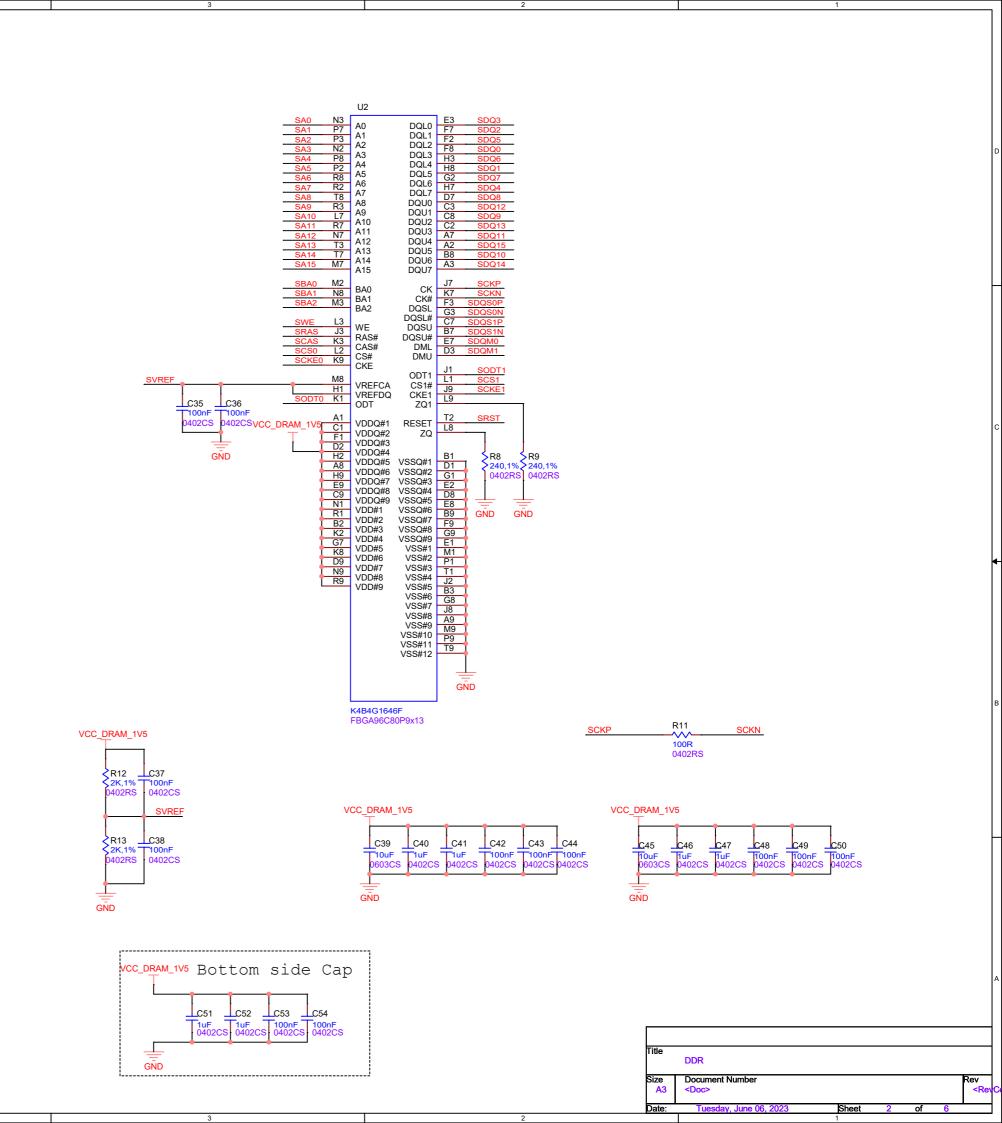
LINEOUTR

VBA1 HTX0P X32KOUT HTX0N HTX1P HTX0N X32KIN NAND [6] LINEOUTL < LINEOUTL VRA1 HTX1N PC0/NAND_WE/SPI0_MOSI
PC1/NAND_ALE/SPI0_MISO
PC2/NAND_CLE/SPI0_CLK
PC3/NAND_CE1/SPI0_CS
PC4/NAND_CE1/SPI0_CS
F16 HTX2P HTX2N HTX2F VRA2 W5 →>>> LCD_MOSI [6] VRA2 AVCC VRP AUDIO HTX2N V3 V4 H3-BGA3XX 12 HTXCP HTXCN HHPD HCEC HSDA ->> LCD_SCL [6] ->> LCD_CS [6] ->> PC4/LCD_RS ->> PC5/LCD_RES AVCC_3V3 |-HTXCP BGA347P0_65B14X14 VRP CODEC VRP V1 LINEINL LINEINF AGND V3 HTXCN PC3/NAND_CE1/SPI0_CS
PC4/NAND_CE0
PC5/NAND_RE/SDC2_CLK
PC6/NAND_RB0/SDC2_CMD
PC9/NAND_DQ1/SDC2_D0
PC9/NAND_DQ1/SDC2_D1
PC10/NAND_DQ2/SDC2_D2
PC11/NAND_DQ4/SDC2_D3
PC12/NAND_DQ4/SDC2_D4
PC13/NAND_DQ5/SDC2_D5
PC14/NAND_DQ5/SDC2_D5
PC14/NAND_DQ7/SDC2_D7
PC16/NAND_DQ5/SDC2_D7
PC16/NAND_DQ5/SDC2_RST HHPD PC4/NAND_CE0 PC5/NAND_RE/SDC2_CLK HCEC **AGND** HSDA U1E HSCI PL0/S_TWI_SCK/S_PL_EINT0 PL1/S_TWI_SDA/S_PL_EINT1 PL2/S_UART_TX/S_PL_EINT2 ->> PL2/USB0-DRVVBUS [4] ->> PL3/POWER_KEY [6] ->> PL4/RECOVERY [6] ->>> PL5/VCC_IO_EN [3] PLZ/S_UART_RX/S_PL_EIN12 PL3/S_UART_RX/S_PL_EIN13 PL4/S_JTAG_MS/S_PL_EIN14 PL5/S_JTAG_CK/S_PL_EIN15 PL6/S_JTAG_DO/S_PL_EIN15 PL7/S_JTAG_SI/S_PL_EIN17 PL8/S_PL_EIN18 PL9/S_PL_EIN18 T4 T3 T2 M6 TVOUT PL7/WIFI_EN [3]
PL8/PWR_STB [3]
PL9/PWR_DRAM
PL10/PWR_LED TVOUT PL10/S_PWM/S_PL_EINT10 PL11/S_CIR_RX/S_PL_EINT11 G9 H8 V33_TV GND_TV -AVCC 3V3 H3-BGA3XX_12 BGA347P0_65B14X14 **CPUS** C6 C10 C8 C9 H3-BGA3XX 12 H3-BGA3XX_12 BGA347P0_65B14X14 BGA347P0_65B14X14 close IC GND GND GND VDD_CPUS VCC_IO_3V3 VDD_CPUX_1V3 VDD_SYS_1V2 C22 C23 C24 C25 C11 C12 C13 C14 C16 C17 C18 C19 C20 C21 STANDBY PWR-STB 0 C26 18pF M2 М3 M4 GND GÑD GND PWR-DRAM 0 M3 МЗ M3 МЗ XIN GND2 GND1XOUT VCC_RTC_3V3 24M-20pF-10PPM SÑD ORIGIN_H3 V1. 1 By:YM 2023/5/25 R6 C31 C32 C33 C27 C29 C28 C34 GND GND GND MTG310_500 MTG310_500 MTG310_500 MTG310_500 R7 OR Document Number GND

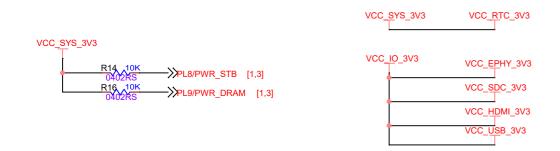
DDR3 16×1

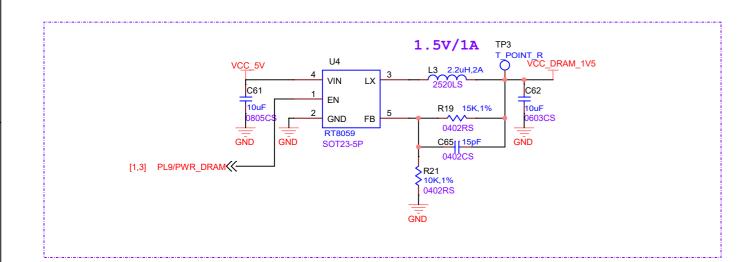


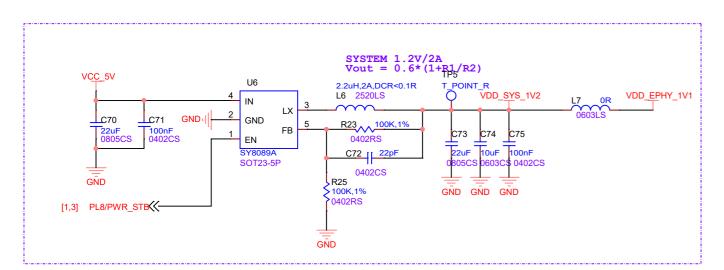


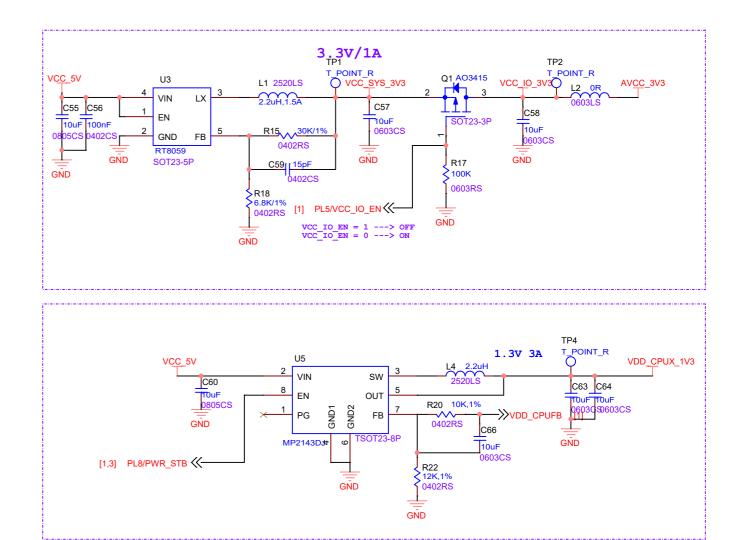


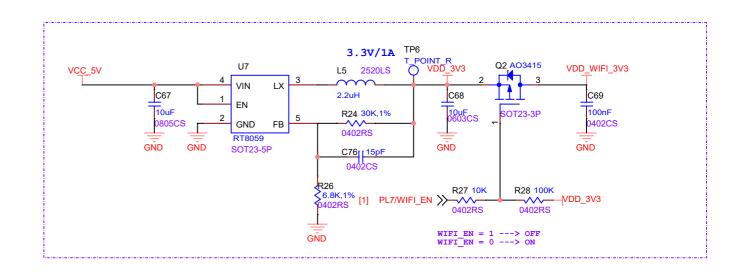
POWER







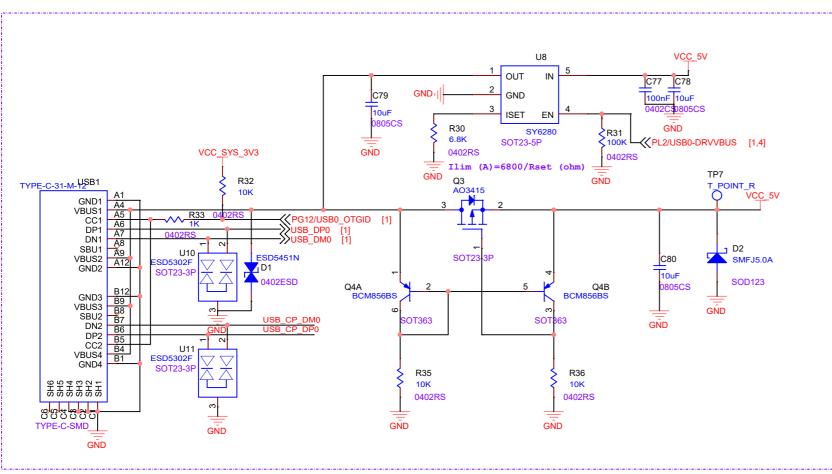


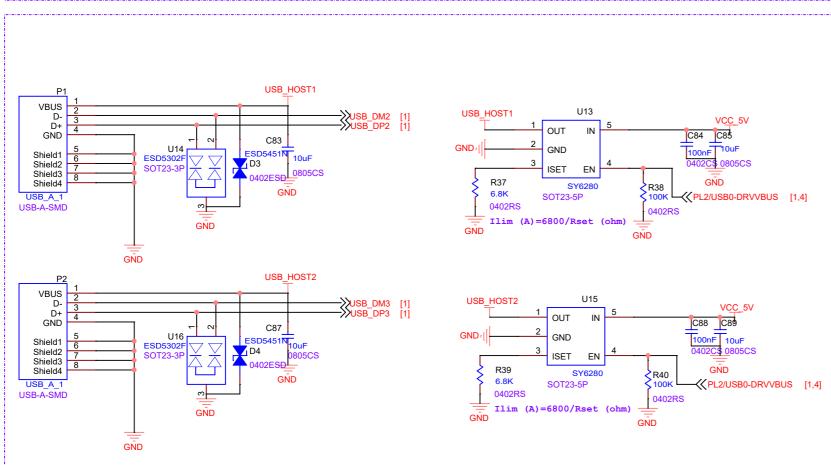


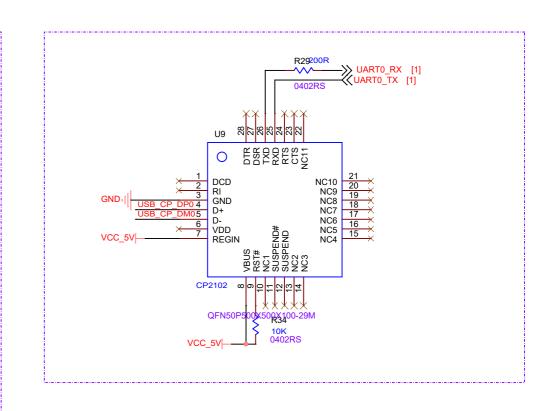
<Title>

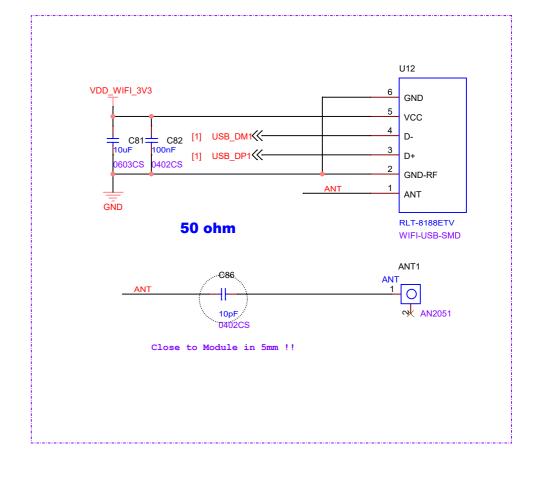
Document Numbe

USB-UART-WIFI



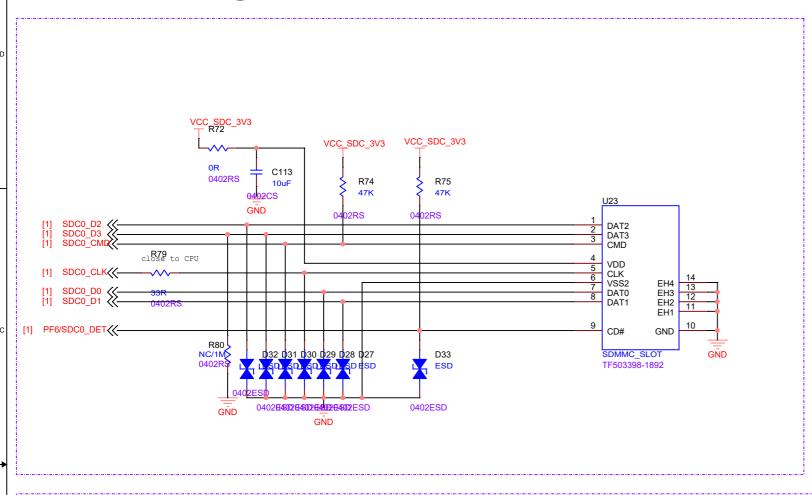


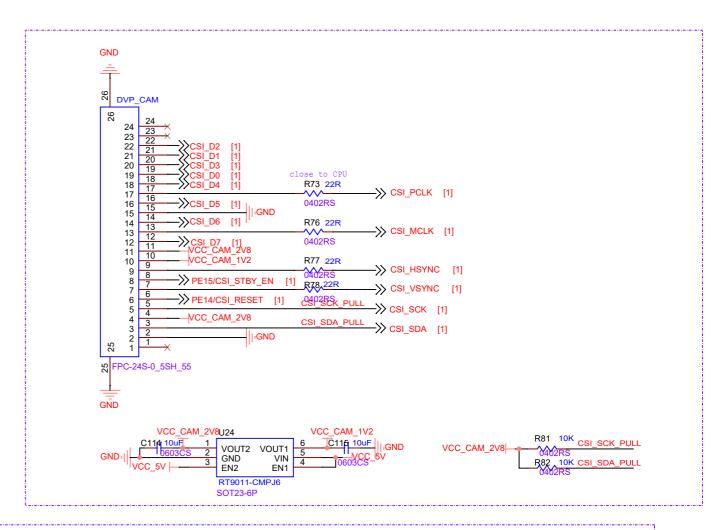




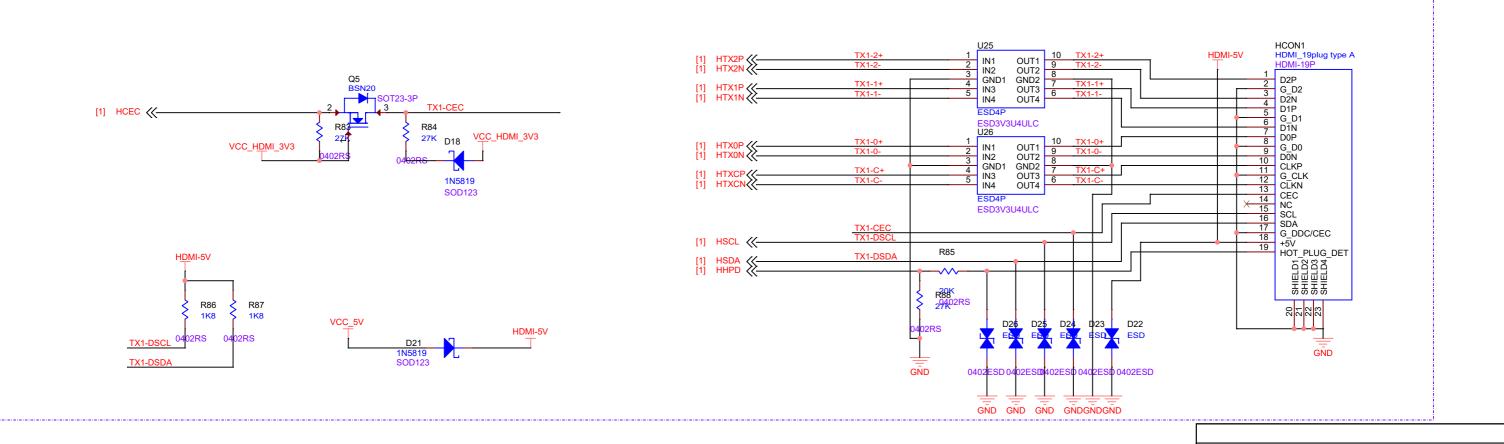
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TF-CAM-HDMI





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IMI-LCD-MIC-OTHERS

