Leadram + 11 Stepper Motor (Anti-clock wise) #Indode Cstdio.n> Hirolode Long Sl. hs char xdata port at oxe 803; charidata account - 0x800; delay () h for (j=0;j6800;j++) f} Yold main() { Port 20x80; cohile (i)? acc zexli; postaz ace; delag (); acc zox22; Porfa 2 acc; delay (); acc 20x44; Portazaco; delay (); acc 20x88; portazacci delay();