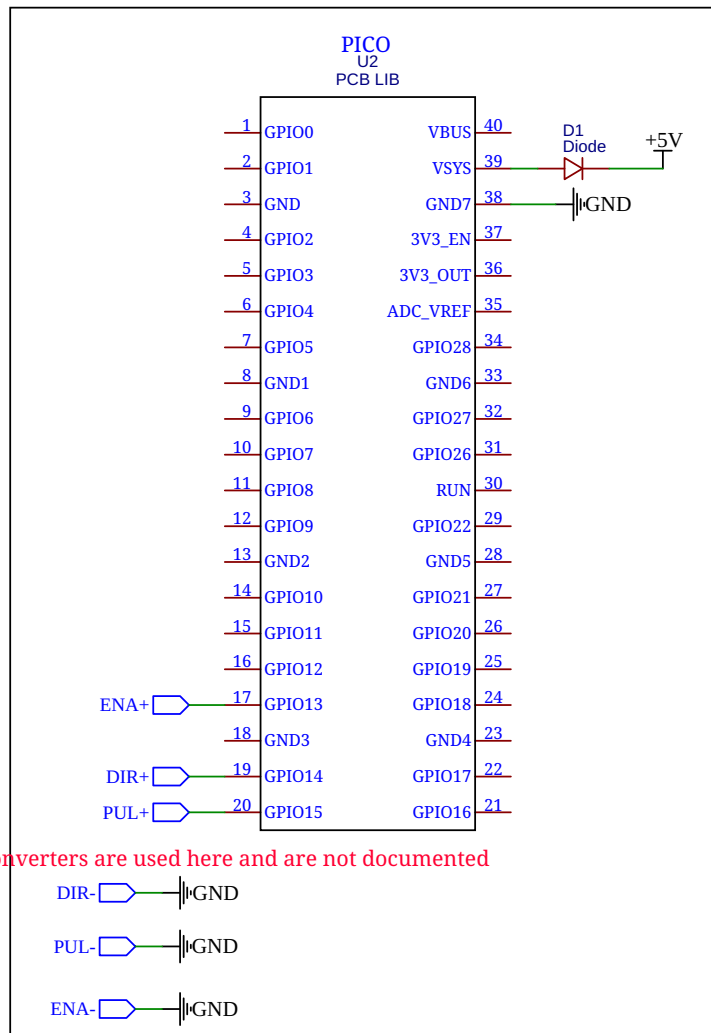


## Microcontroller



Note: Logic Level Converters are used here and are not documented

DIR- GND

PUL- GND

ENA- GND

TITLE: Clock		REV: 1.0
Company:		Sheet: 1/1
Date: 2024-10-20	Drawn By: Logan Mc	

