

2023 Digital IC Design Homework 2

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Functional Simulation Result			
Score		100	
<pre># # ----- # ---- Simulation finish, ALL PASS, Score = 100 ---- # ----- # ** Note: \$finish : D:/dic2023/HW2/file/tb.v(139) # Time: 77 us Iteration: 1 Instance: /testfixture # 1 # Break in Module testfixture at D:/dic2023/HW2/file/tb.v line 139</pre>			
Description of your design			
<p>Overall, I separate my code into 3 parts:</p> <ol style="list-style-type: none">1. Reserve the data that we get in the data bus.2. Start to calculate the result.3. If the calculate done, reset all the value of parameters. <p>I use three array in my program. First one reserves the saving data, which is the departing train order, second one reserves the arriving train non-decreasing order, the last one representing the train station.</p> <p>And my condition is, if the arriving train array is empty, and the departing train is still having the rest of train which can not depart, the result will be 0. On the other hand, if the arriving train is all gone and the departing train is also empty, the result will be 1.</p>			