



Instituto Politécnico Nacional Escuela Superior de Cómputo

Práctica 13 - Microinstrucciones

Unidad de aprendizaje: Arquitectura de Computadoras

Grupo: 3CV1

Alumno(a):
Ramos Diaz Enrique

Profesor(a): Vega García Nayeli

Práctica 13 Microinstrucciones

1. Obtenga la microinstrucción de cada una de las instrucciones del set y complete la siguiente tabla. Esta tabla puede modificarla a conveniencia

														ALU	OP					
Instrucción	SDMP	UP	DW	WPC	SR2	SWD	SEXT	SHE	DIR	WR	SOP1	SOP2	[3]	[2]	[1]	[0]	SDMD	WD	SR	LF
LI	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
LWI	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0
LW	0	0	0	0	0	1	1	0	0	1	0	1	0	0	1	1	0	0	0	1
SWI	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0
SW	0	0	0	0	1	0	1	0	0	0	0	1	0	0	1	1	0	1	0	1
ADD	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	1	0	0	1	1
SUB	0	0	0	0	0	1	0	0	0	1	0	0	0	1	1	1	0	0	1	1
ADDI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	0	1	1
SUBI	0	0	0	0	0	1	0	0	0	1	0	1	0	1	1	1	0	0	1	1
AND	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	1	1
OR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	1	1
XOR	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1	1
NAND	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	1	0	0	1	1
NOR	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	0	0	0	1	1
XNOR	0	0	0	0	0	1	0	0	0	1	0	0	0	1	1	0	0	0	1	1
NOT	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0	0	0	0	1	1
ANDI	0	0	0	0	0	1	1	0	0	1	0	1	0	0	0	0	0	0	1	1
ORI	0	0	0	0	0	1	1	0	0	1	0	1	0	0	0	1	0	0	1	1

													ALU	OP						
Instrucción	SDMP	UP	DW	WPC	SR2	SWD	SEXT	SHE	DIR	WR	SOP1	SOP2	[3]	[2]	[1]	[0]	SDMD	WD	SR	LF
XORI	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	0	1	1
NANDI	0	0	0	0	0	1	0	0	0	1	0	1	1	1	0	1	0	0	1	1
NORI	0	0	0	0	0	1	0	0	0	1	0	1	1	1	0	0	0	0	1	1
XNORI	0	0	0	0	0	1	0	0	0	1	0	1	0	1	1	0	0	0	1	1
SLL	0	0	0	0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0
SRL	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0
BEQI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BEQI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BNEI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BNEI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BLTI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BLTI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BLETI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BLETI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BGTI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BGTI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
BGETI (个)	0	0	0	0	1	0	0	0	0	0	0	0	0	1	1	1	0	0	0	1
BGETI (↓)	1	0	0	1	0	0	0	0	0	0	1	1	0	0	1	1	0	0	1	1
В	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CALL	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RET	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NOP	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0