Experiment 9:

AIM:-

To design and implement the two bit half adder using Logisim simulator.

TRUTH TABLE:-

Truth Table			
Input		Output	
A	B	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

S = A XOR B   C = A AND B .

OUTPUT: