

Experiment20-1's and 2's complement of 8 bits

FileResetAssemblerDebugHelp

Registers

A

F4

BC

1E

00

DE

00

00

HL

00

00

PSW

00

00

PC

42

0D

SP

FF

FF

Int-Reg

00

Flag

S

1

Z

0

AC

0

P

0

C

0

Decimal - Hex Conversion

Decimal

Hex

0

0

→To Hex

←To Dec

I/O Ports

0

−

+

00

Update Port Value

Memory

0

−

+

00

Update Memory

Load me at

1

LDA

3000

2

CMA

3

STA

3001

4

ADI

01

5

STA

3002

6

HLT

DataStackKeyPadMemoryI/O Ports

Start

3000

OK

Address (Hex)

Address

Data

0B88

3000

12

0B89

3001

243

0B8A

3002

244

0B8B

3003

0

0B8C

3004

0

0B8D

3005

0

0B8E

3006

0

0B8F

3007

0

0BC0

3008

0

0BC1

3009

0

0BC2

3010

0

0BC3

3011

0

Line No

Assembler Message

0

Program assembled successfully

Simulator: Idle