

ability to perform sequences of observable actions. Such a sequence of observable actions is obtained from a sequence of actions under abstraction from the internal action τ . If q can perform the sequence of actions $a \cdot \tau \cdot \tau \cdot b \cdot c \cdot \tau$ ($a, b, c \in L$), i.e., $q \xrightarrow{a \cdot \tau \cdot \tau \cdot b \cdot c \cdot \tau} q'$, then we write $q \xRightarrow{a \cdot b \cdot c} q'$ for the τ -abstracted sequence of observable actions. We say that q is able to perform the *trace* $a \cdot b \cdot c \in L^*$. These, and some other notations and properties are formally given in Definition 4.

Definition 4. Let $p = \langle Q, L, T, q_0 \rangle$ be a labelled transition system with $q, q' \in Q$, $a, a_i \in L$, and $\sigma \in L^*$.

$$\begin{array}{lll}
 q \xRightarrow{\epsilon} q' & \Leftrightarrow_{\text{def}} & q = q' \text{ or } q \xrightarrow{\tau \cdots \tau} q' \\
 q \xRightarrow{a} q' & \Leftrightarrow_{\text{def}} & \exists q_1, q_2 : q \xRightarrow{\epsilon} q_1 \xrightarrow{a} q_2 \xRightarrow{\epsilon} q' \\
 q \xRightarrow{a_1 \cdots a_n} q' & \Leftrightarrow_{\text{def}} & \exists q_0 \dots q_n : q = q_0 \xRightarrow{a_1} q_1 \xRightarrow{a_2} \dots \xRightarrow{a_n} q_n = q' \\
 q \xRightarrow{\sigma} & \Leftrightarrow_{\text{def}} & \exists q' : q \xRightarrow{\sigma} q' \\
 q \not\xRightarrow{\sigma} & \Leftrightarrow_{\text{def}} & \text{not } \exists q' : q \xRightarrow{\sigma} q'
 \end{array}$$

Example 2. In Figure 2:

$$u_0 \xRightarrow{\text{but} \cdot \text{liq} \cdot \text{but} \cdot \text{choc}} u_0, v_0 \xRightarrow{\text{but} \cdot \text{but} \cdot \text{but} \cdot \text{liq}} v_0, \text{ and } u_0 \not\xRightarrow{\text{but} \cdot \text{but}}.$$

In our reasoning about labelled transition systems we will not always distinguish between a transition system and its initial state. If $p = \langle Q, L, T, q_0 \rangle$, we will identify the process p with its initial state q_0 , and, e.g., we write $p \xRightarrow{\sigma}$ instead of $q_0 \xRightarrow{\sigma}$. With this in mind, we give some additional definitions and notations in Definition 5, which are exemplified in Example 3.

Definition 5. Let p be a (state of a) labelled transition system, and $\sigma \in L^*$.

1. $\text{init}(p) =_{\text{def}} \{ \mu \in L \cup \{\tau\} \mid p \xrightarrow{\mu} \}$
2. $\text{traces}(p) =_{\text{def}} \{ \sigma \in L^* \mid p \xRightarrow{\sigma} \}$
3. $p \text{ after } \sigma =_{\text{def}} \{ p' \mid p \xRightarrow{\sigma} p' \}$
4. $P \text{ after } \sigma =_{\text{def}} \bigcup \{ p \text{ after } \sigma \mid p \in P \}$, where P is a set of states.
5. $P \text{ refuses } A =_{\text{def}} \exists p \in P, \forall \mu \in A \cup \{\tau\} : p \not\xrightarrow{\mu}$,
where P and A are sets of states and labels, respectively.
6. $\text{der}(p) =_{\text{def}} \{ p' \mid \exists \sigma \in L^* : p \xRightarrow{\sigma} p' \}$
7. p has finite behaviour if there is a natural number n such that all traces in $\text{traces}(p)$ have length smaller than n .
8. p is finite state if the number of reachable states $\text{der}(p)$ is finite.
9. p is deterministic if, for all $\sigma \in L^*$, $p \text{ after } \sigma$ has at most one element.
If $\sigma \in \text{traces}(p)$, then $p \text{ after } \sigma$ may be overloaded to denote this element.
10. p is image finite if, for all $\sigma \in L^*$, $p \text{ after } \sigma$ is finite.
11. p is strongly converging if there is no state of p that can perform an infinite sequence of internal transitions.
12. $\mathcal{LTS}(L)$ is the class of all image finite and strongly converging labelled transition systems with labels in L .

if and only if all possible test runs lead to the verdict **pass**. This means that each test case must be executed several times in order to explore all possible non-deterministic behaviours of the implementation, and, moreover, that a particular fairness must be assumed on implementations, i.e., it is assumed that an implementation by re-execution of a test case shows all its possible non-deterministic behaviours with that test case.

Definition 16. Let $t \in \mathcal{TTS}(L_U, L_I)$ and $i \in \mathcal{IOTS}(L_I, L_U)$.

1. Running a test case t with an implementation i is expressed by the parallel operator $\parallel : \mathcal{TTS}(L_U, L_I) \times \mathcal{IOTS}(L_I, L_U) \rightarrow \mathcal{LTS}(L_I \cup L_U \cup \{\theta\})$ which is defined by the following inference rules:

$$\frac{i \xrightarrow{\tau} i'}{t \parallel i \xrightarrow{\tau} t \parallel i'} \quad \frac{t \xrightarrow{a} t', i \xrightarrow{a} i'}{t \parallel i \xrightarrow{a} t' \parallel i'} \quad a \in L_I \cup L_U \quad \frac{t \xrightarrow{\theta} t', i \xrightarrow{\delta} i'}{t \parallel i \xrightarrow{\theta} t' \parallel i'}$$

2. A test run of t with i is a trace of $t \parallel i$ leading to one of the states **pass** or **fail** of t :

$$\sigma \text{ is a test run of } t \text{ and } i \Leftrightarrow_{\text{def}} \exists i' : t \parallel i \xRightarrow{\sigma} \mathbf{pass} \parallel i' \text{ or } t \parallel i \xRightarrow{\sigma} \mathbf{fail} \parallel i'$$

3. Implementation i passes test case t if all test runs go to the **pass**-state of t :

$$i \text{ passes } t \Leftrightarrow_{\text{def}} \forall \sigma \in L_{\theta}^*, \forall i' : t \parallel i \not\xRightarrow{\sigma} \mathbf{fail} \parallel i'$$

4. An implementation i passes a test suite T if it passes all test cases in T :

$$i \text{ passes } T \Leftrightarrow_{\text{def}} \forall t \in T : i \text{ passes } t$$

If i does not pass the test suite, it fails: $i \text{ fails } T \Leftrightarrow_{\text{def}} \exists t \in T : i \text{ fails } t$.

Example 14. Consider the test cases in Figure 7 and the implementations in Figure 4. The only test run of t_1 with k_1 is $t_1 \parallel k_1 \xRightarrow{?but.!liq.\theta} \mathbf{pass} \parallel k'_1$, so k_1 **passes** t_1 .

For t_1 with k_2 there are two test runs:

$$t_1 \parallel k_2 \xRightarrow{?but.!liq.\theta} \mathbf{pass} \parallel k'_2, \text{ and } t_1 \parallel k_2 \xRightarrow{?but.!choc} \mathbf{fail} \parallel k''_2, \text{ so } k_2 \text{ fails } t_1.$$

Also k_3 **fails** t_1 : $t_1 \parallel k_3 \xRightarrow{?but.!liq.\theta} \mathbf{pass} \parallel k'_3$, but also $t_1 \parallel k_3 \xRightarrow{?but.\theta} \mathbf{fail} \parallel k''_3$.

When t_2 is applied to k_3 we get:

$$t_2 \parallel k_3 \xRightarrow{?but.!liq.?but.\theta} \mathbf{pass} \parallel k'_3, t_2 \parallel k_3 \xRightarrow{?but.\theta.?but.!choc} \mathbf{fail} \parallel k''_3, \text{ so } k_3 \text{ fails } t_2.$$

5.2 Test Generation

Now all ingredients are there to present an algorithm to generate test cases from a labelled transition system specification, which test implementations for **ioco**-correctness. To see how such test cases may be constructed, we consider the