TABLE I
AND GATE $Y = A \cdot B$

	_	_
Input1	Input2	Output
	1	
1		

TABLE II OR GATE



	Y = A + B	
	F	T
F	F	T
T	T	T

Input1	Input2	Output
	1	1
1		1
1	1	1

TABLE III (EXCLUSIVE)XOR GATE



$Y = A \oplus B$		
	F	T
F	F	T
T	T	F

Input1	Input2	Output
	1	1
1		1
1	1	

TABLE IV Inverter or NOT Gate



	TD.
F	T
T	F
1	1

Input1	Output
1	
	1