

- covered basics of DFA

 - L abstract domain + abstract semantics + abstract execution

(mop) (mfp)

L flow-sensitive

L intra-procedural

L no ptr info

- math behind DFA

 - L posetrian lattices

 - L monotone functions

 - L fixpoint theorems

- set constraint-based analysis

 - L inclusion constraints

 - L flow-insensitive

 - L constraint gen. & constraint solving

- andersen-style pointer analysis

- go back to DFA, but w/ ptr info

 - L reaching defs

 - L control analysis

} program

} slicing

- extend DFA w/ inter procedural analysis

 - L taint analysis

 - L context-sensitivity

- ...

L context-sensitivity

. if time:

- SSA & sparse analysis
- abstract interpretation

;

program slicing

problem: given a specific instruction I_1 ,
what set of instructions can influence
the execution I

L backwards slice

L also "forward slice" (set of instructions
that I can influence)

. built on

. reaching defs (w/ ptr info)

. control analysis



program dependence graph

(PDG)



slicing

reaching defs w/ ptr info

Reaching def's w/ ptr info

assume : flow-insensitive, inter procedural
pointer analysis soln

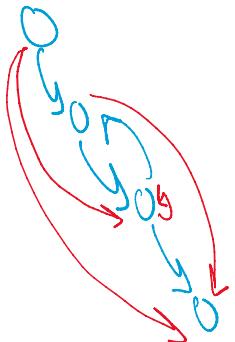
add mod/ref info

\uparrow
modified \uparrow
referenced

① compute call-graph

- node for each function

- edge $A \rightarrow B$ if function A calls function B



② compute transitive closure of call graph

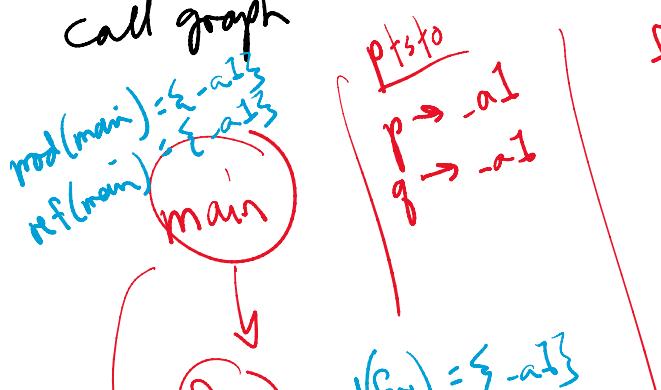
③ initialize mod/ref info:

for each function F, compute

a) the set of globals \in pointed-to objects
that F can define (e.g. \$store) } $\text{mods}(F)$

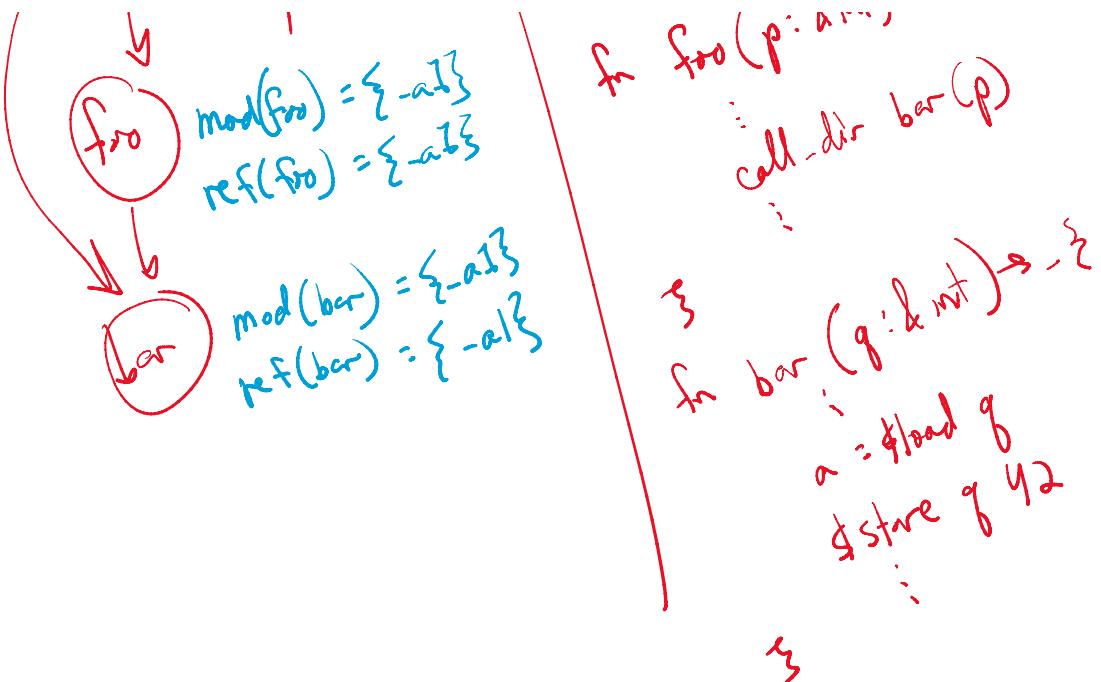
b) the set of globals \in pointed-to objects
that F can use (e.g. \$load) } $\text{refs}(F)$

④ propagate the initial mod/ref info
backwards in the transitively closed
call graph



for $\text{main}() \rightarrow \text{int} \{$
 $p := \$alloc 1 \{ -a \}$
call-dir $\text{foo}(p)$
:
:

for $\text{foo}(p: \text{list}) \rightarrow \{$
 $\text{...} \}_{\text{var}(p)}$



improved reaching defs

- no need for fake variables
- all instructions stay the same except

L $x = \$load\ y$

USE = $\{y\} \cup \text{pts-to}(y)$

L $\$store\ X \langle op \rangle$

DEF = $\text{pts-to}(X)$

$p := \$address\ a$
 $\text{fcall-dir foo}(p)$

L $[x :=] \$call - \{\text{dir}, \text{idr}\} \langle \text{id} \rangle / \text{fp} (\text{args} \dots)$ then bb
 $\cdot \text{r}_1, \text{FFS} = (\{\text{id}\} \quad \text{if } \$call-\text{dir}$

$\$call_dir \text{ foo}(p)$
 \vdots
 $g = \$addr\& b$
 $\$call_dir \text{ foo}(q)$

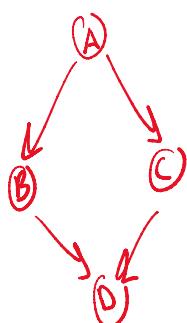
$\text{fun } \text{foo}(r: \text{int}) \{$
 \vdots
 $\$store r \#2$
 $\}$

- . CALLEES = $\begin{cases} \{id\} & \text{if } \$call_dir \\ \text{pts-to}(fp) & \text{if } \$call_idr \end{cases}$
- . REACHABLE = globals + all objects reachable from globals or arguments (use pts-to sol'n)
- . WDEF = $\left(\bigcup_{c \in \text{CALLEES}} \text{mod}(c) \right) \cap \text{REACHABLE}$
- . USE = $\{fp\} \cup \{arg \mid arg \text{ is a variable}\} \cup \left(\left(\bigcup_{c \in \text{CALLEES}} \text{ref}(c) \right) \cap \text{REACHABLE} \right)$

control dependence

block X is control-dependent on block Y iff
Y is in post-dominance frontier of X

↑
reverse the CFG



PDG

- . a node for each instruction

- a node for each instruction
- a data dependence edge $A \rightarrow B$ if
the def at A reaches a use at B
- a control dependence edge $A \rightarrow B$ if
B is control dependent on A

L if block X is control dependent on block Y,
then all instructions in X are control
dependent on the terminal of Y

