

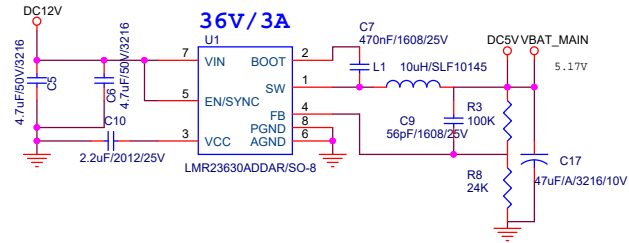
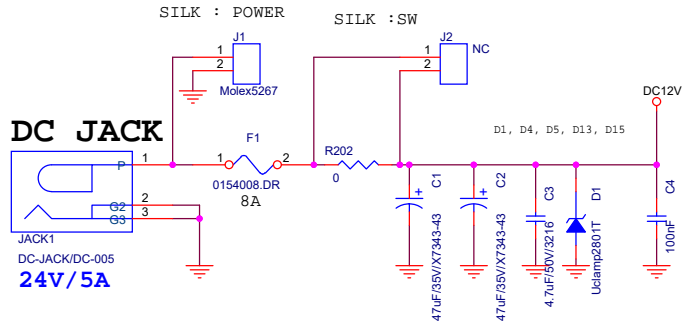
ACCELEROMETER 4CH MCDL BOARD REV2.0

Revision	Date	Description
1.0	2018.10.30	First Version (4CH_DAQ_A8K01AEF1)
2.0	2019.09.04	Added DSP & Board name was changed from DAQ to MCDL MCDL(Multi-Channel Data Logger)

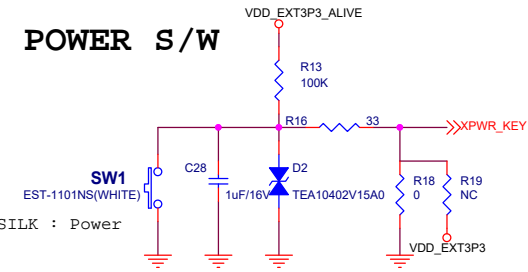
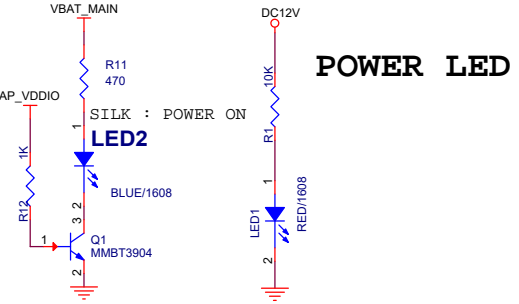
Table of Contents		Abbreviation List	
Page	Description	Page	Description
01:	Title		
02:	Main Power		
03:	ARTIK module LGA pad		
04:	DSP_TMS320F28379DPTPS		
05:	ETHERNET-RTL8211E-VB-CG		
06:	DUAL USB2.O CONN		
07:	USB_HSIC Hub		
08:	SD/MMC Card		
09:	Boot Mode Sel_RTC		
10:	USB to UART for debugging		
11:	External LDO		
12:	ADC_ADS1274		
13:	SENSOR AMP		
14:	Mini PCIe for LTE-Removed circuit		
15:	JTAG		
16:	Revision History		

This schematic was designed using standard symbol.

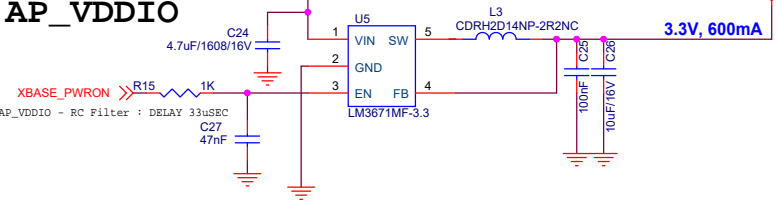
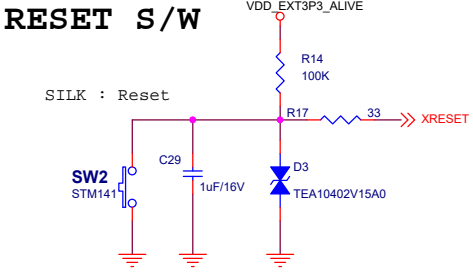
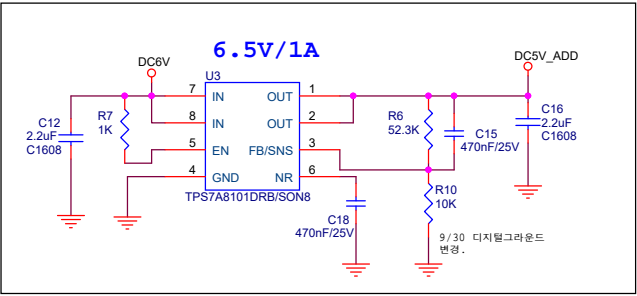
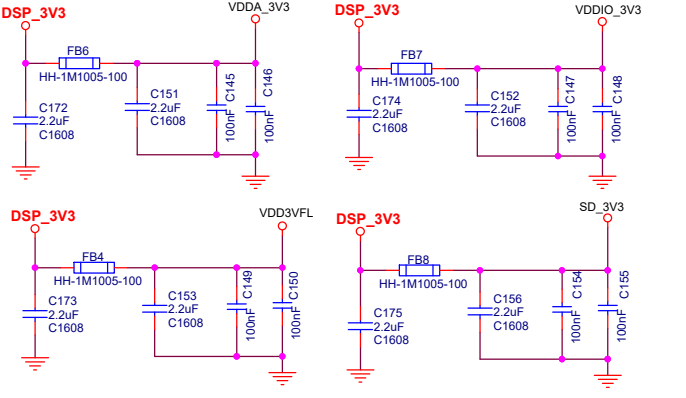
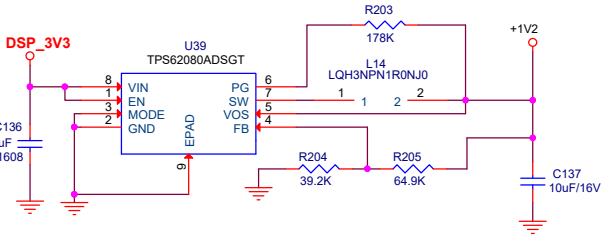
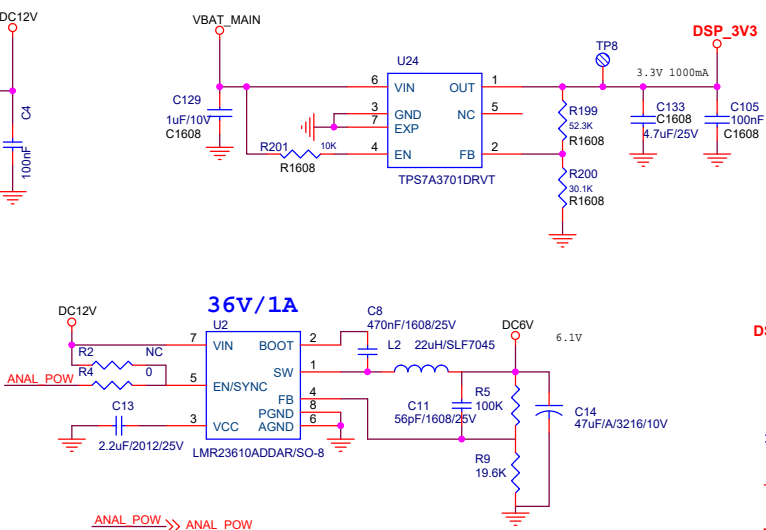
Power MAIN POWER



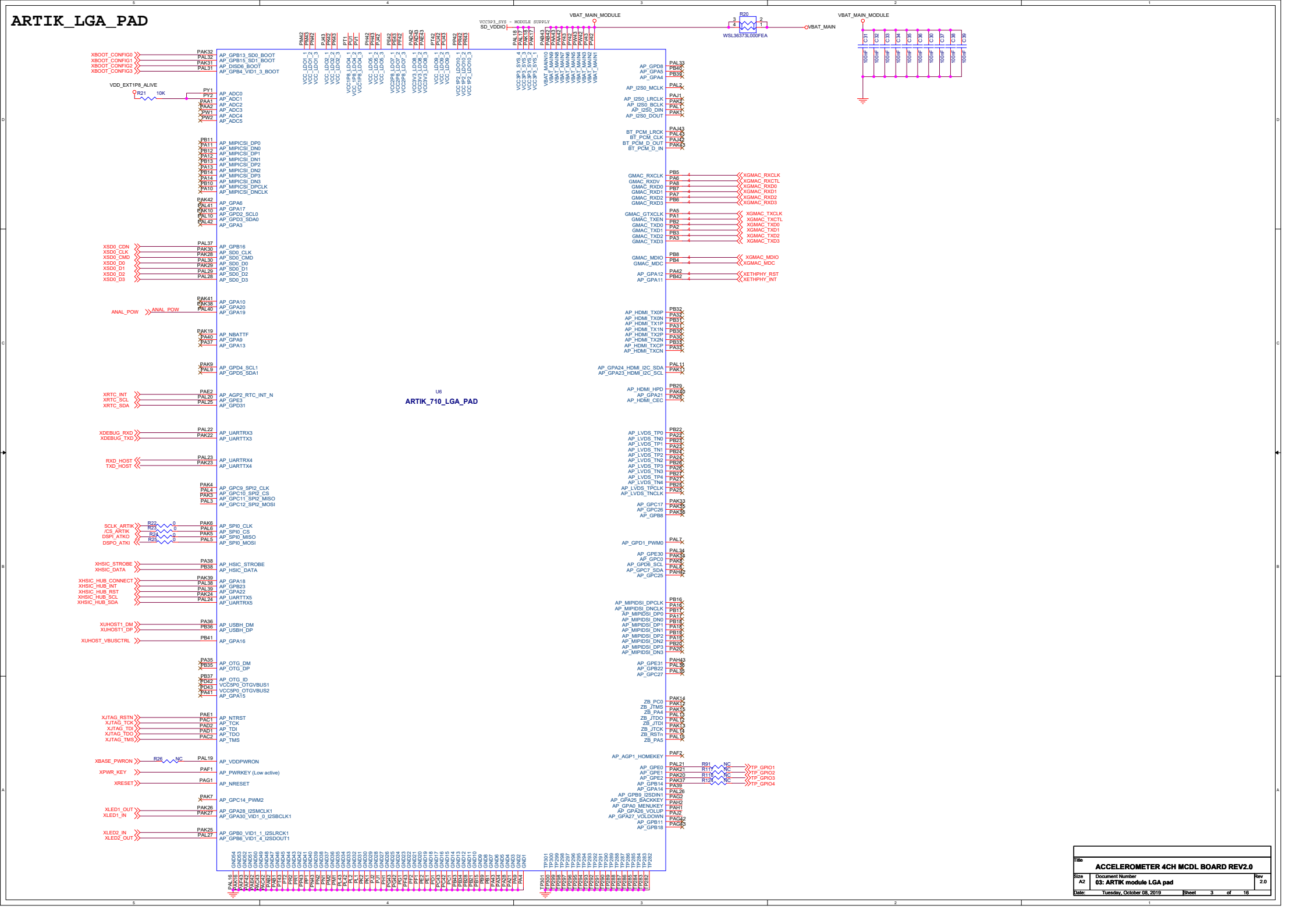
Net Name was changed from DC24V to DC12V



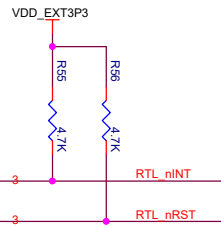
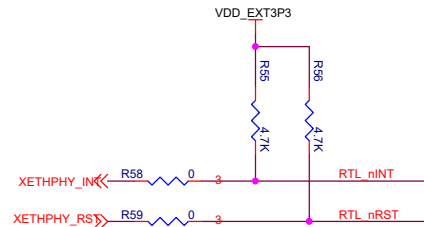
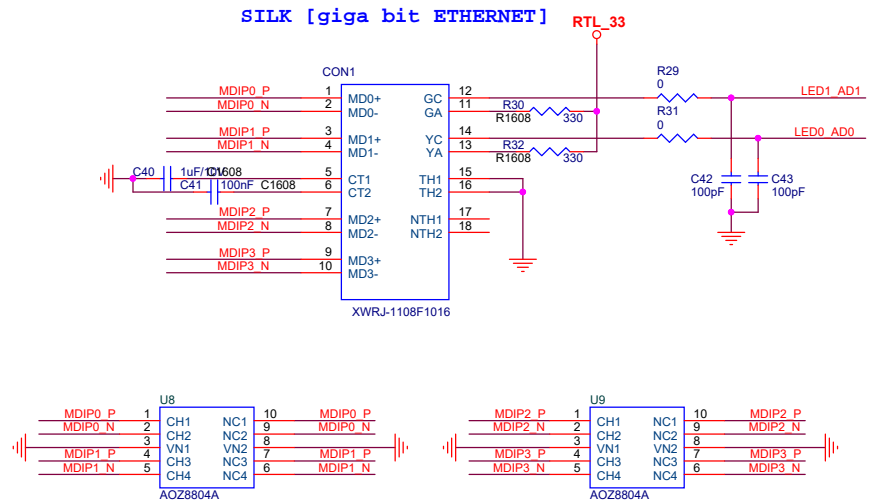
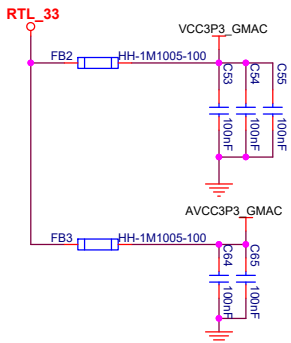
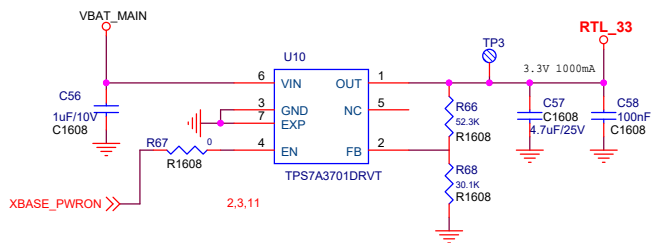
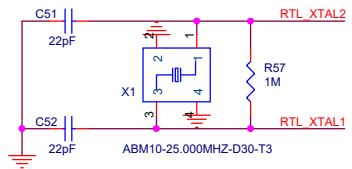
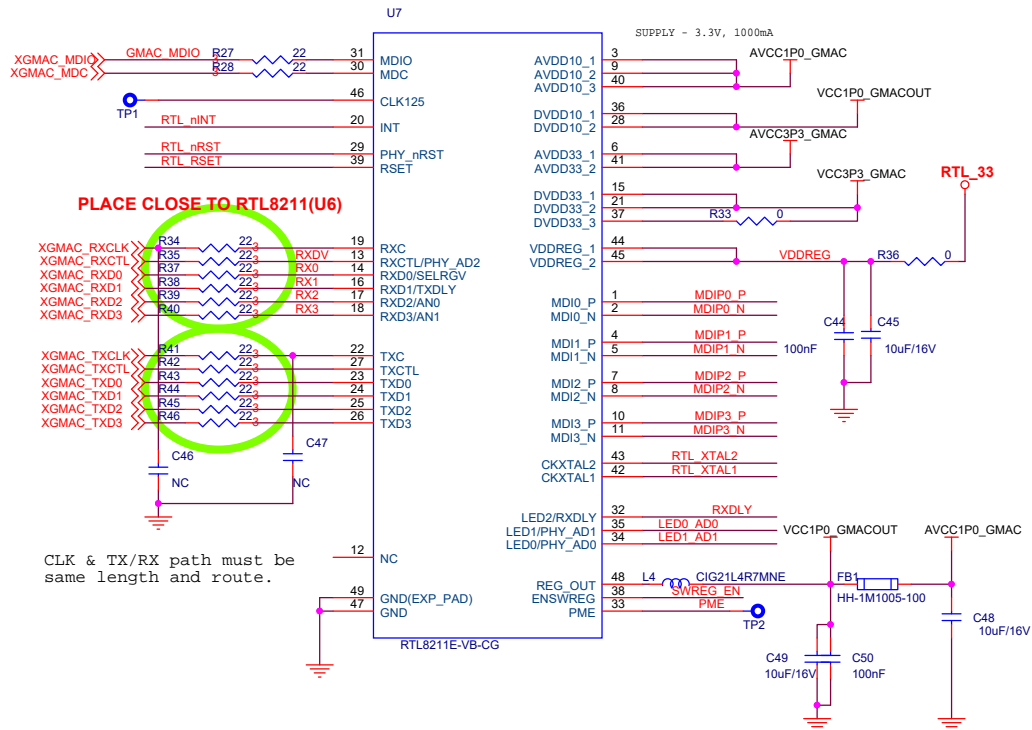
DSP Main block Power supply



Title		
ACCELEROMETER 4CH MCDL BOARD REV2.0		
Size	Document Number	Rev
A3	02: MAIN POWER	2.0
Date:	Friday, December 06, 2019	Sheet 2 of 16

[illegible]

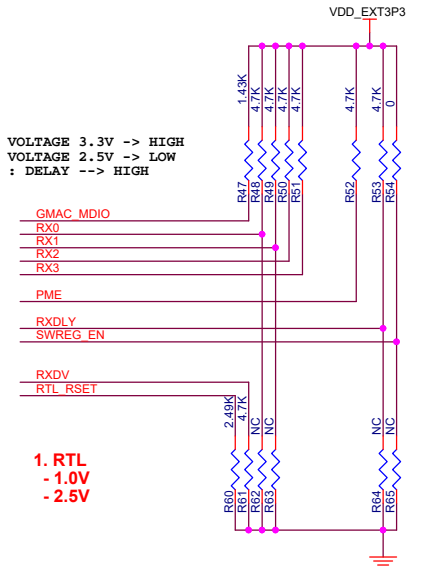
RGMII ETHERNET PHY



```

RX0 : I/O VOLTAGE 3.3V -> HIGH
      I/O VOLTAGE 2.5V -> LOW
RXDLY,RX1 : DELAY --> HIGH

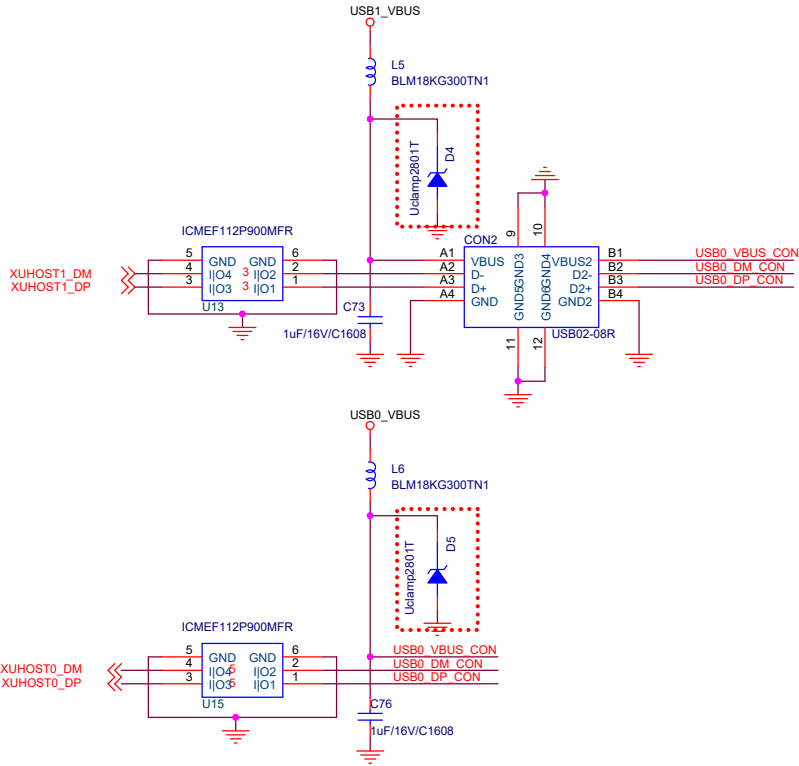
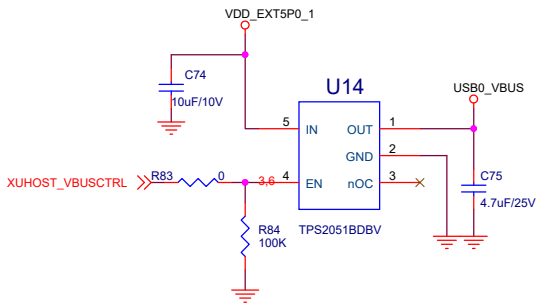
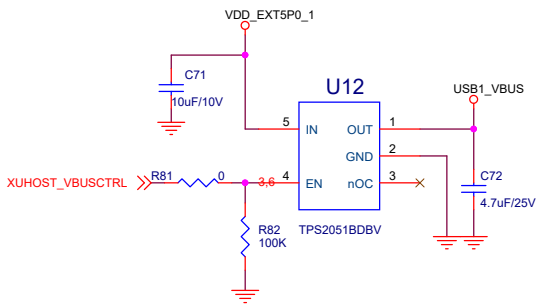
```



1. RTL
- 1.0V
- 2.5V

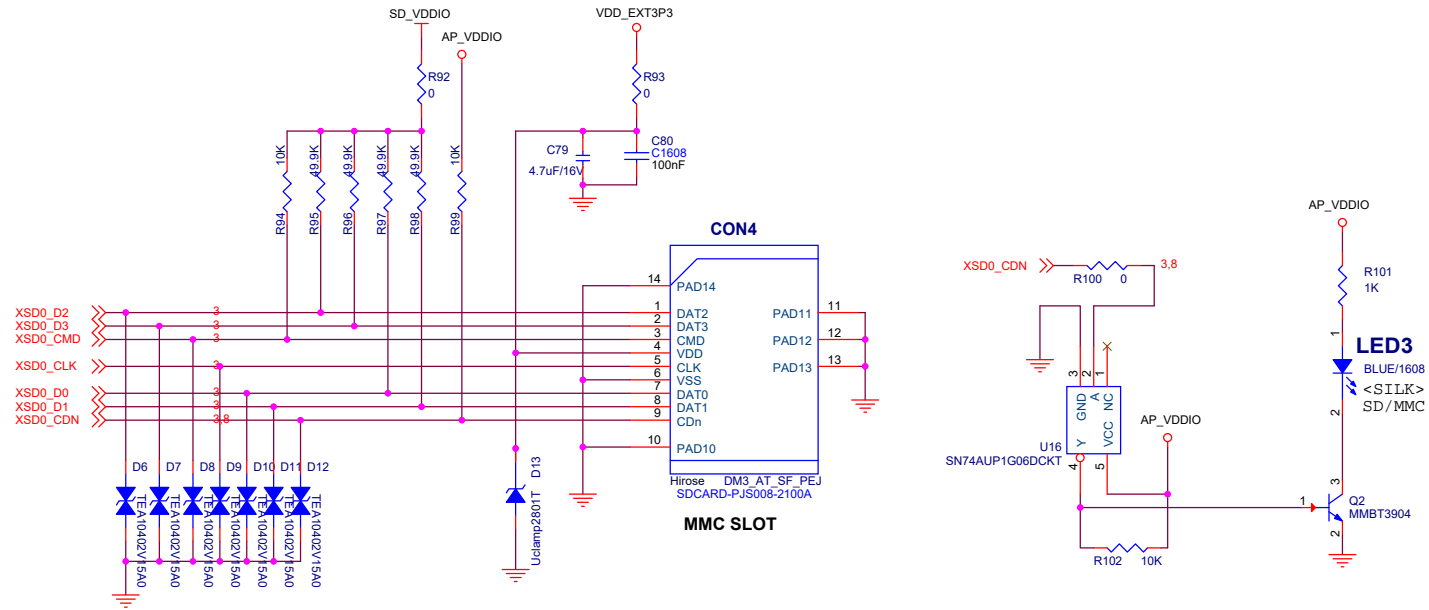
Title			
ACCELEROMETER 4CH MCDL BOARD REV2.0			
Size A3	Document Number 04: ETHERNET-RTL8211E-VB-CG		Rev 2.0
Date:	Tuesday, October 08, 2019	Sheet	5 of 16

USB2.0 connector





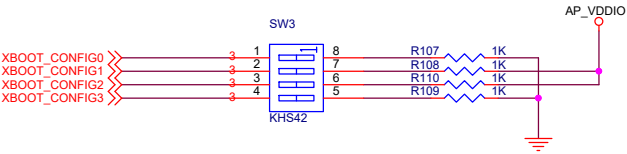
SD/MMC



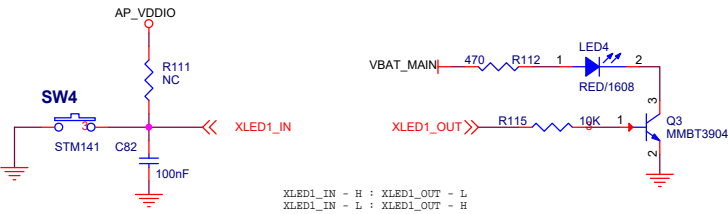
Title			
ACCELEROMETER 4CH MCDL BOARD REV2.0			
Size A3	Document Number 08: SD/MMC Card		Rev 2.0
Date:	Tuesday, October 08, 2019	Sheet	8 of 16

BOOT MODE SEL

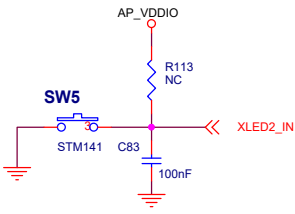
SW3	Comment	eMMC 1st Boot	SD 1st Boot	USB 1st Boot
1	-	Off	Off	On
2	-	Off	Off	On
3	HW_Rev>1.0	X	X	X
4	-	Off	On	X



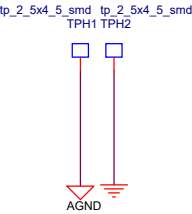
XLED TEST



XLED1_IN ~ H : XLED1_OUT ~ L
XLED1_IN ~ L : XLED1_OUT ~ H



XLED2_IN ~ H : XLED2_OUT ~ L
XLED2_IN ~ L : XLED2_OUT ~ H

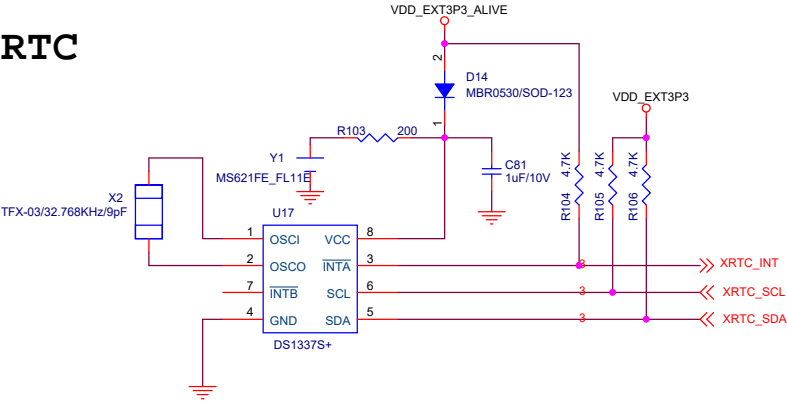


GND TP HOLE

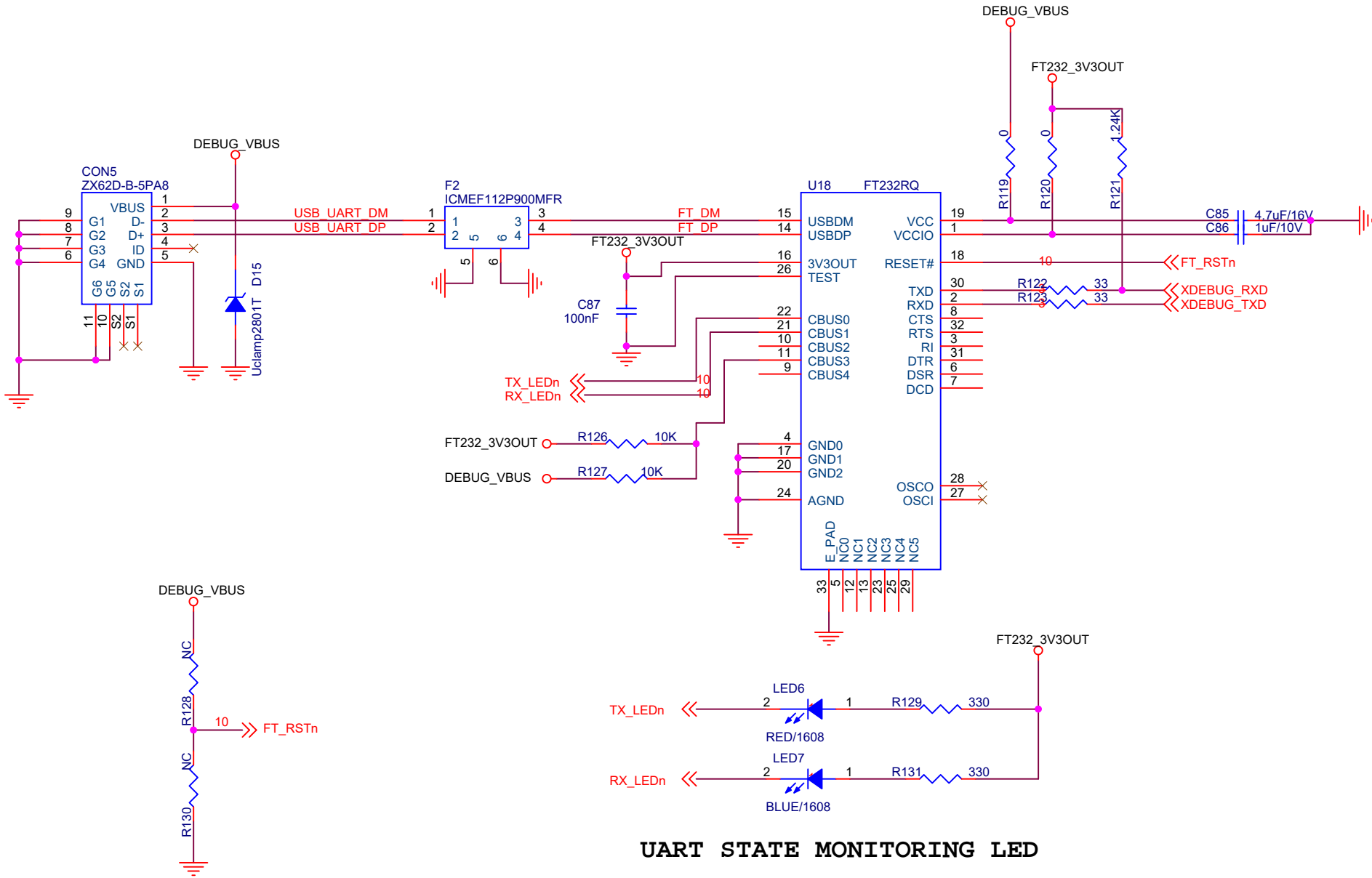


Mount Hole (Φ3.2)

RTC



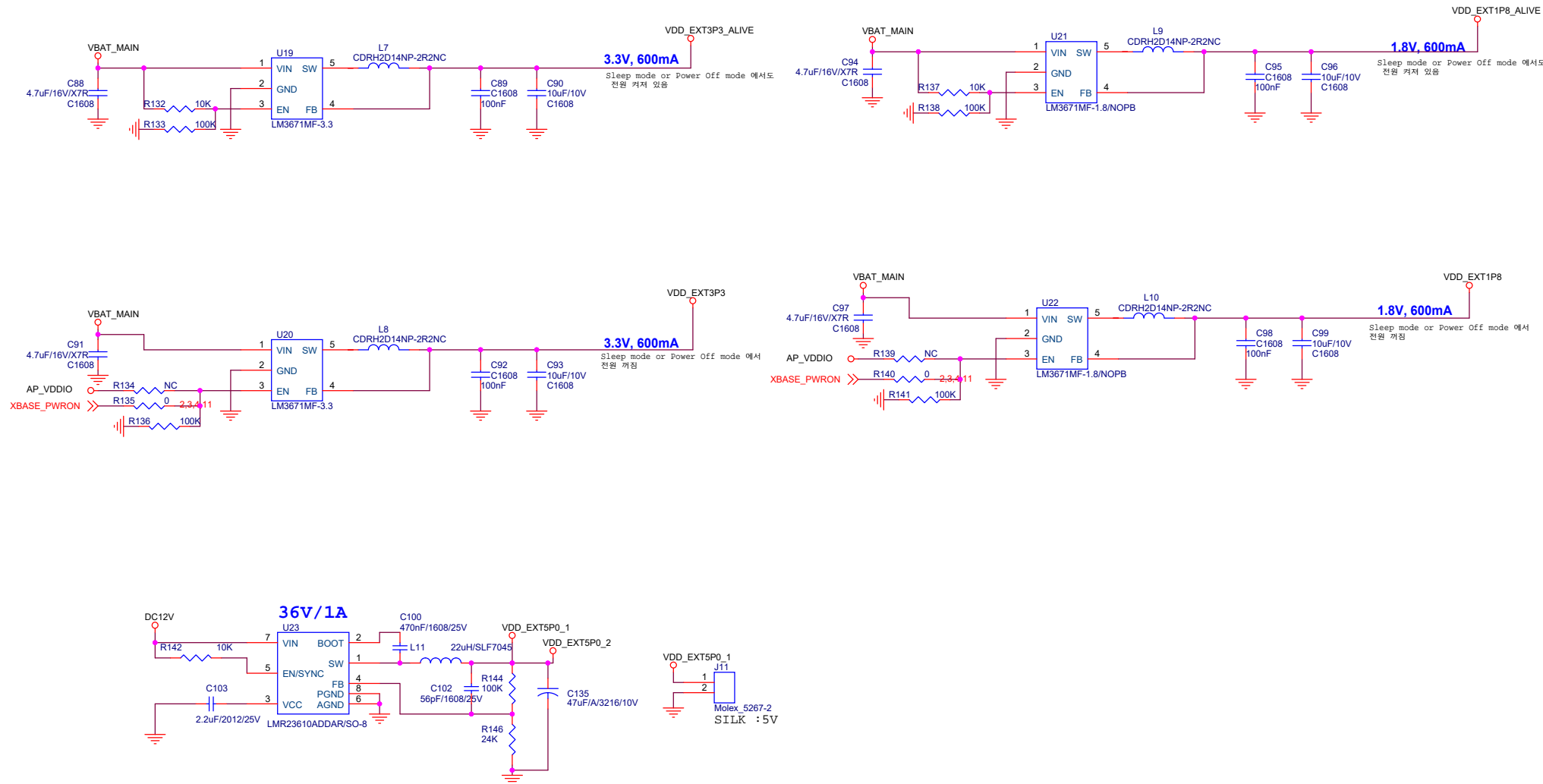
USB UART



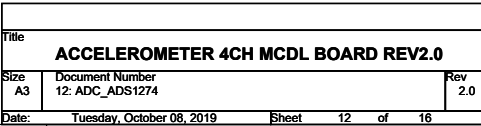
UART STATE MONITORING LED

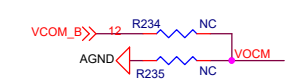
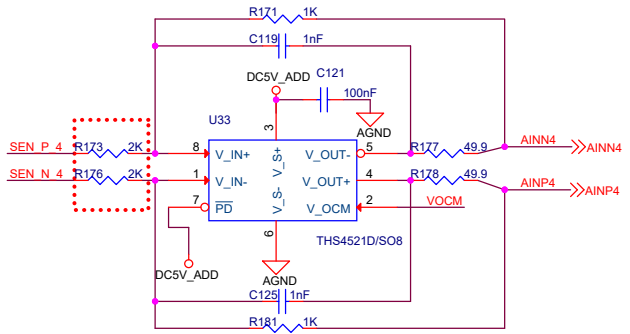
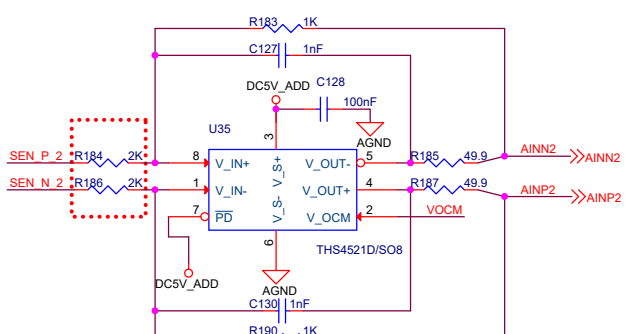
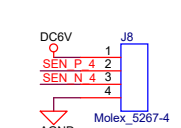
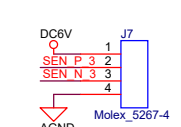
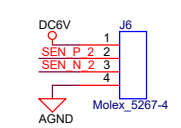
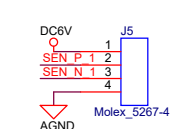
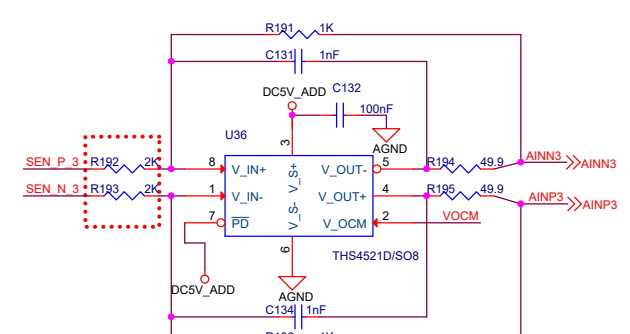
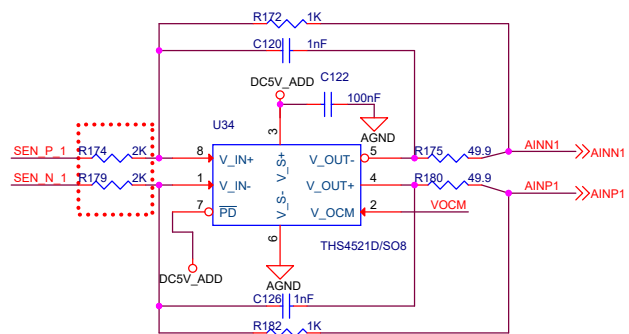
Title		
ACCELEROMETER 4CH MCDL BOARD REV2.0		
Size	Document Number	Rev
A4	10: USB to UART for Debug	2.0
Date:	Tuesday, October 08, 2019	Sheet 10 of 16

EXT LDO



FORMAT	INTERFACE	DOUT	POSITION
000	SPI	TDM	DYNAMIC
001	SPI	TDM	FIXED
010	SPI	discrete	---
011	FrameSync	TDM	DYNAMIC
100	FrameSync	TDM	Fixed
101	FrameSync	discrete	---
110	Modulator	---	---





1K was changed to 2K for adjusting the signal level from +/-5V to +/-2.5V

Title		
ACCELEROMETER 4CH MCXL BOARD REV2.0		
Size	Document Number	Rev
A3	13. SENSOR AMP	2.0
Date:	Tuesday, October 08, 2019	Sheet 13 of 16

REMOVED MINI PCIE BLOCK DUE TO NOT USE IT
20.Sep.2019

Title		
ACCELEROMETER 4CH MCDL BOARD REV2.0		
Size	Document Number	Rev
A4	14: DaughterBoard_LTE_Removed	2.0
Date:	Tuesday, October 08, 2019	Sheet 14 of 16

ARTIK JTAG

