

6.27

A、

$t = 0x45 = 0b01000101$, $s = 0b001$, $b = ??$ 故内存地址范围为 $0x08A4 \sim 0x08A7$

$t = 0x38 = 0b00111000$, $s = 0b001$, $b = ??$ 故内存地址范围为 $0x0704 \sim 0x0707$
 $0x08A4 \sim 0x08A7$; $0x0704 \sim 0x0707$

B、

$t = 0x91 = 0b10010001$, $s = 0b110$, $b = ??$ 故内存地址范围为 $0x1238 \sim 0x123B$