

# Required opcodes

## Required op-codes and addressing modes

Below are the list of instructions and addressing modes assigned for this project.

Effective Addressing Modes:

1. Data Register Direct
2. Address Register Direct
3. Address Register Indirect
4. Immediate Data
5. Address Register Indirect with Post incrementing
6. Address Register Indirect with Pre decrementing
7. Absolute Long Address
8. Absolute Word Address

Autumn 2020 Requirement:

1. NOP
2. MOVE
3. MOVEM
4. ADD
5. SUB
6. MULS
7. DIVU
8. LEA
9. AND
10. NOT
11. LSL
12. LSR
13. ASL
14. ASR
15. Bcc (BLT, BGE, BEQ)
16. JSR
17. RTS
18. BRA