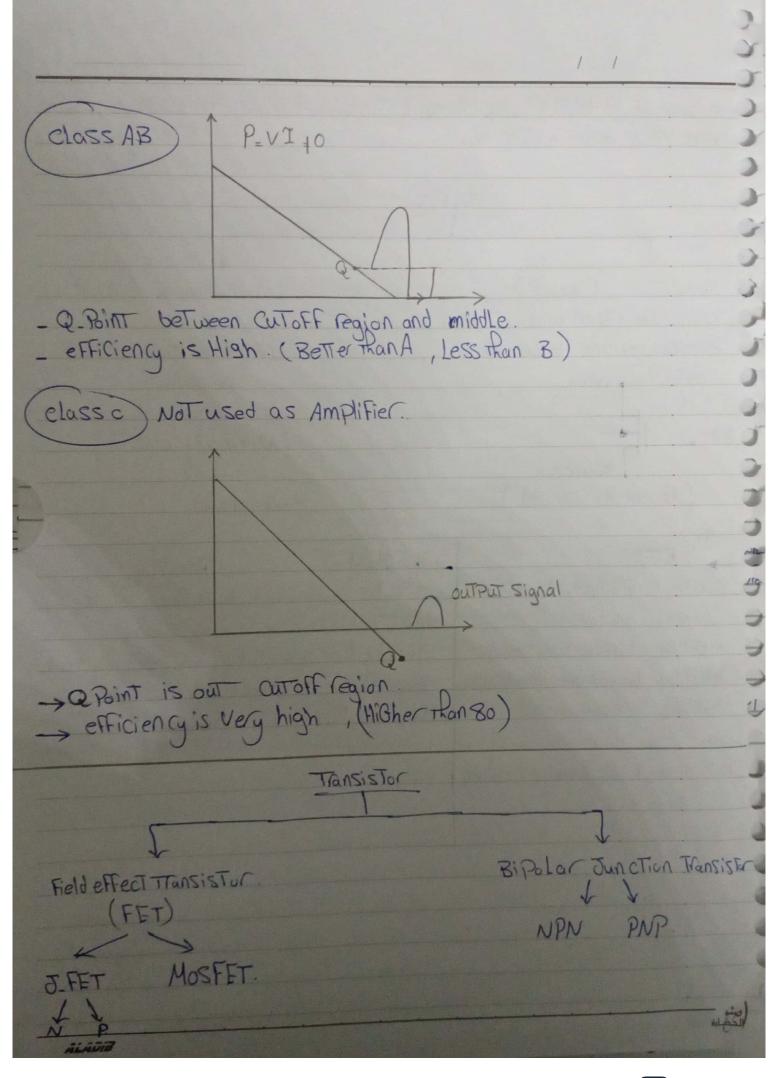
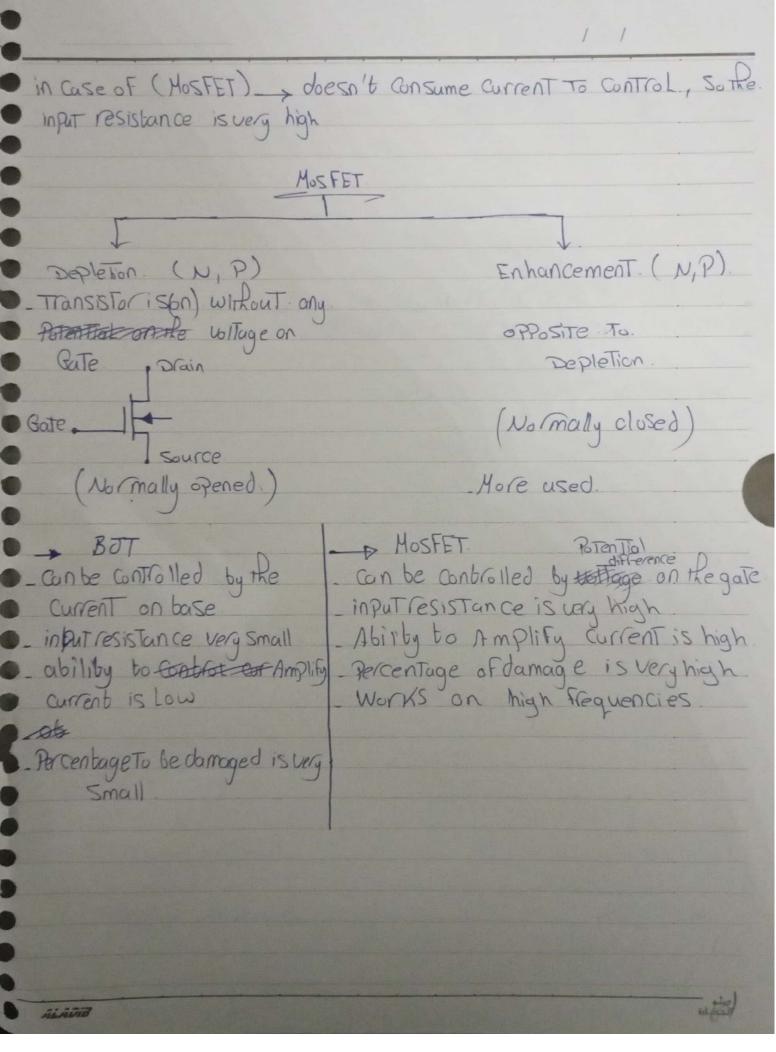
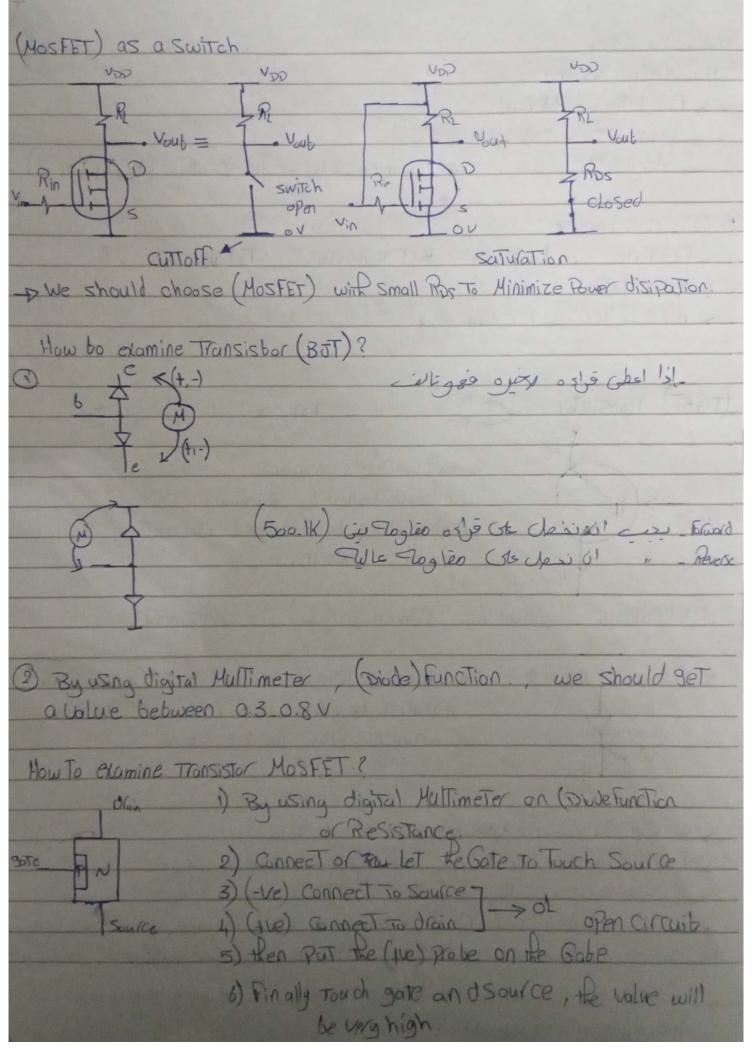


		/ /
Common Emilber	Common Collector	a mmon base.
internal Impedence	High	X Low
impedence	Low.	X High
Amplified ** ** ** ** ** ** ** ** ** ** ** ** **	Same.	(NoTused)
*Amplifier classes: (class A) Q-Point at middle. Consumes DC current, Low (25) (class B) P=VI=0 P=VI=0 Consumes current only when a liable High efficiency (70-80)	efficiency. 300% TIME A Signalis Signalis	OUTPUT. OUTPUT. OUTPUT. AAA.







Transistor Coding DATO election scheme FIST LETTER - MOTEGIAL second Letter = Type and uses > Herd letter - Can be used. Commercially and industrially (W, X, Yor Z) JEDEC Scheme 1 = Diode BOT Transistor = 2 FET Transistor = 3. - BUST DO: > Second Letter (clulus voj z s third Letter (IGBT) Transistor ._ FET, BOT II Wis Ou To DIENT CONSUMES CULTENT. can be controlled by Potential d. High ability to with tand current. the most expensive APPlications sinverters, (3Phose of so), Power Supply Paristor (its Function like the piode but it differs, that Anode A * it must be given a pulse on its gate to Pass current from Anote to Cathode , we can control the same thyristor through gate if we give the gate any utitinge, the thyristic will be on and act as a diade > IF we remove the latinge on Gate the thyristor Emaisson without anythings > we can change it to (OFF) if the VolTage on Arode become (-ve) or removing the source of by making short arount be Tween Anoth and Cathode > if the current Passing through try listor reaches "minimum holding current" # Hyristor will be OFF

Diac : Flement which is similar to diode I Threeds more voltage to Passife current. Whas no (anode and cut
* How to Examine diac.
BUSING DAM to the Blos of Diac and change the Blos out to take the
By using DHM to the Poles of Diac and change the Poles are to take the readings in both Cases and they must be equal
The state of the s
Applications, used to control loads working on Accurrents
APPlications, used to control books working on Ac Currents
Disadvantage of Traic
So, to Solve this Problem we use a diac to aloid this problem
So, to Solve this Problem we use a diac to a loid this Problem
Also there is another element called (Quadrac) which Consists
of aldiac and a Traic) Together My
G(A)
How To examine Triac?
Put Positive Probe on the Gate and MT1 and the negative Probe on
MTD, softe Resistance will be very Small value
VolTage Regulator
(xx) signtify the input and output white
in Gnd out.
Vine TvolTage Reg. Vocat
ov- l ov
(Voltage Regulators), decreases its Temperature at Certain Point to
Notect Uself from Monthlogyang Camaging