## Vending Machine in Verilog & VHDL

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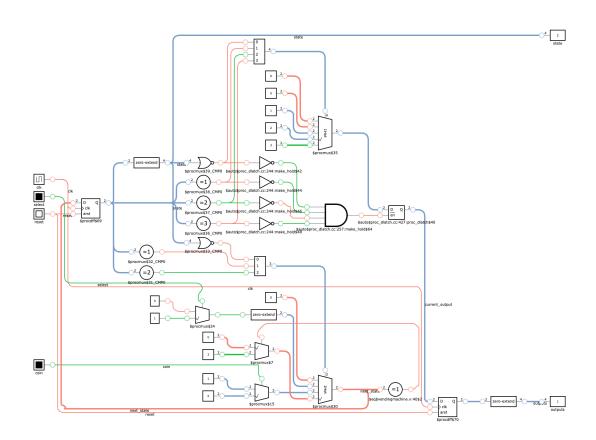


Figure a: The Vending Machine Simulation Output on DigitalJS

This project implements a simple digital logic vending machine using Verilog and DigitalJS. The vending machine has four states, which are defined as parameters in the code: S\_IDLE, S\_COLLECTING, S\_DISPENSING, and S\_CHANGE. The vending machine's state and output are controlled by two reg variables: next\_state and current\_output.

The logic for the vending machine is implemented in an always block that uses a case statement to determine what state the vending machine is in and what output should be displayed on the

vending machine's display. The vending machine transitions between states based on the inputs coin and select. If the vending machine is in the S\_IDLE state and the customer selects a product by pressing the select button, the vending machine transitions to the S\_COLLECTING state. If the customer inserts enough coins to pay for the product, the vending machine transitions to the S\_DISPENSING state and dispenses the product. If the vending machine is in the S\_DISPENSING state, it transitions to the S\_CHANGE state if the next state is S\_COLLECTING, which means the customer has paid more than the price of the product and is owed change. Finally, if the vending machine is in the S\_CHANGE state, it transitions back to the S\_IDLE state.

The vending machine's state and output are assigned in a second always block that sets the vending machine's initial state and output to S\_IDLE and 0, respectively, when the reset signal is high. Otherwise, the block updates the vending machine's state and output based on the values of next state and current output.

This vending machine project could be useful for learning digital logic, Verilog, and DigitalJS. It could also be extended to include additional features, such as a product inventory, a coin dispenser, or a user interface with buttons and LEDs.

