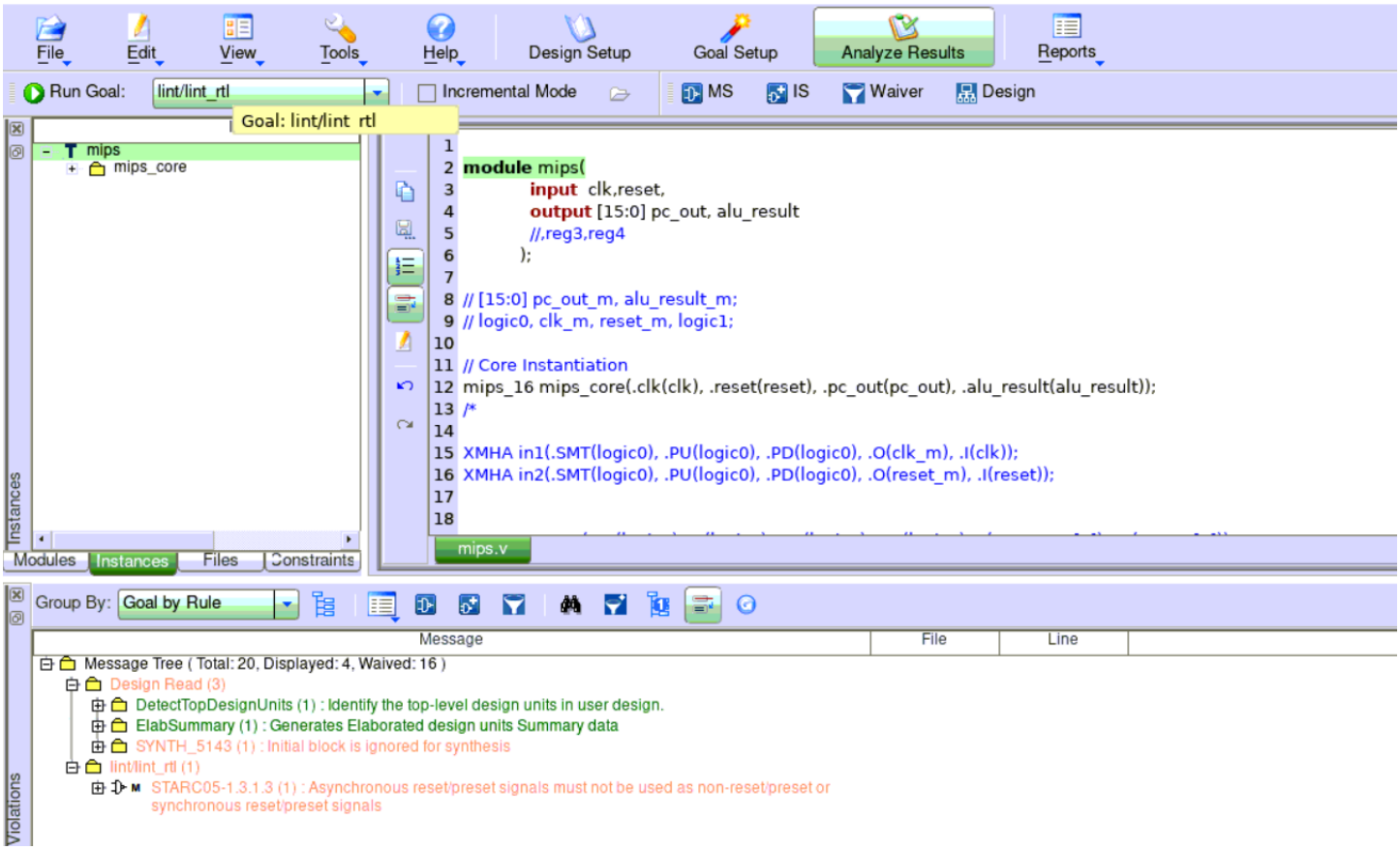
ITI Summer Training

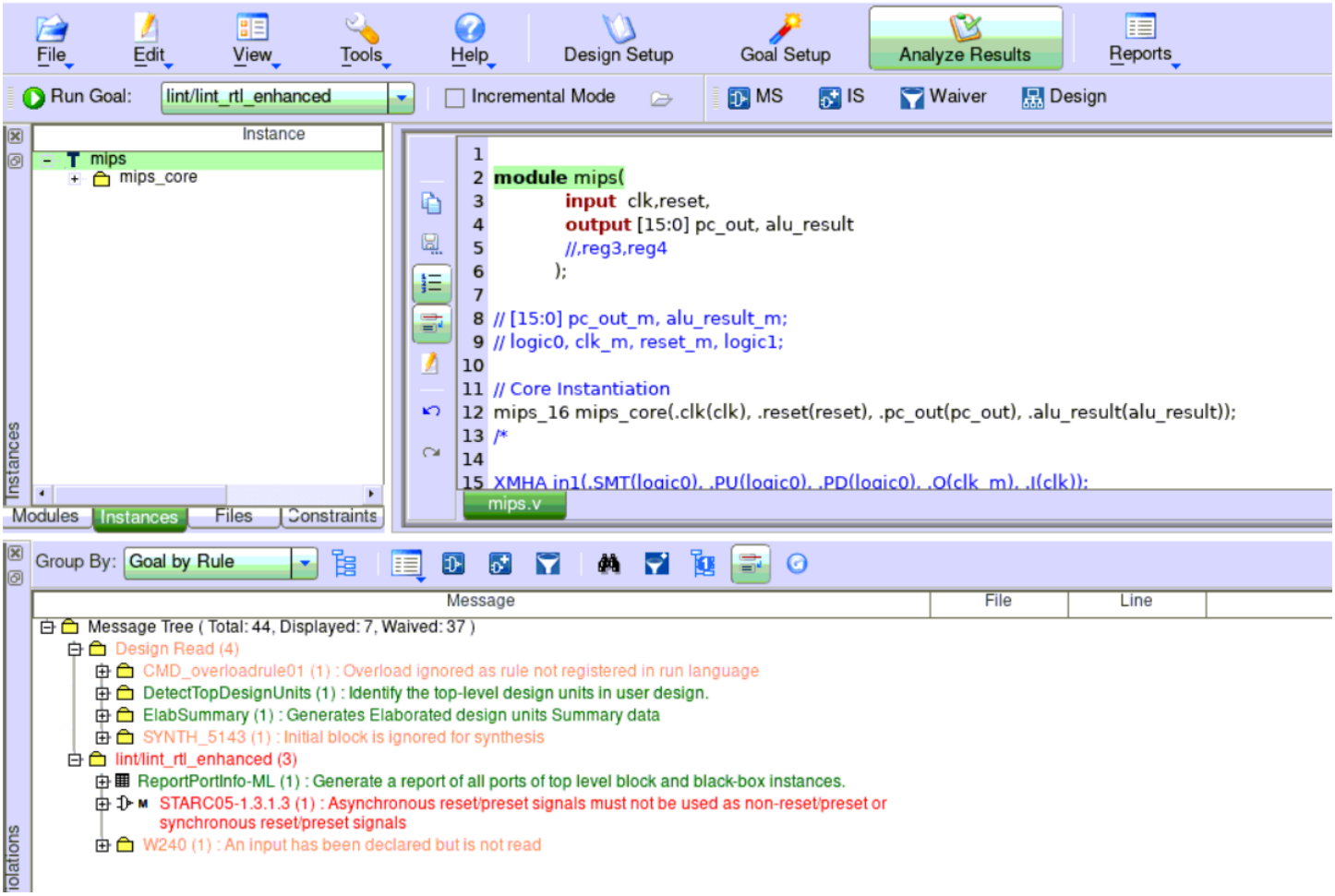
LAB Spyglass

Results

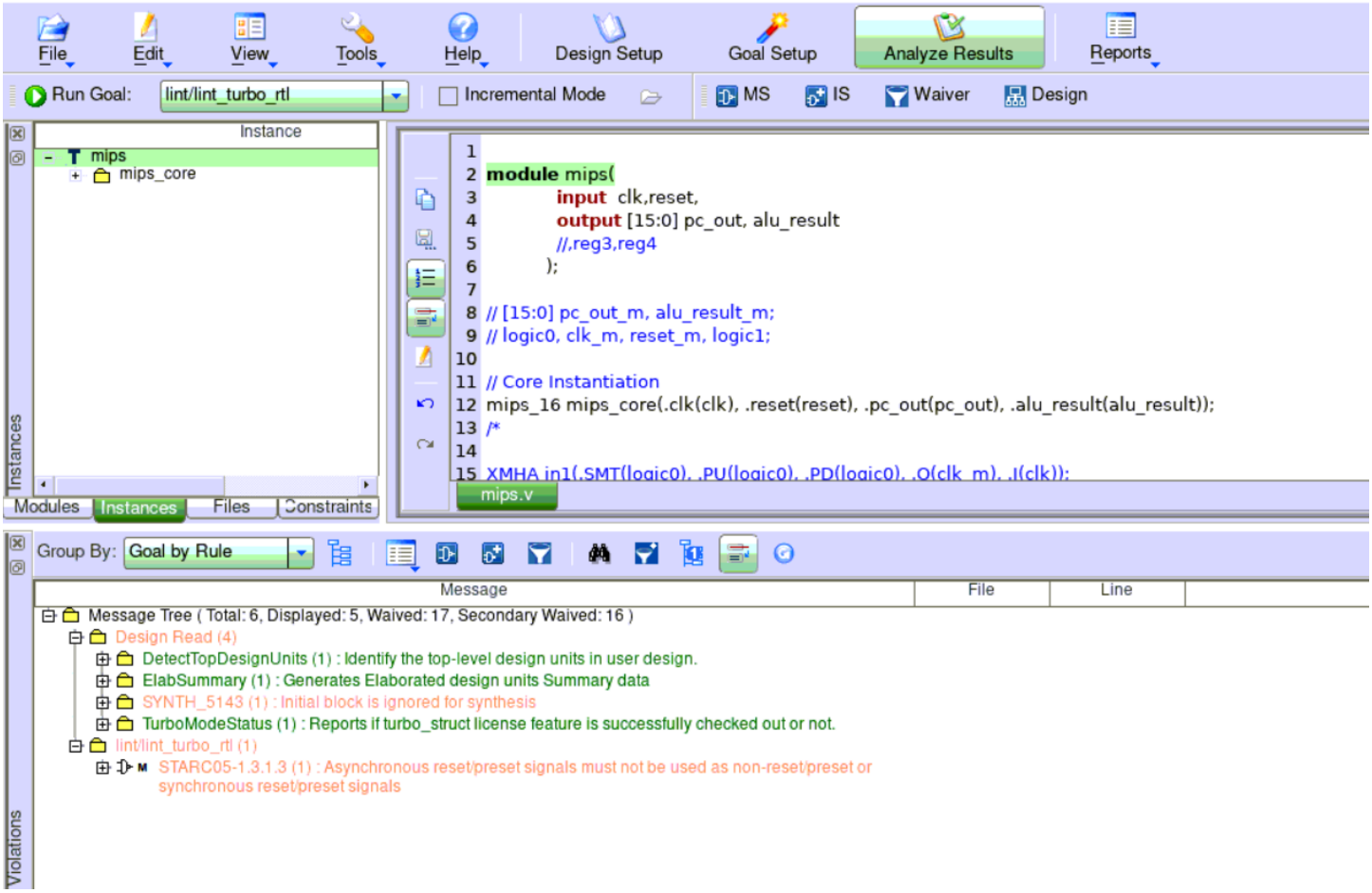
1- Lint RTL



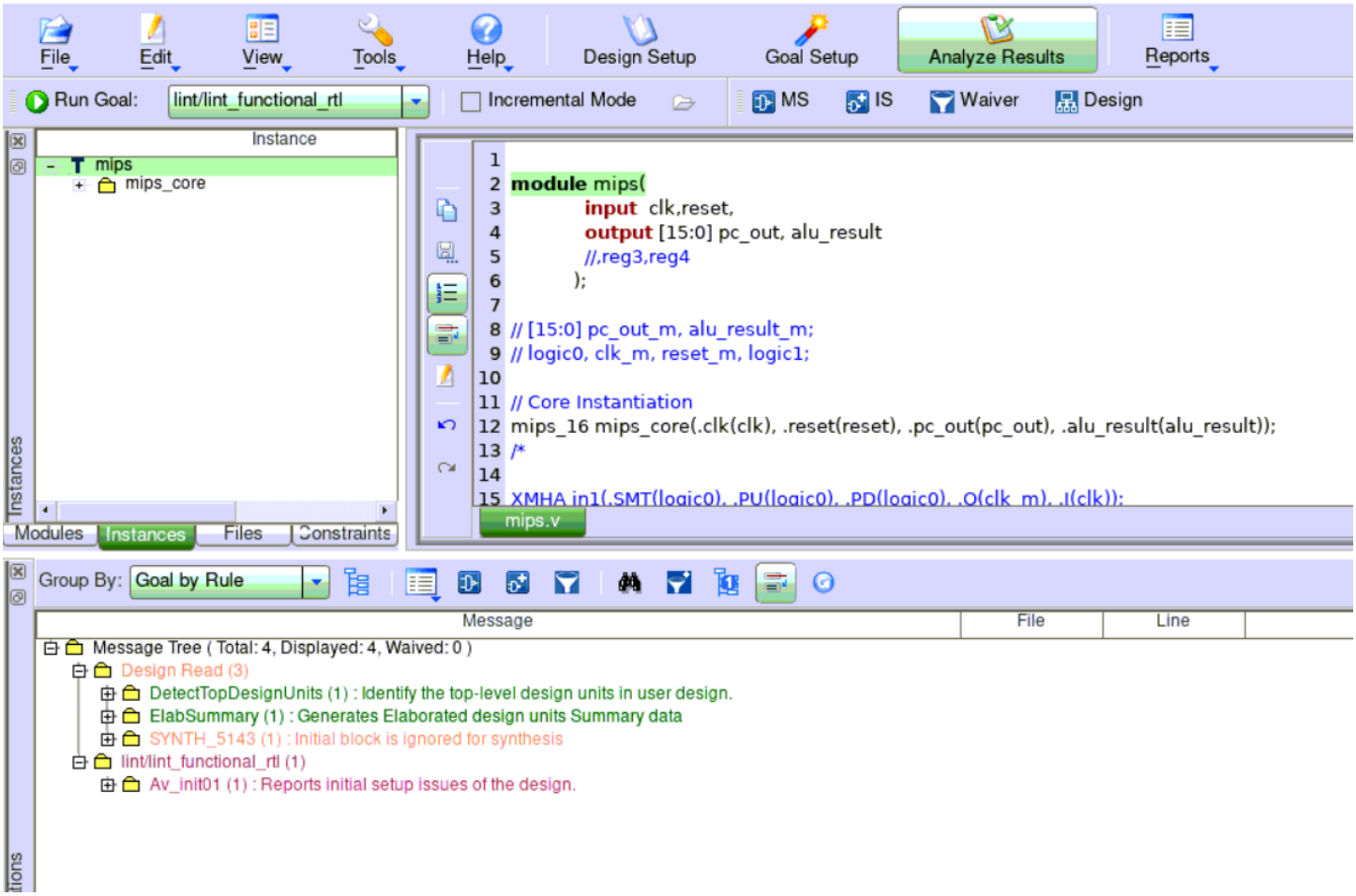
2- Lint RTL enhanced


File
Edit
View
Tools
Help
Design Setup
Goal Setup
Analyze Results
Reports
Run Goal:
lint/lint_rtl_enhanced
Incremental Mode
MS
IS
Waiver
Design
Instance
mips
mips_core
Modules
Instances
Files
Constraints
1
2 module mips(
3 input clk,reset,
4 output [15:0] pc_out, alu_result
5 //,reg3,reg4
6);
7
8 // [15:0] pc_out_m, alu_result_m;
9 // logic0, clk_m, reset_m, logic1;
10
11 // Core Instantiation
12 mips_16 mips_core(.clk(clk), .reset(reset), .pc_out(pc_out), .alu_result(alu_result));
13 /*
14
15 XMHA in1(.SMT(logic0), .PU(logic0), .PD(logic0), .O(clk_m), .I(clk));
mips.v
Group By:
Goal by Rule
Message
File
Line
Message Tree (Total: 44, Displayed: 7, Waived: 37)
Design Read (4)
CMD_overloadrule01 (1) : Overload ignored as rule not registered in run language
DetectTopDesignUnits (1) : Identify the top-level design units in user design.
ElabSummary (1) : Generates Elaborated design units Summary data
SYNTH_5143 (1) : Initial block is ignored for synthesis
lint/lint_rtl_enhanced (3)
ReportPortInfo-ML (1) : Generate a report of all ports of top level block and black-box instances.
STARC05-1.3.1.3 (1) : Asynchronous reset/preset signals must not be used as non-reset/preset or synchronous reset/preset signals
W240 (1) : An input has been declared but is not read

3- RTL Turbo



4- Lint Functional RTL



5- Lint abstract