

A

B

C

D

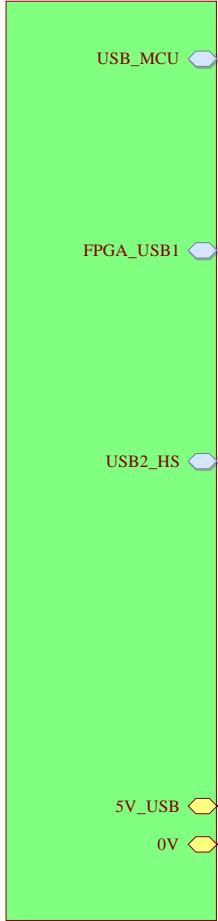
A

B

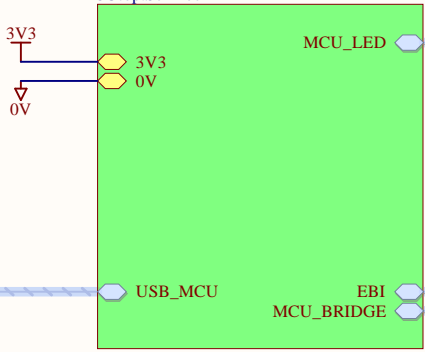
C

D

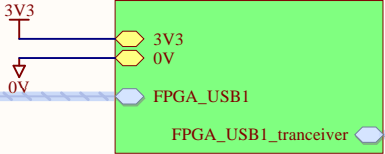
Connectors
Connectors.SchDoc



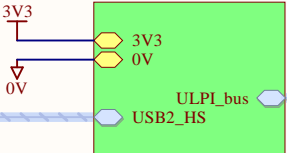
MCU
MCUtop.SchDoc



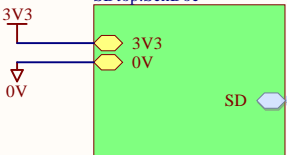
USB_Transceiver
USB_trans.SchDoc



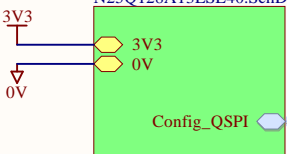
USB 2.0 PHY
USB3300.SchDoc



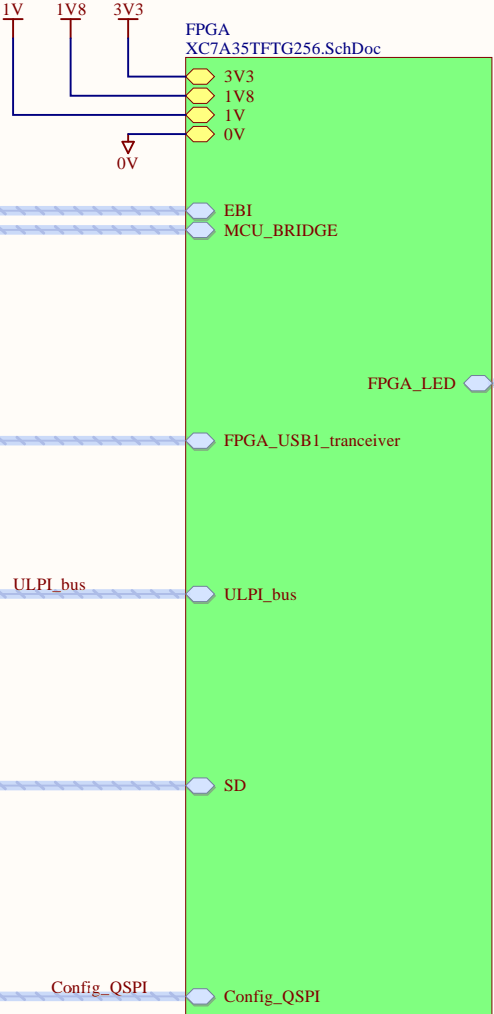
SD
SDtop.SchDoc



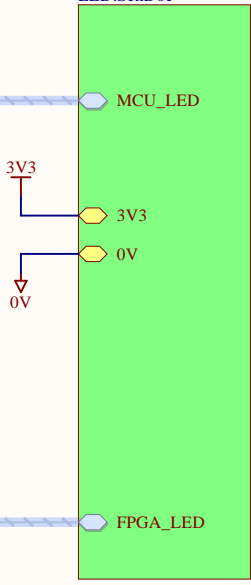
Configuration memory
N25Q128A13ESE40.SchDoc



FPGA
XC7A35TFTG256.SchDoc



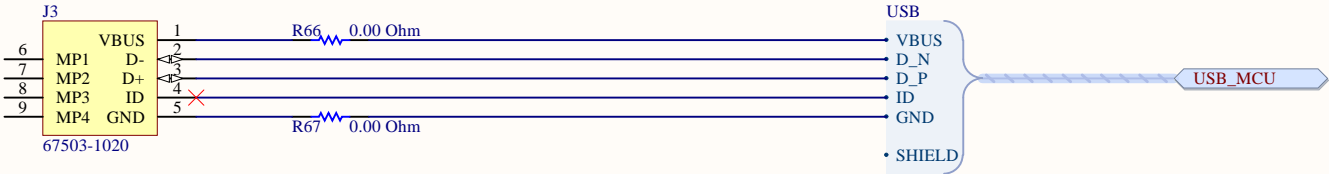
LEDs
LED.SchDoc



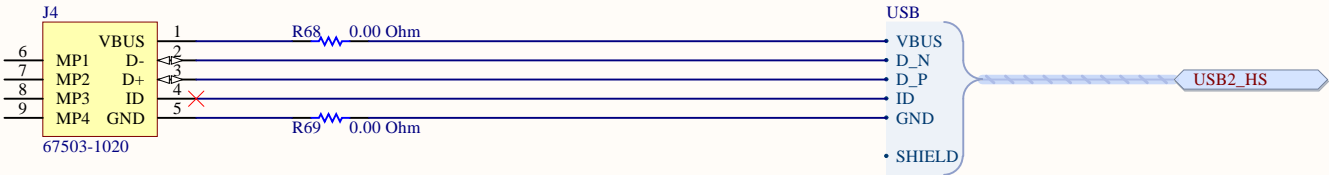
Board:	BottyMcBotFace	Version:	0.1
Sheetname:	Top level	Sheet	1 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	main.SchDoc		



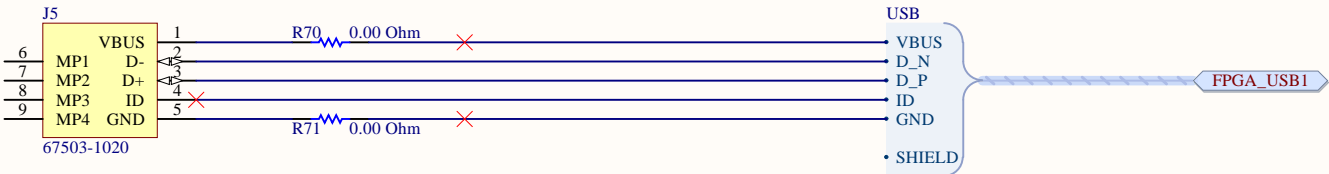
Do not connect to 0V,
grounding only on
host side



Do not connect to 0V,
grounding only on
host side



Do not connect to 0V,
grounding only on
host side



Do not connect to 0V,
grounding only on
host side

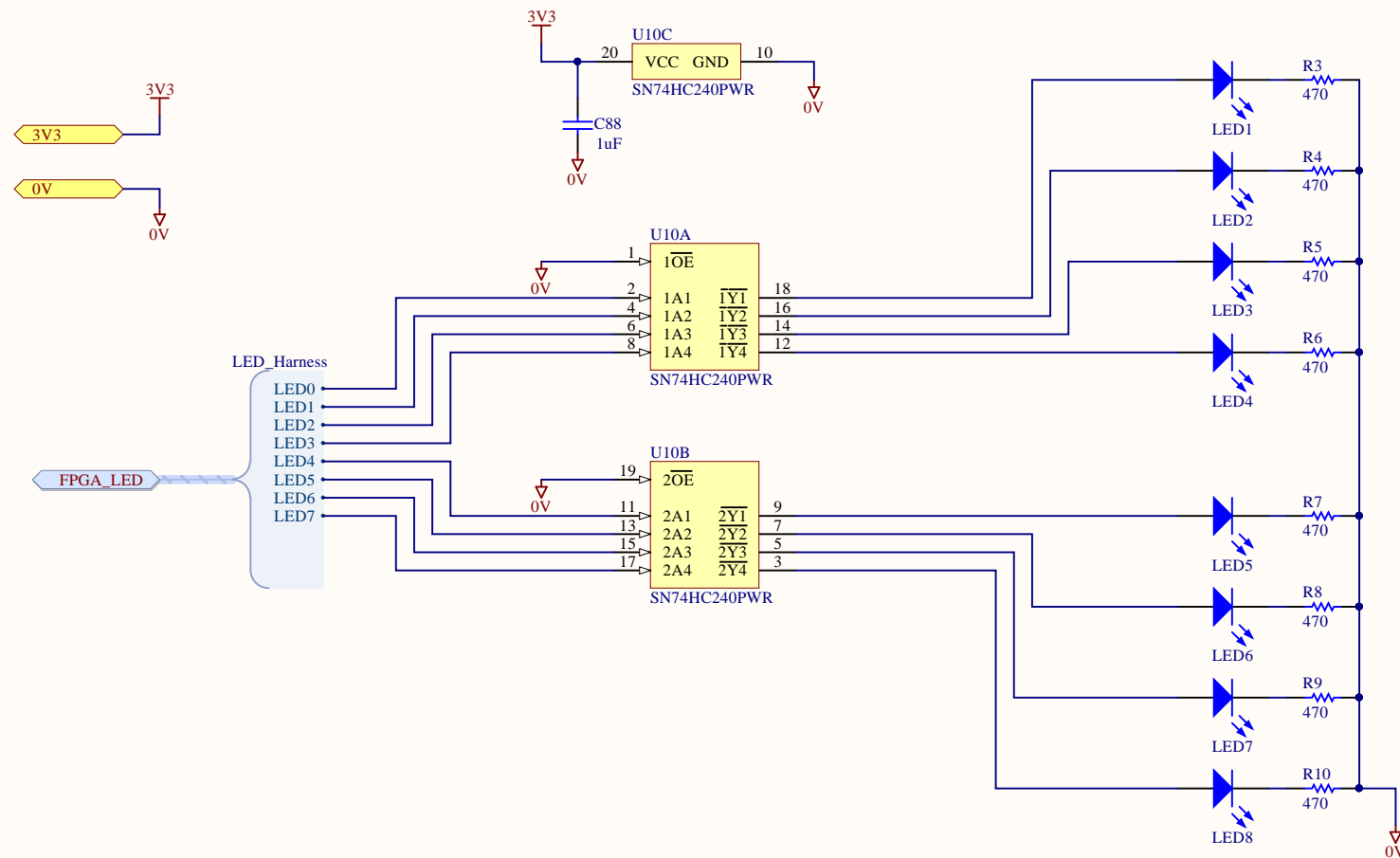


Board: BottyMcBotFace	Version: 0.1
Sheetname: Connectors	Sheet 2 of 20
Subject: TDT4295 Datamaskinprosjekt 2016	Group: Ytelse
Shematic file: Connectors.SchDoc	



A

A



Resistors give current of approx.
3 mA

Debug leds for FPGA

B

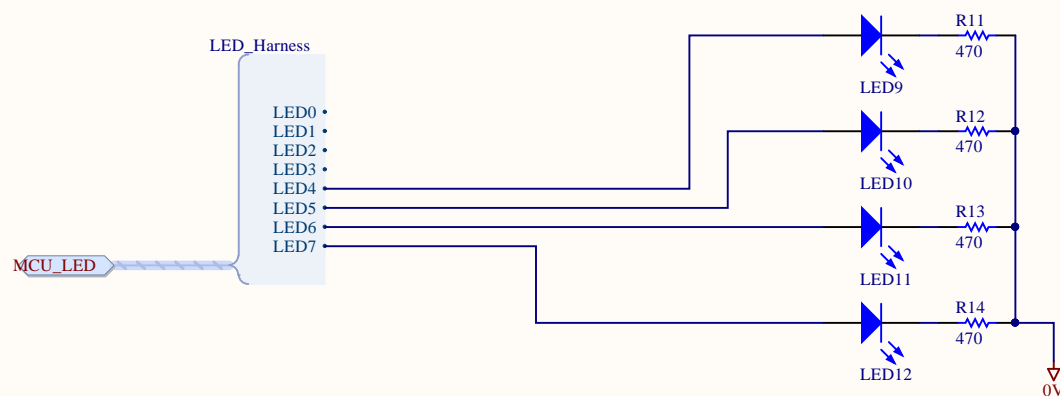
B

C

C

D

D



Debug leds for MCU

Board:	BottyMcBotFace	Version:	0.1
Sheetname:	Debug LEDs	Sheet	3 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	LED.SchDoc		



A

B

C

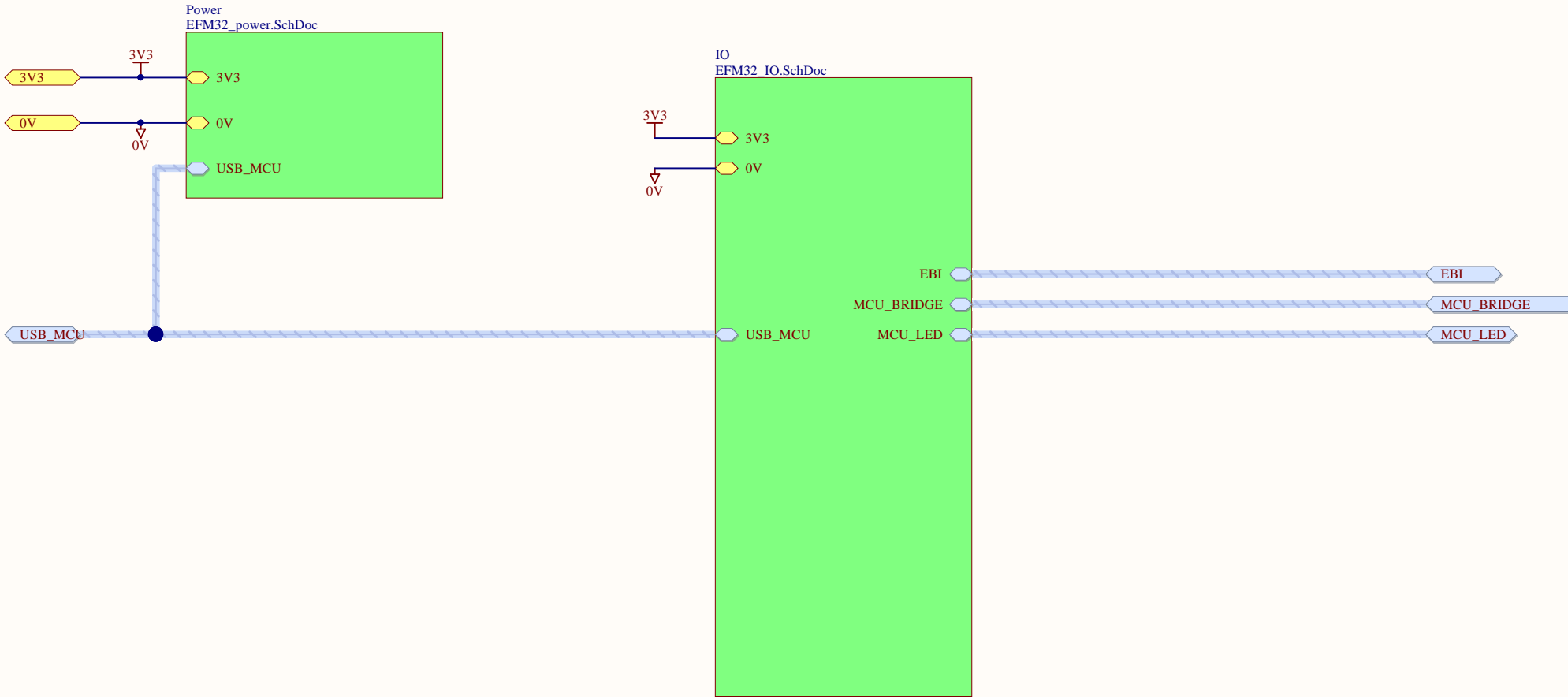
D

A

B

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D



Board:	BottyMcBotFace	Version:	0.1	
Sheetname:	Microcontroller Top Level	Sheet	4 of 20	
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse	
Schematic file: MCUtop.SchDoc				

A

B

C

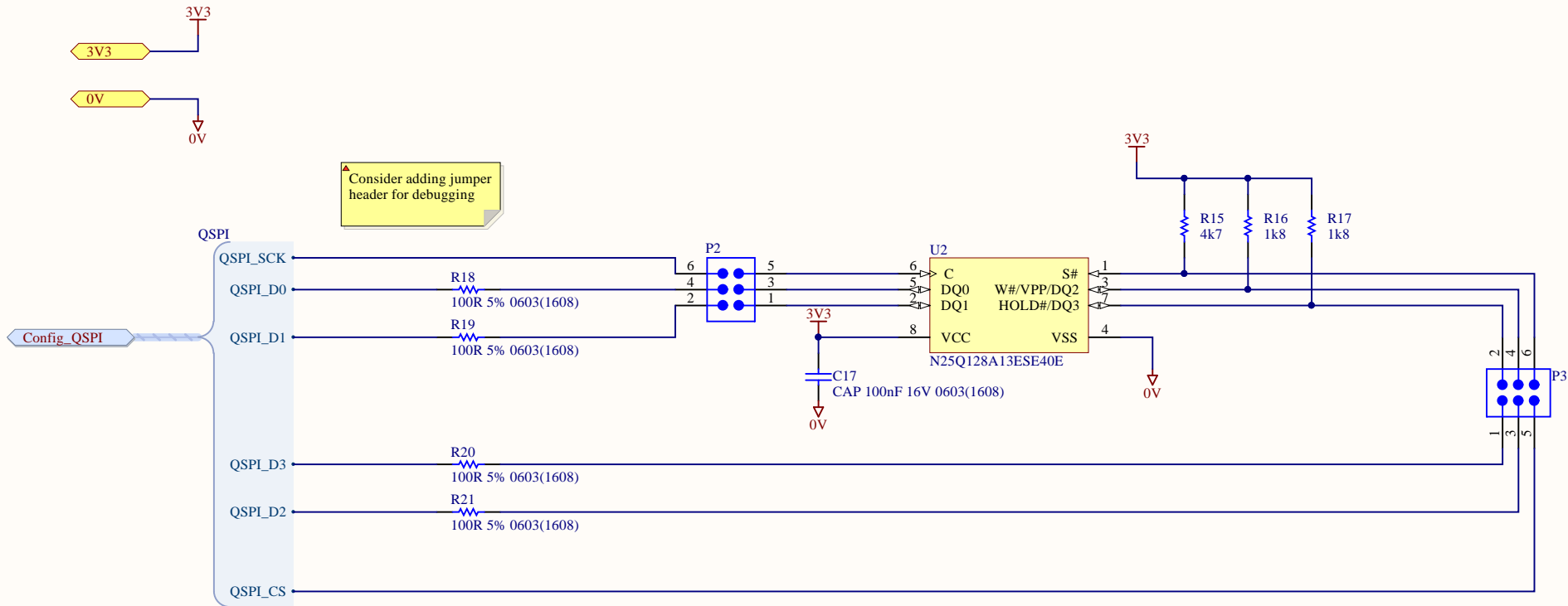
D

A

B

C

D



Board:	BottyMcBotFace	Version:	0.1	
Sheetname:	Configuration Memory	Sheet	7 of 20	
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse	
Schematic file:	N25Q128A13ESE40.SchDoc			

A

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C

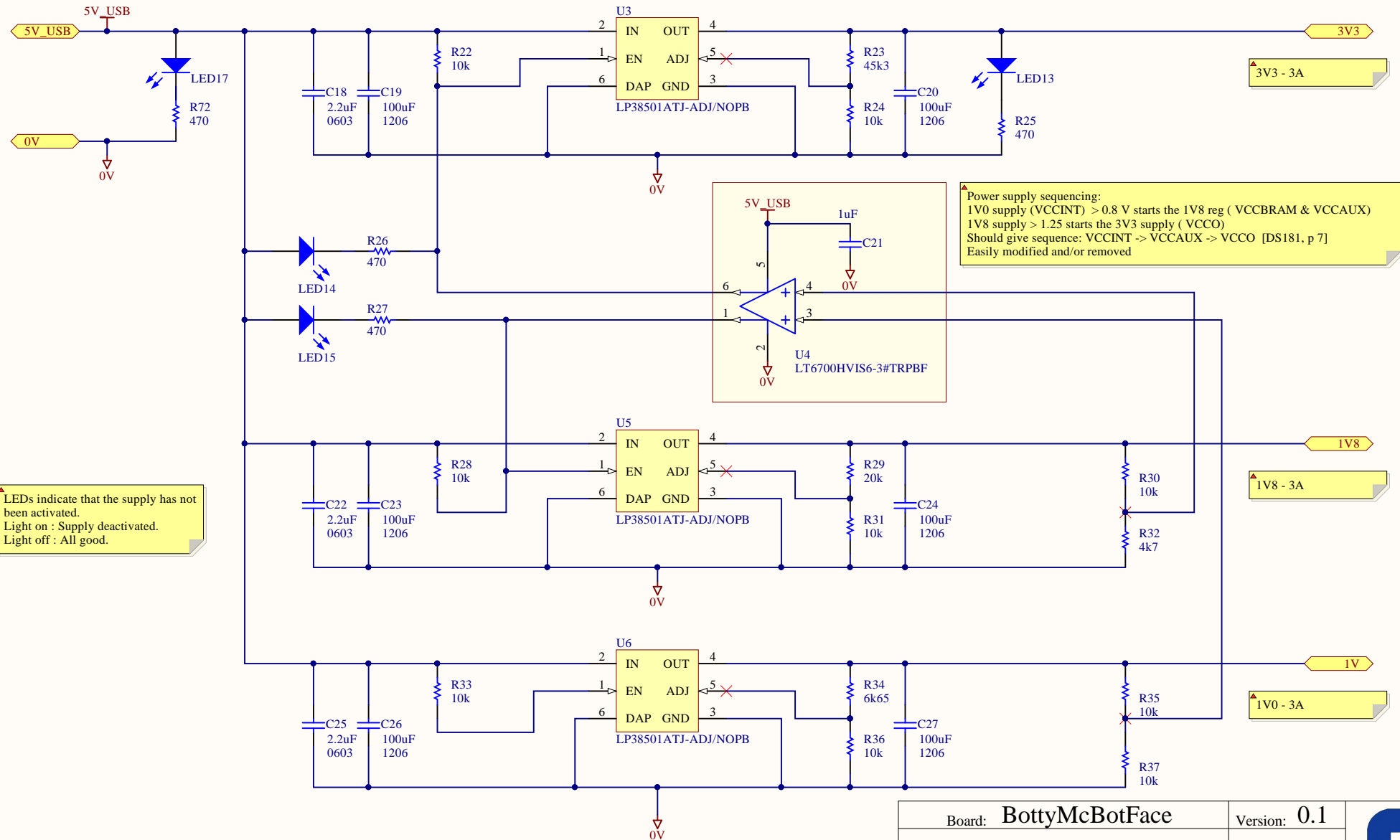
D

A

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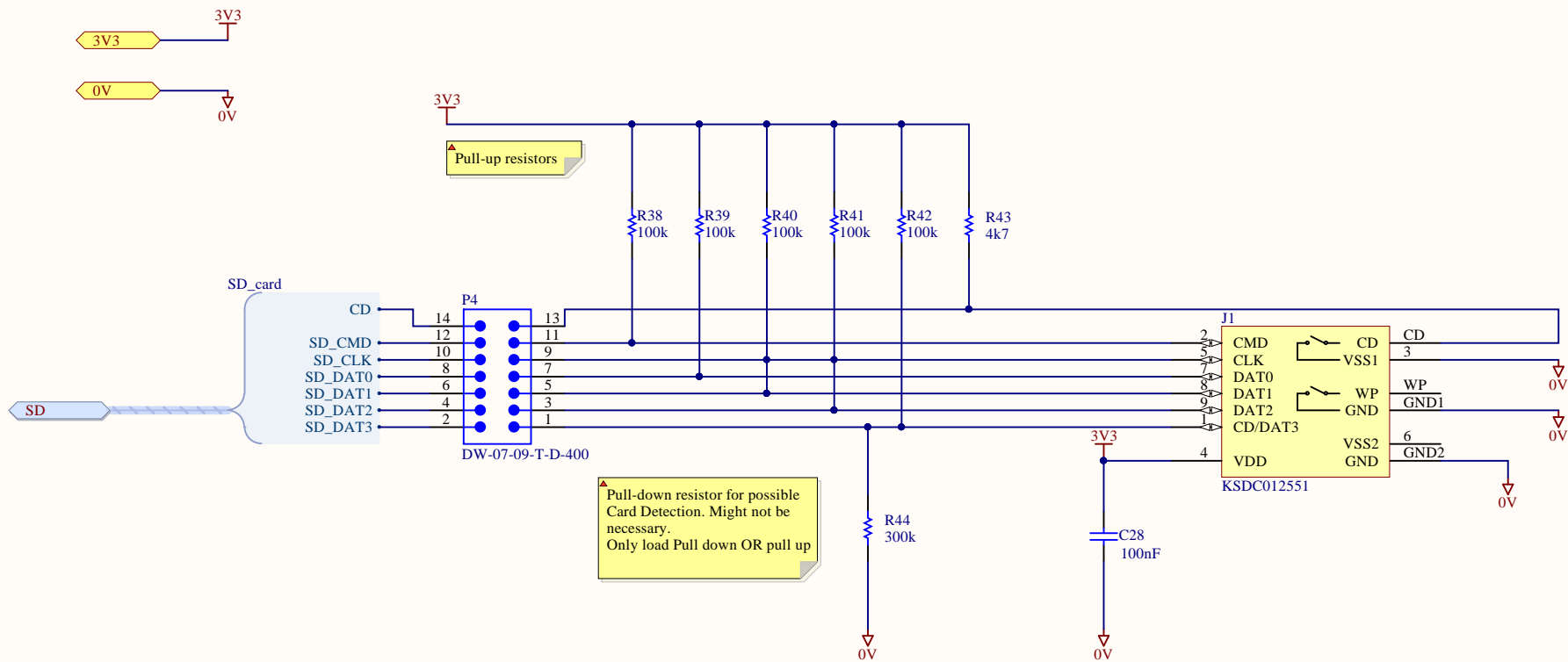
C

D



Board:	BottyMcBotFace	Version:	0.1
Sheetname:	Power Regulators	Sheet	8 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	Power.SchDoc		





Board:	BottyMcBotFace	Version:	0.1
Sheetname:	SD Card	Sheet	9 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	SDtop.SchDoc		



A

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C

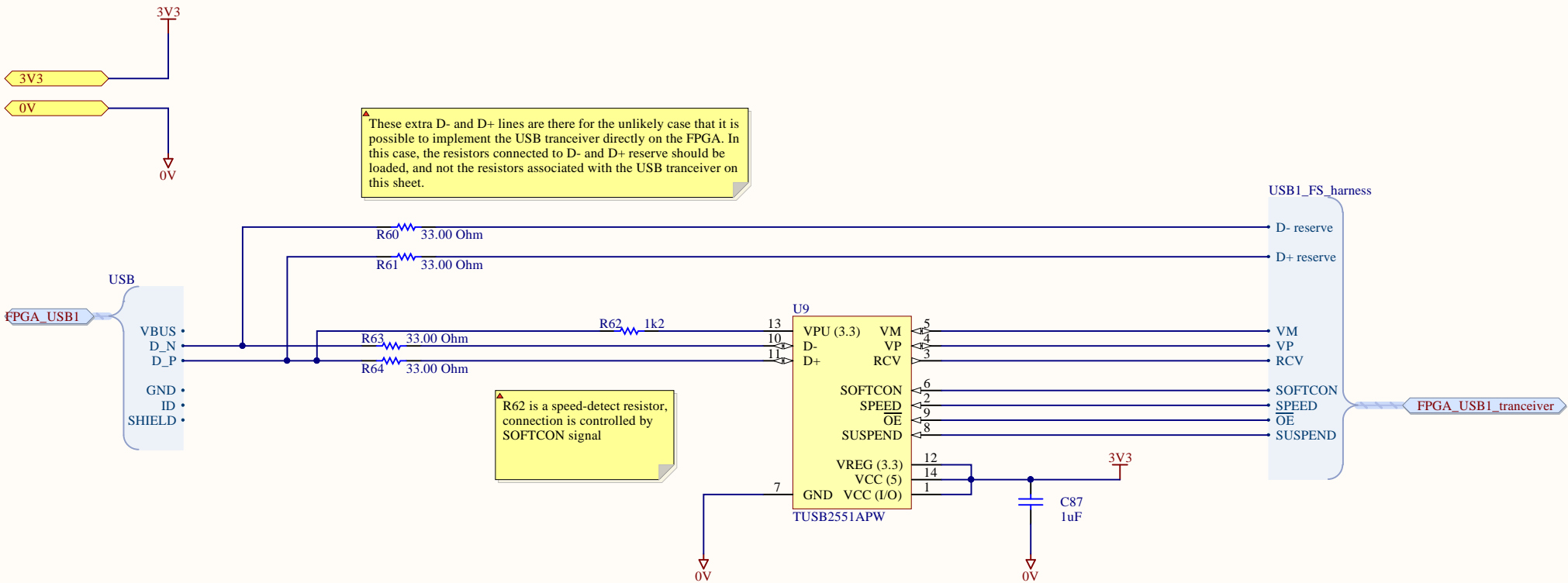
D

A

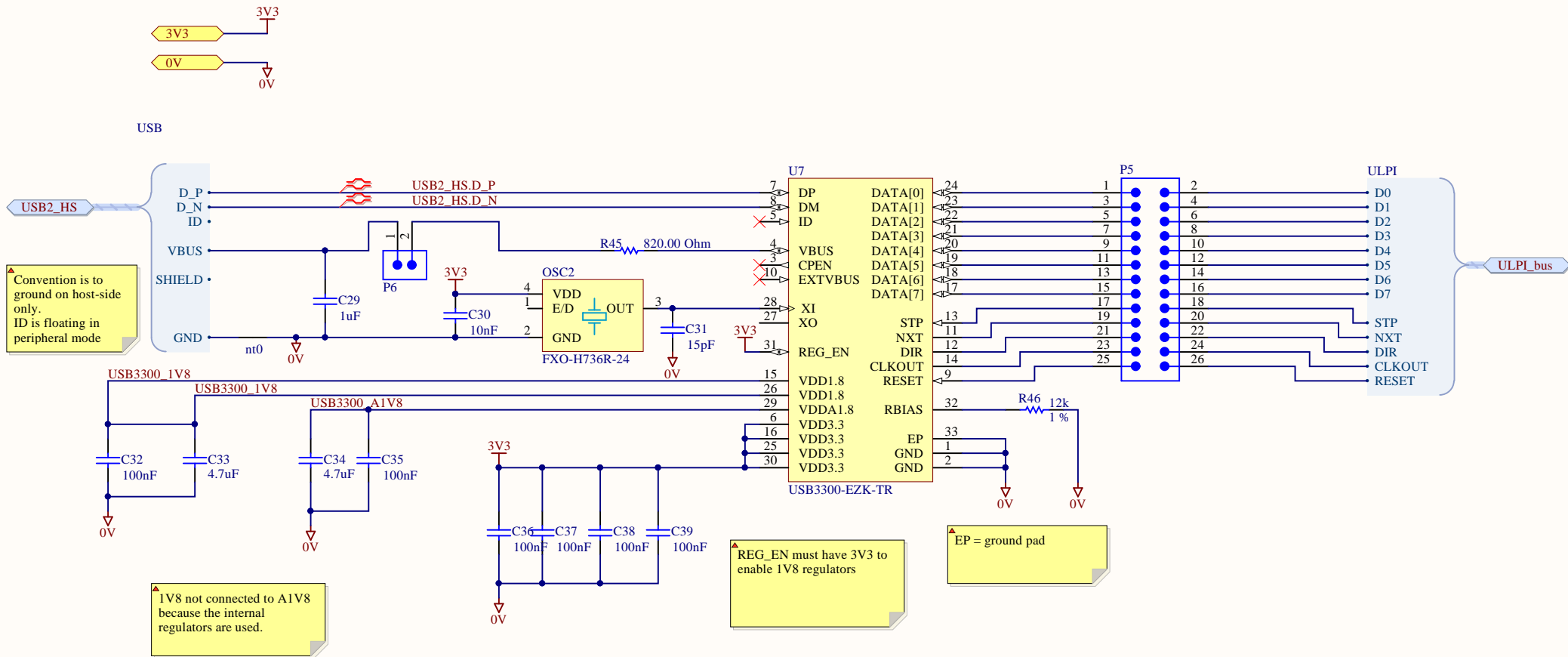
B

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D



Board:	BottyMcBotFace	Version:	0.1	
Sheetname:	USB Full Speed tranceiver	Sheet	10 of 20	
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse	
Schematic file:	USB_trans.SchDoc			



▲ USB3300 - Silicon Labs USB 2.0 High speed transceiver
- Connected in peripheral mode

Board:	BottyMcBotFace	Version:	0.1
Sheetname:	USB 2.0 HS PHY	Sheet	11 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	USB3300.SchDoc		



A

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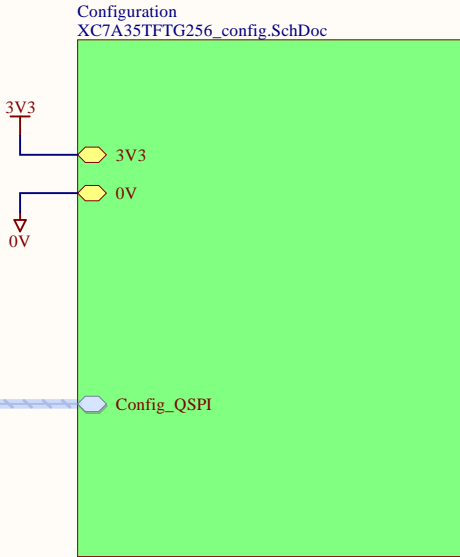
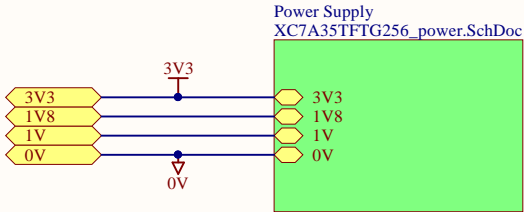
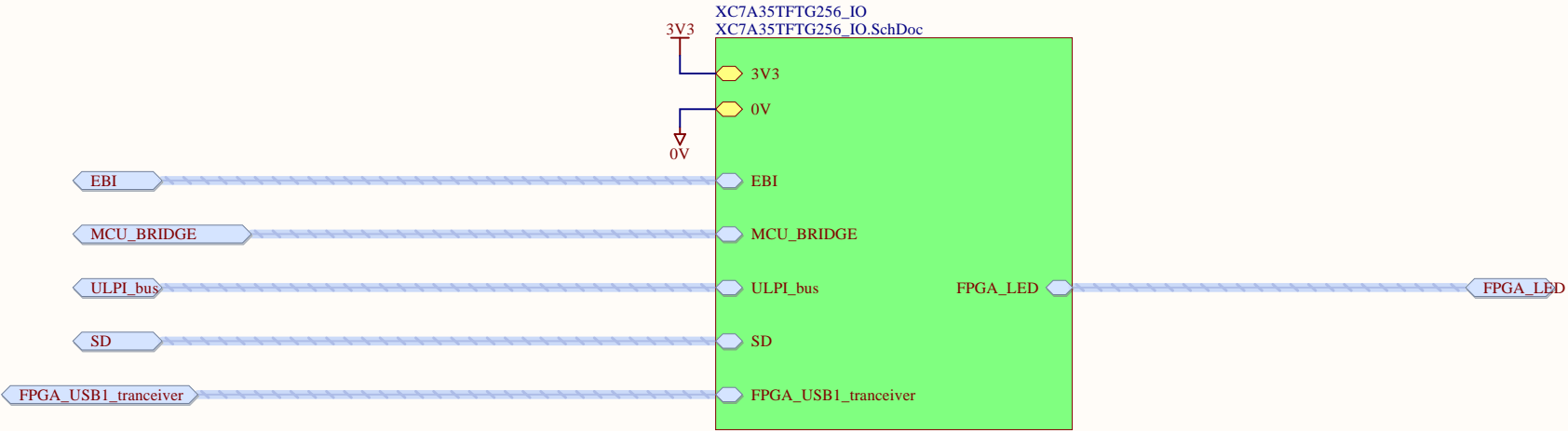
D

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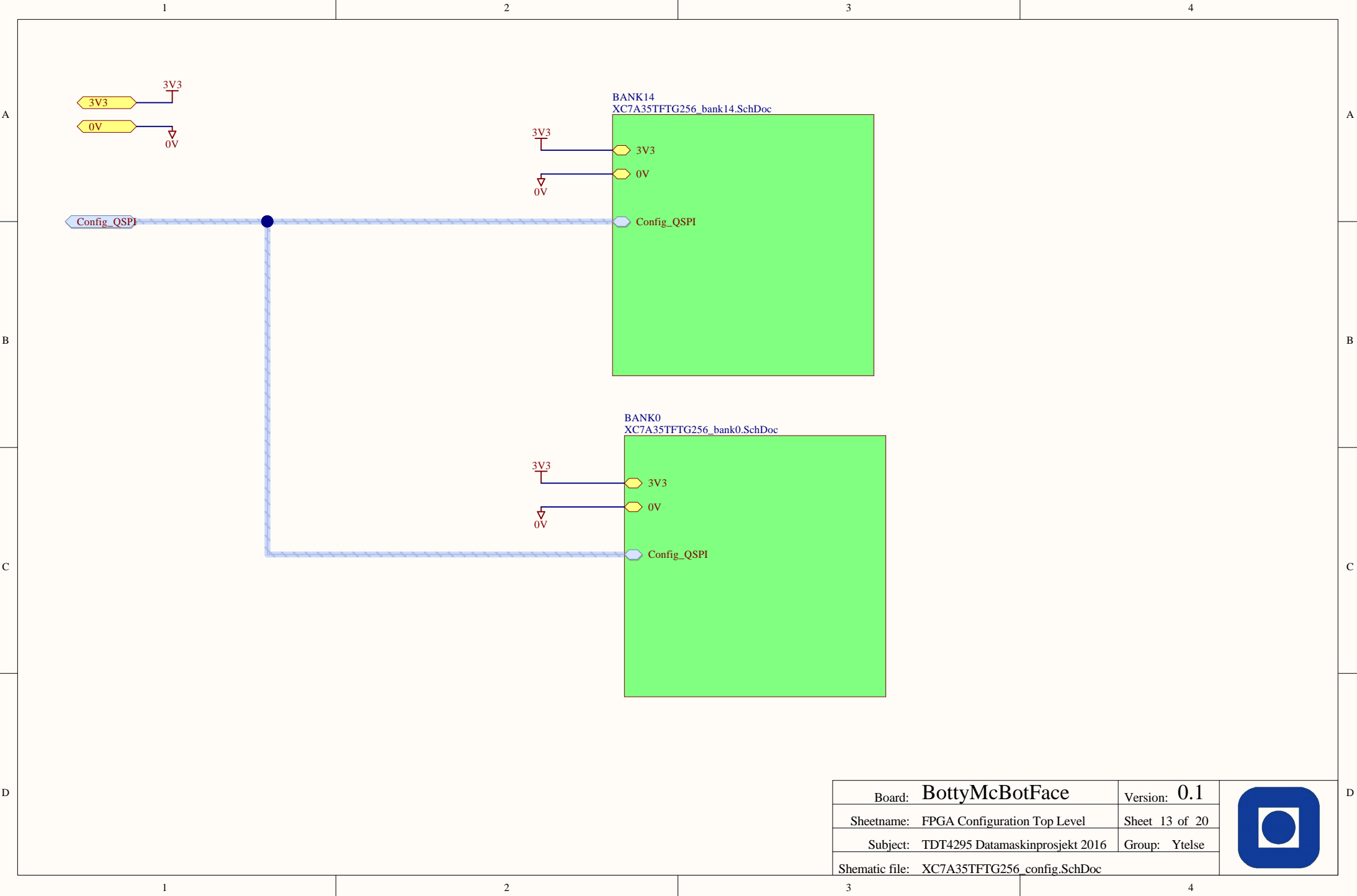
C

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Board:	BottyMcBotFace	Version:	0.1
Sheetname:	FPGA Top level	Sheet	12 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	XC7A35TFTG256.SchDoc		





A

A

B

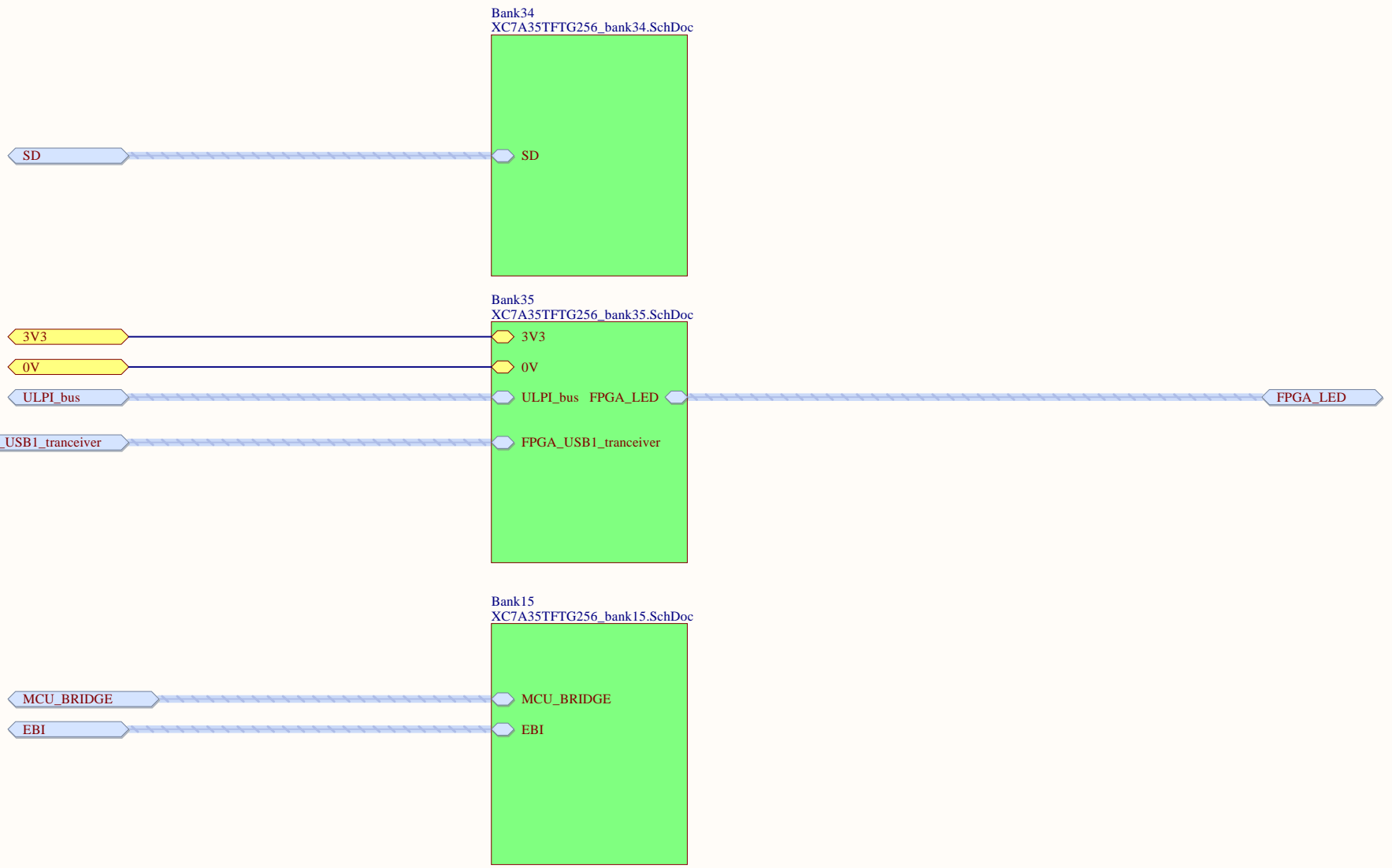
B

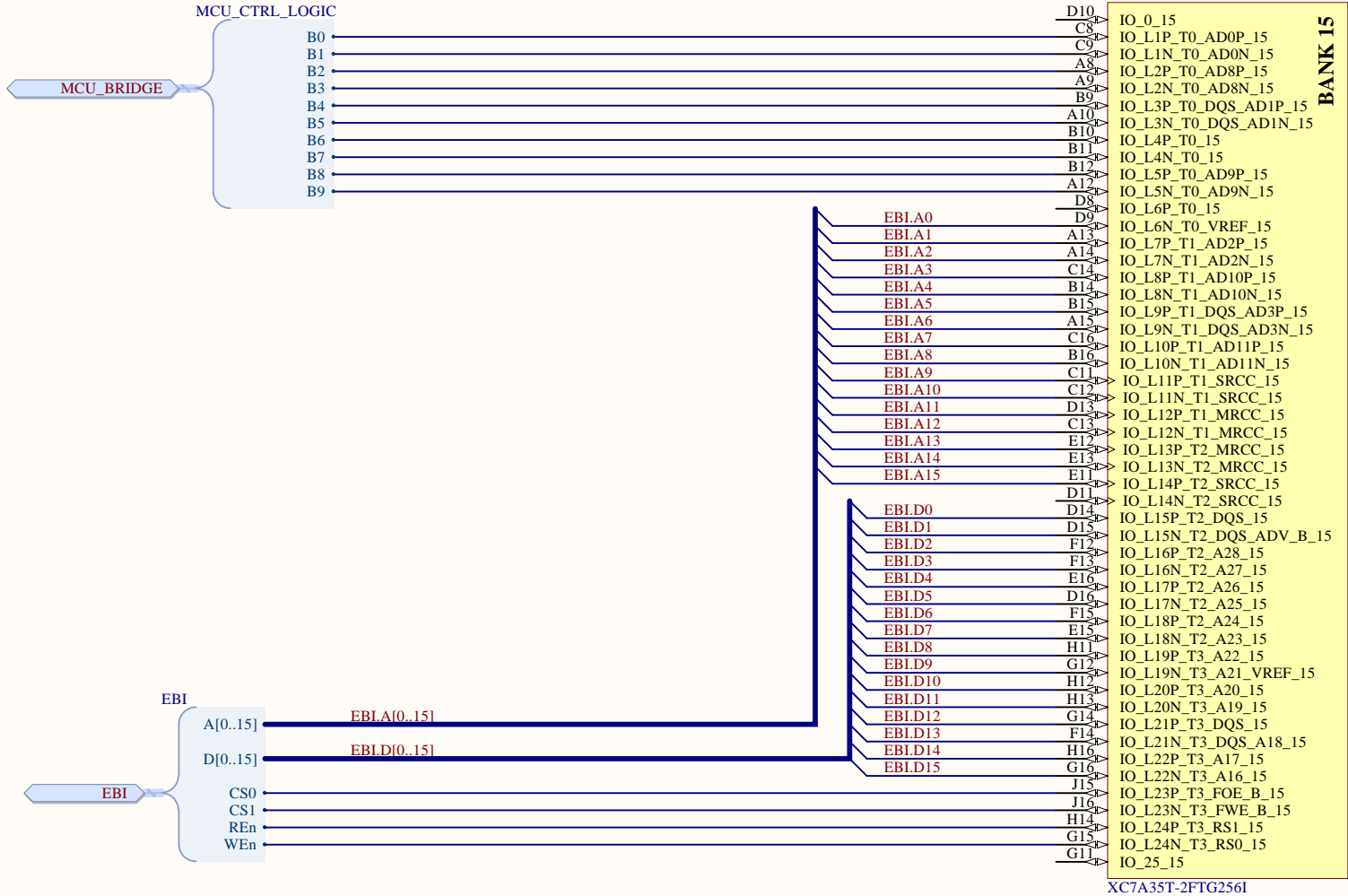
C

C

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D





Board: BottyMcBotFace	Version: 0.1	
Sheetname: FPGA Bank 15	Sheet 17 of 20	
Subject: TDT4295 Datamaskinprosjekt 2016	Group: Ytelse	
Schematic file: XC7A35TFTG256_bank15.SchDoc		

A

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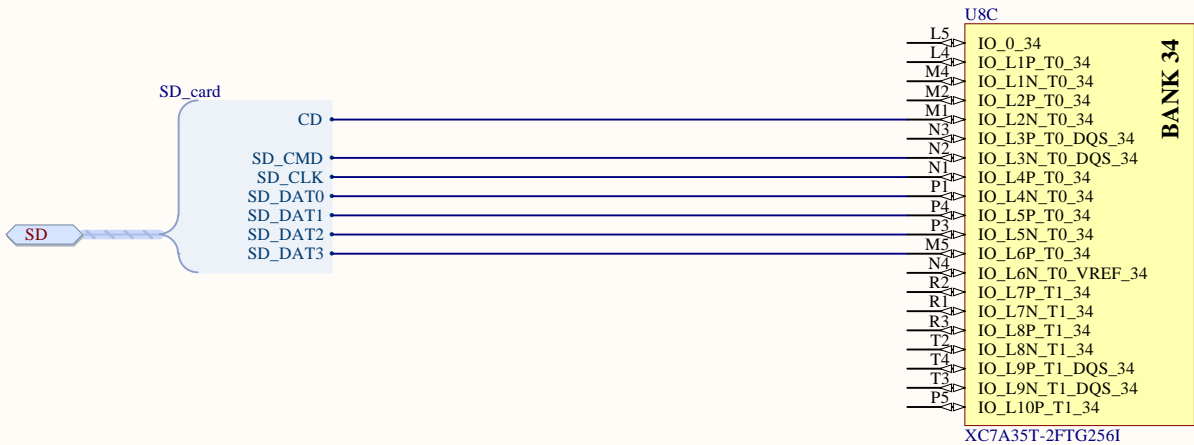
D


A

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Board:	BottyMcBotFace	Version:	0.1	
Sheetname:	FPGA Bank 34	Sheet	18 of 20	
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse	
Schematic file:	XC7A35TFTG256_bank34.SchDoc			

A

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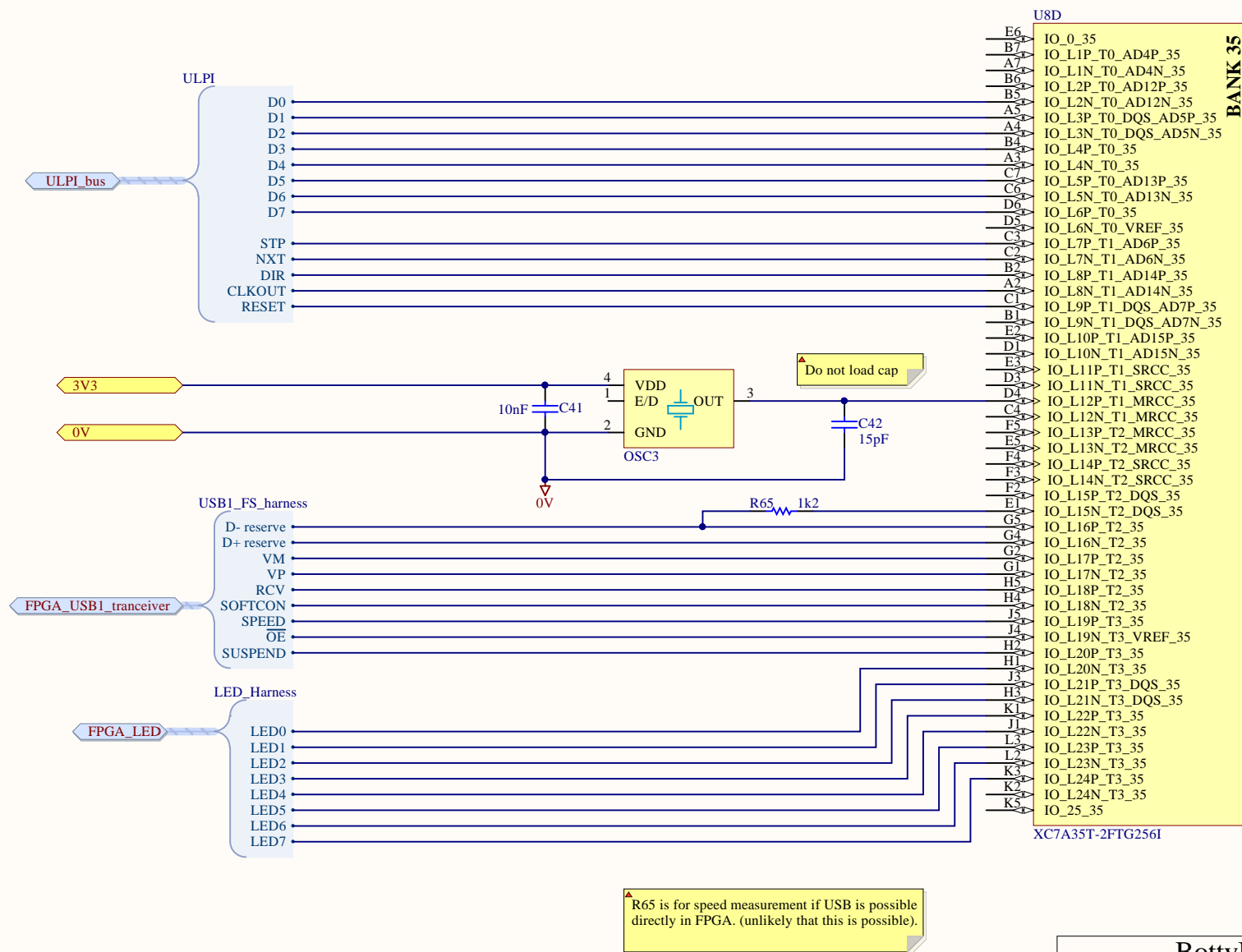
B

C

C

D

D



Board:	BottyMcBotFace	Version:	0.1
Sheetname:	FPGA Bank 35	Sheet	19 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	XC7A35TFTG256_bank35.SchDoc		



A

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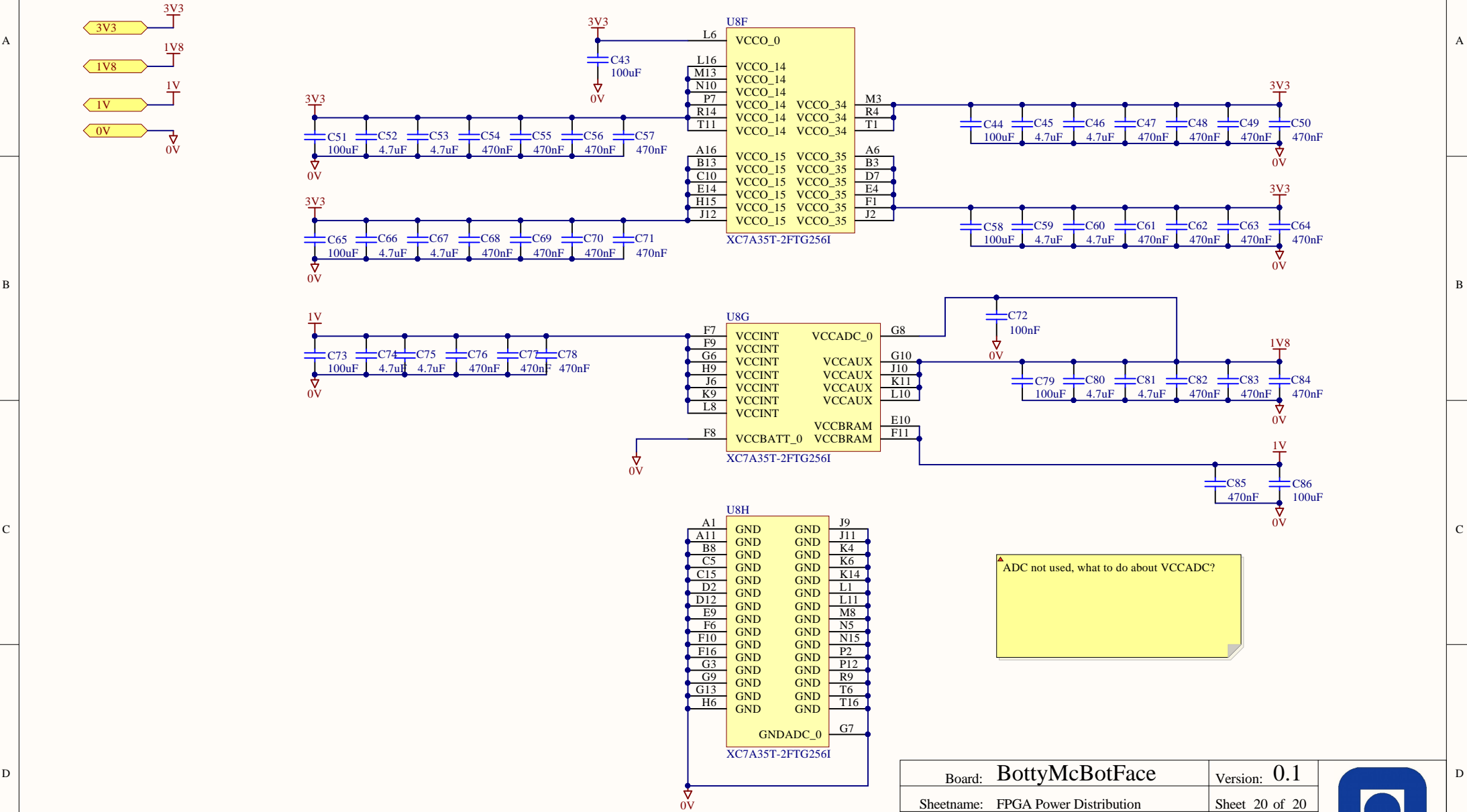
D

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D



Board:	BottyMcBotFace	Version:	0.1
Sheetname:	FPGA Power Distribution	Sheet	20 of 20
Subject:	TDT4295 Datamaskinprosjekt 2016	Group:	Ytelse
Shematic file:	XC7A35TFTG256_power.SchDoc		

