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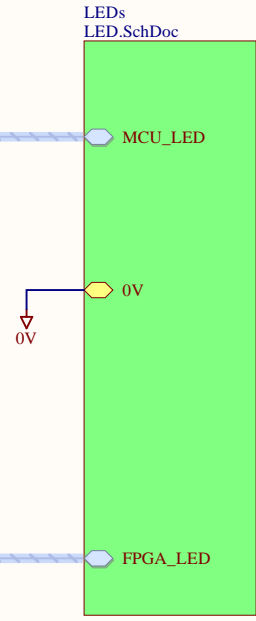
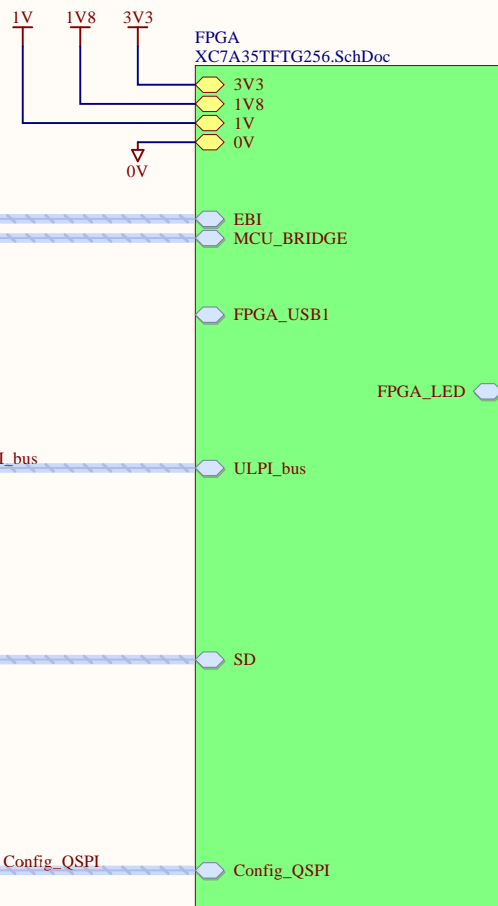
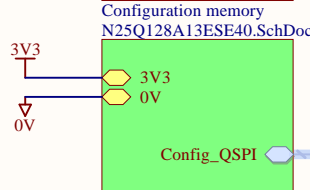
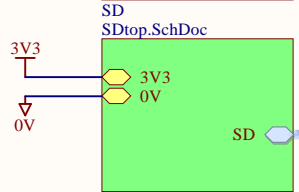
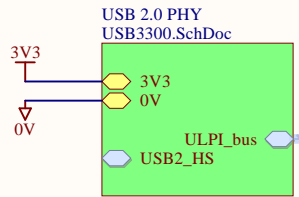
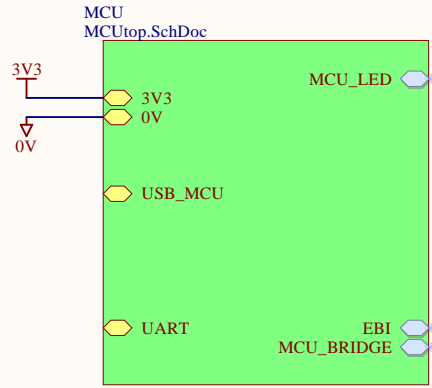
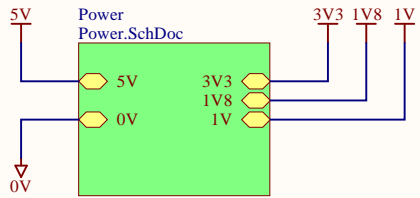
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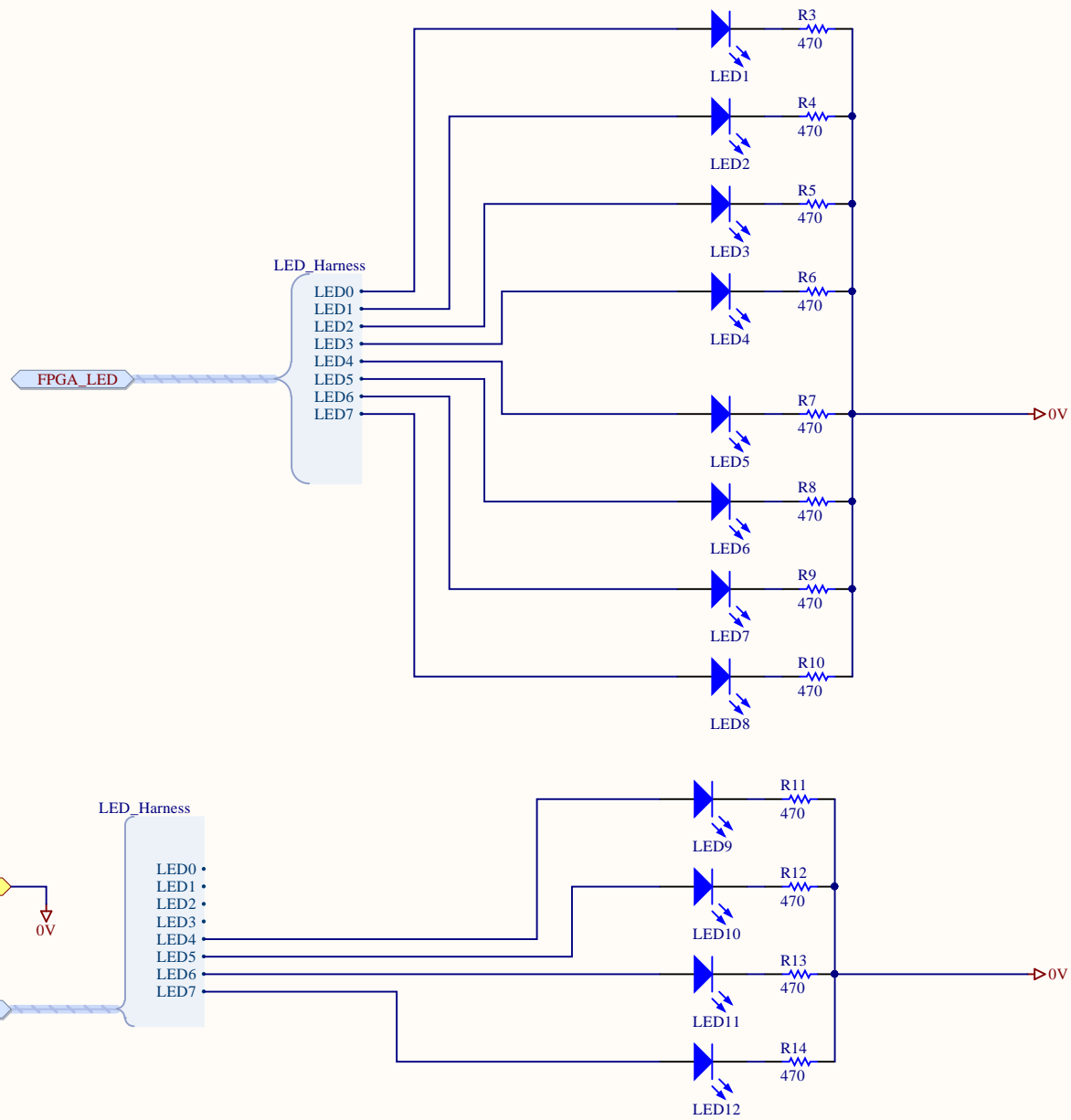
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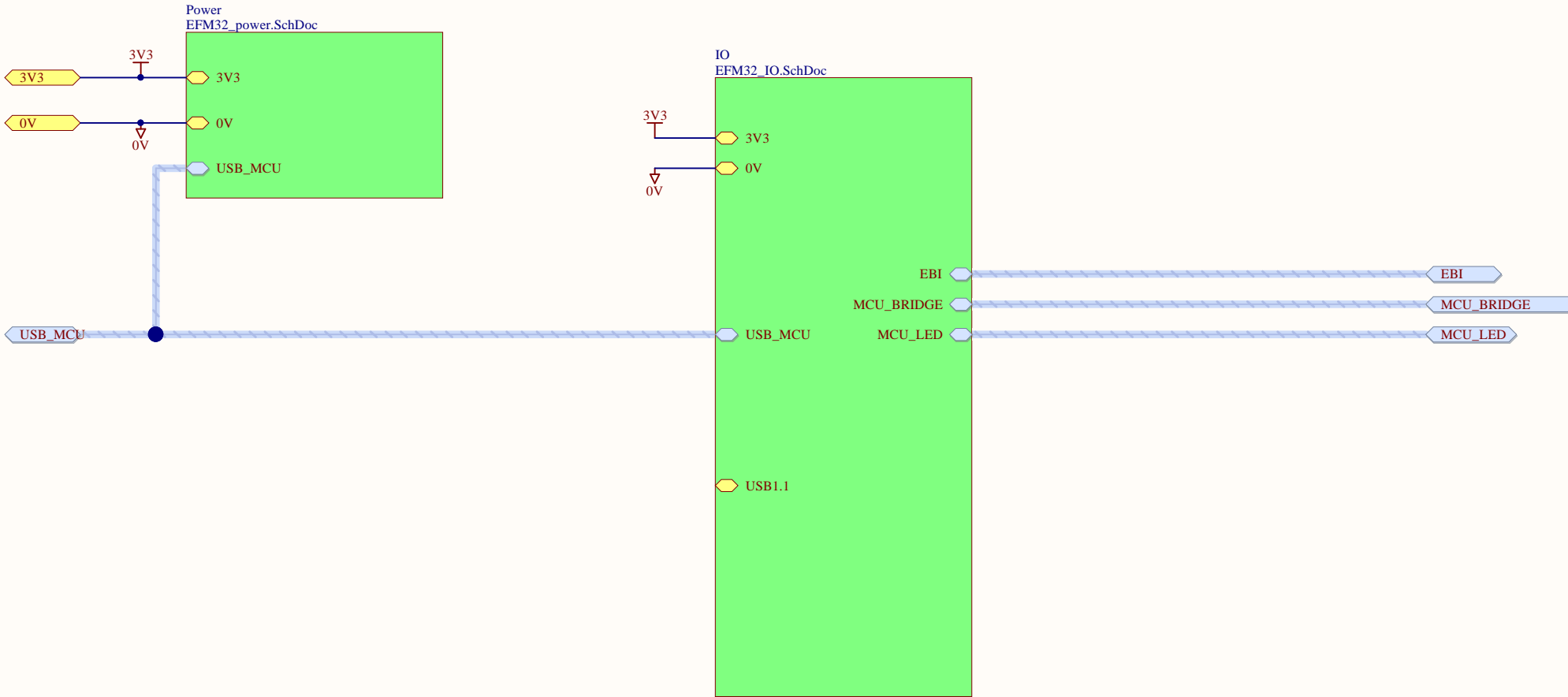
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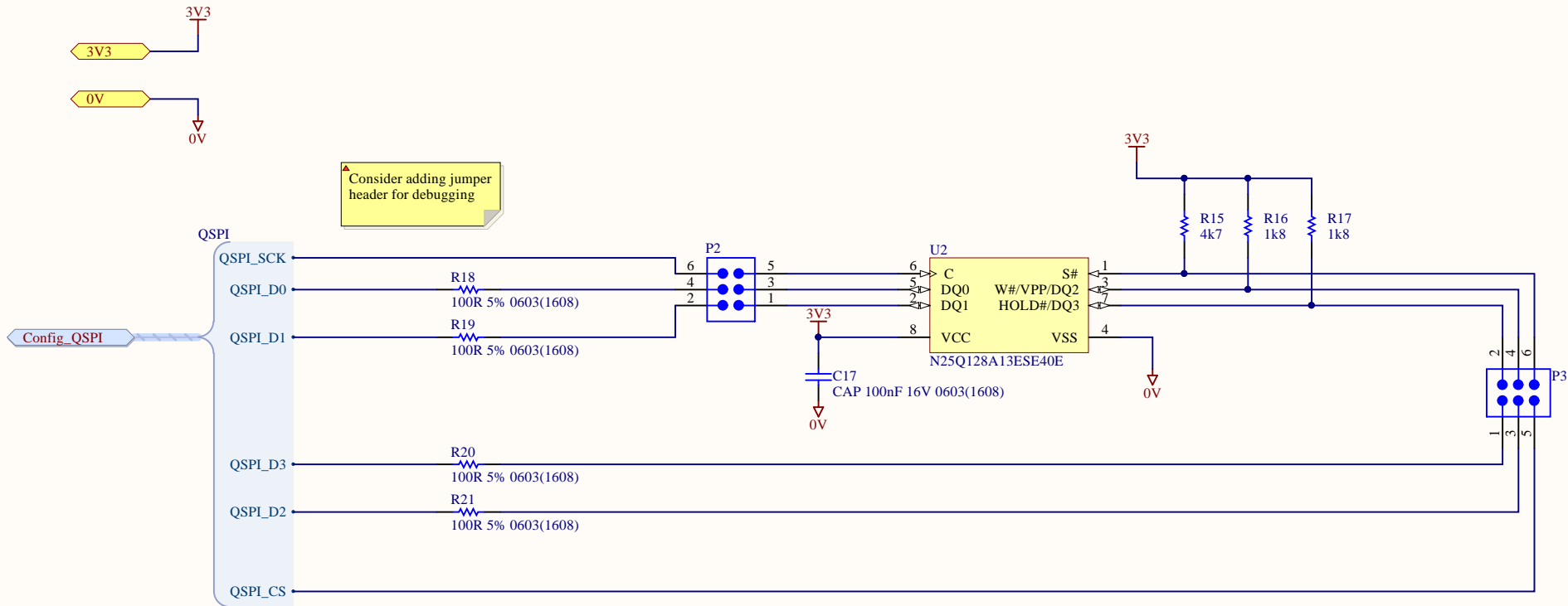
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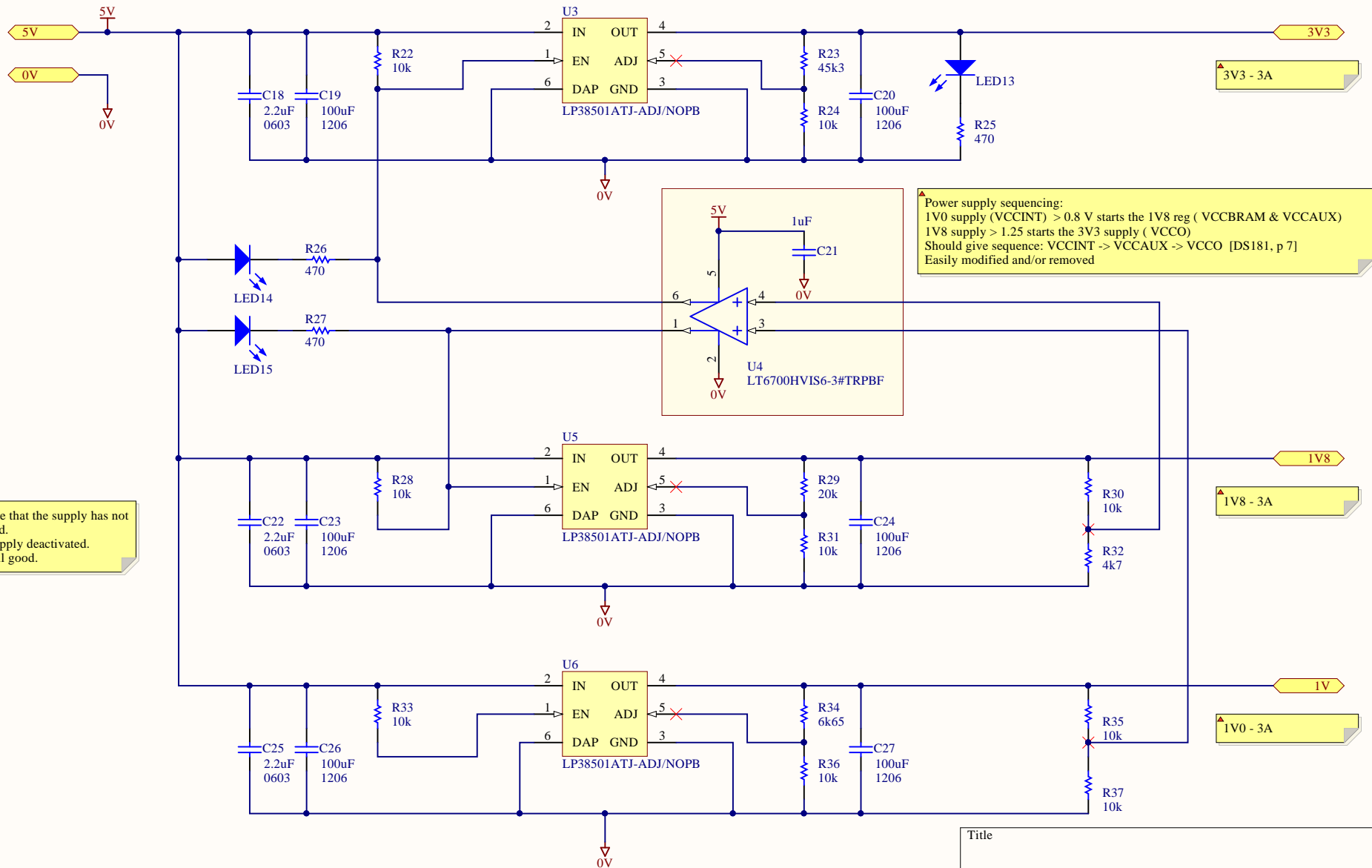
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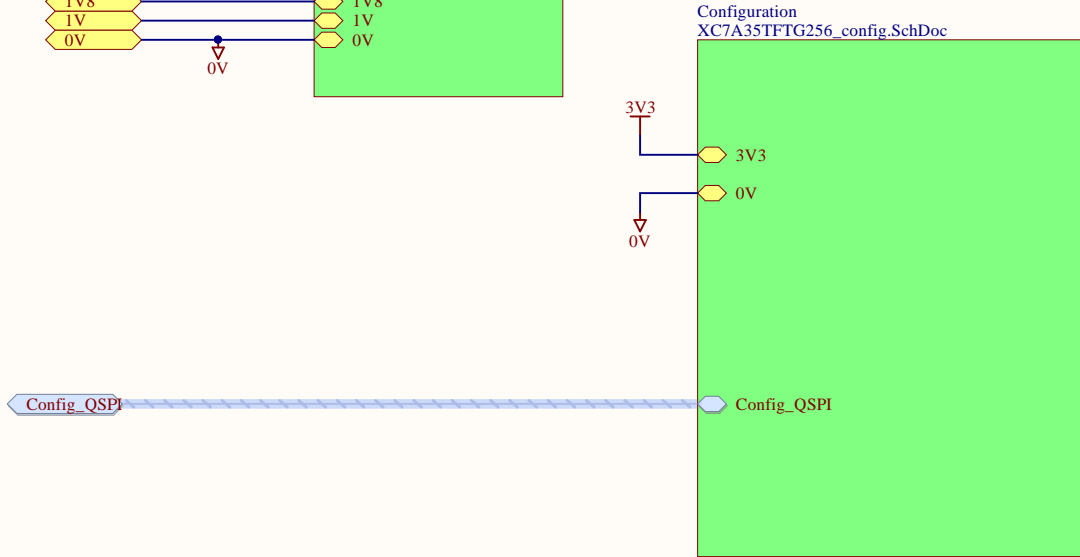
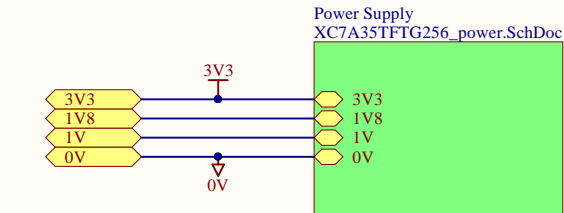
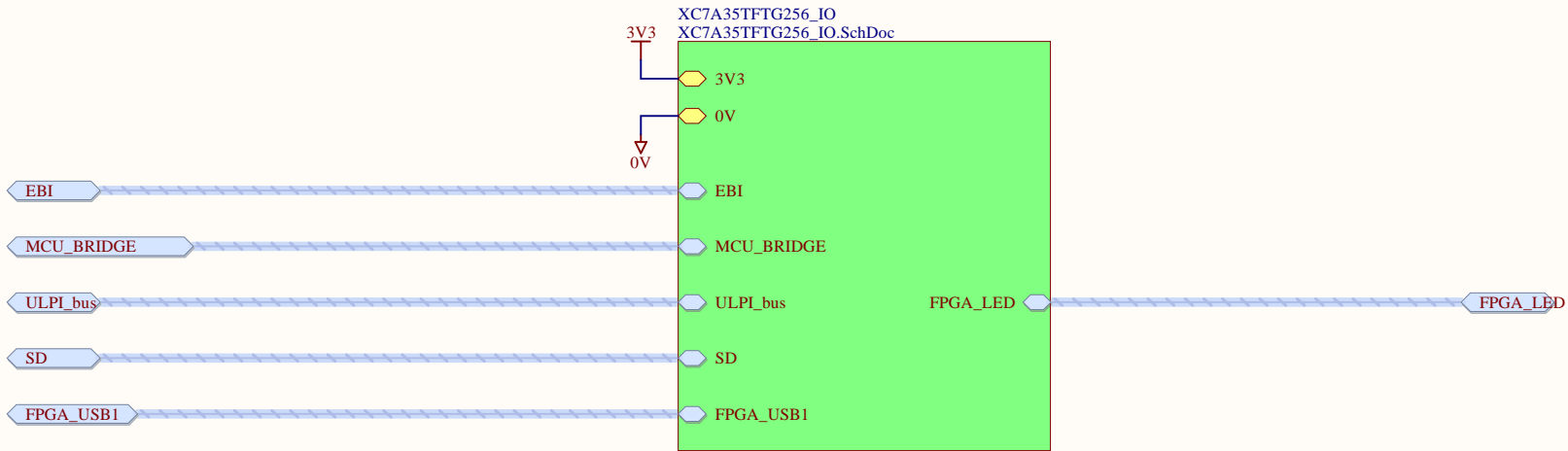
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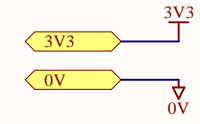
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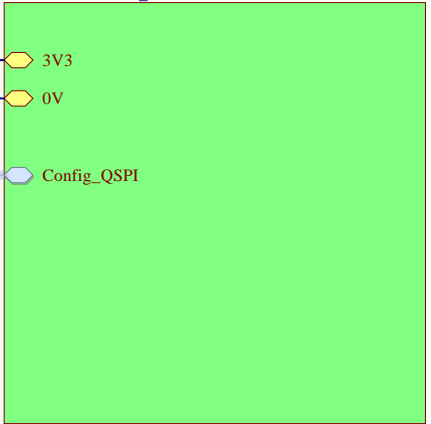
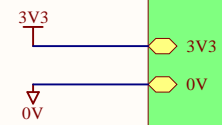
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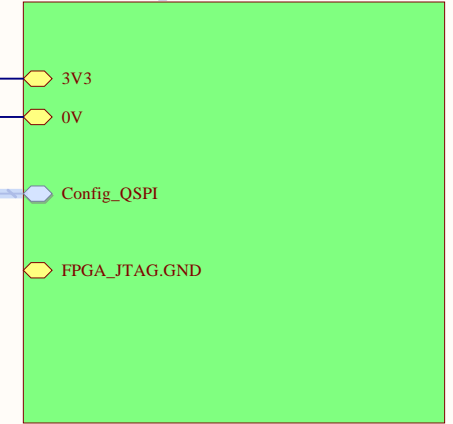
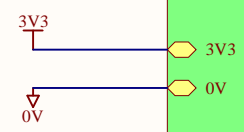
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BANK14
XC7A35TFTG256_bank14.SchDoc



BANK0
XC7A35TFTG256_bank0.SchDoc



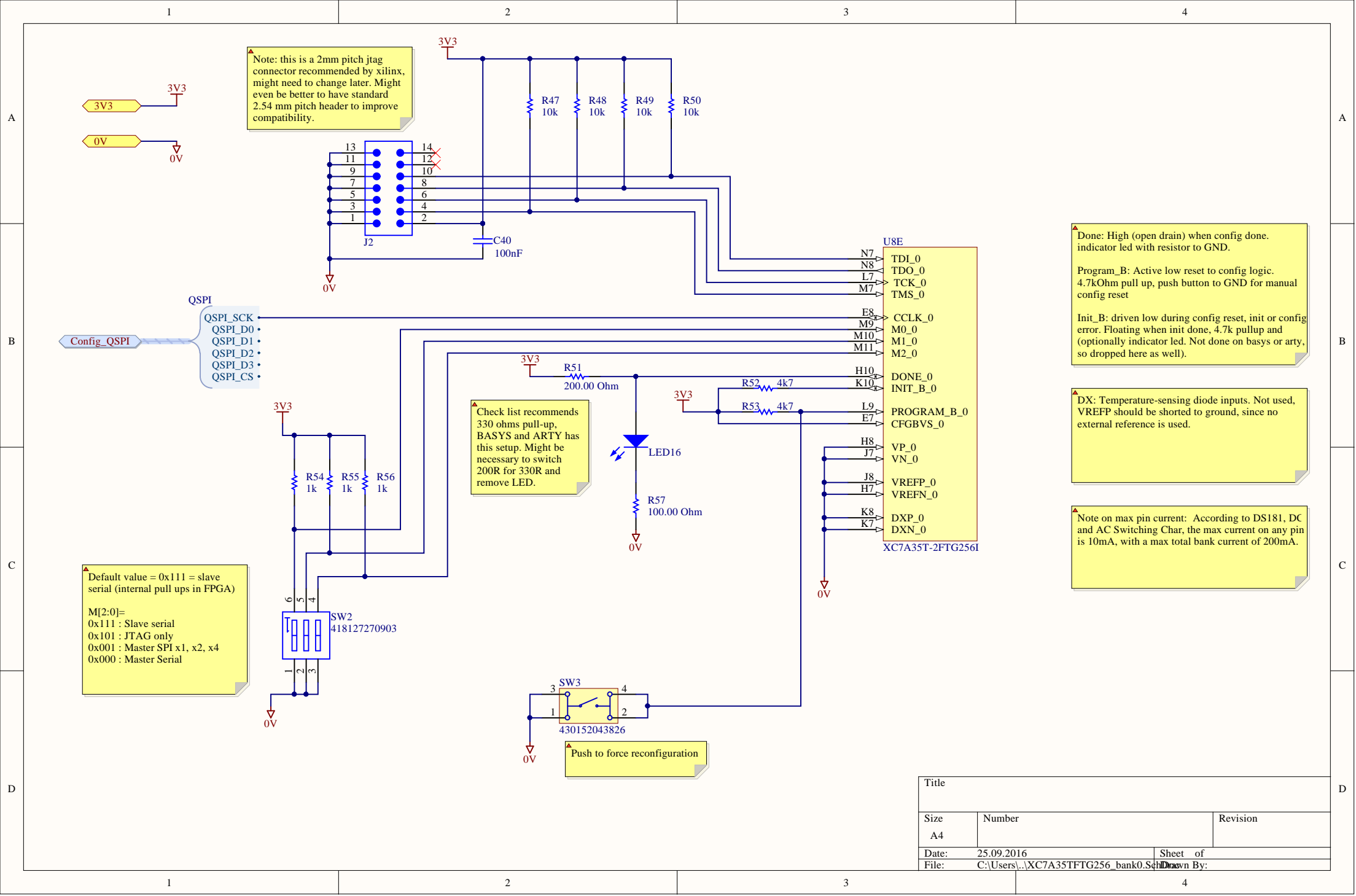
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Config_QSPI

Config_QSPI

FPGA_JTAG.GND

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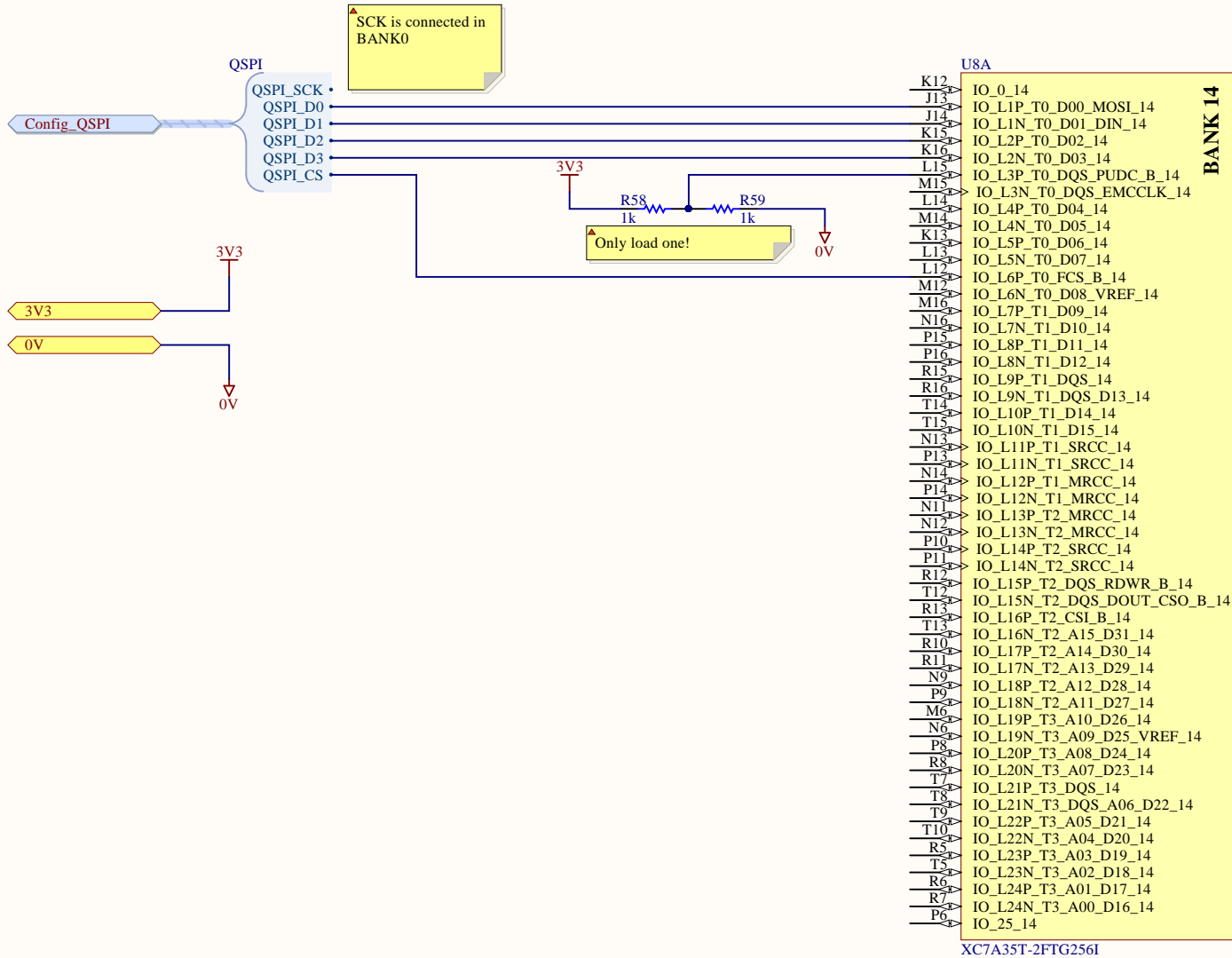
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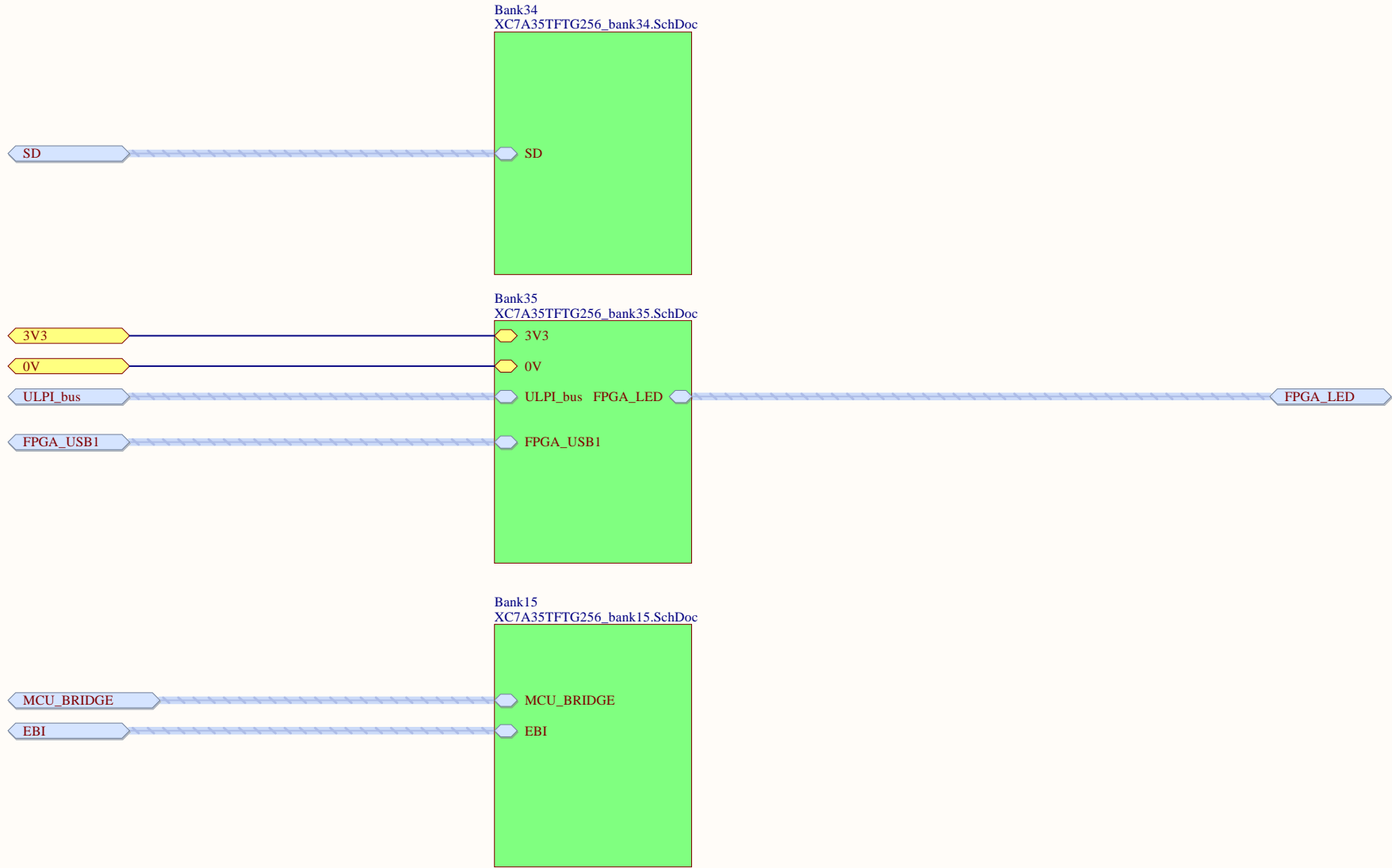
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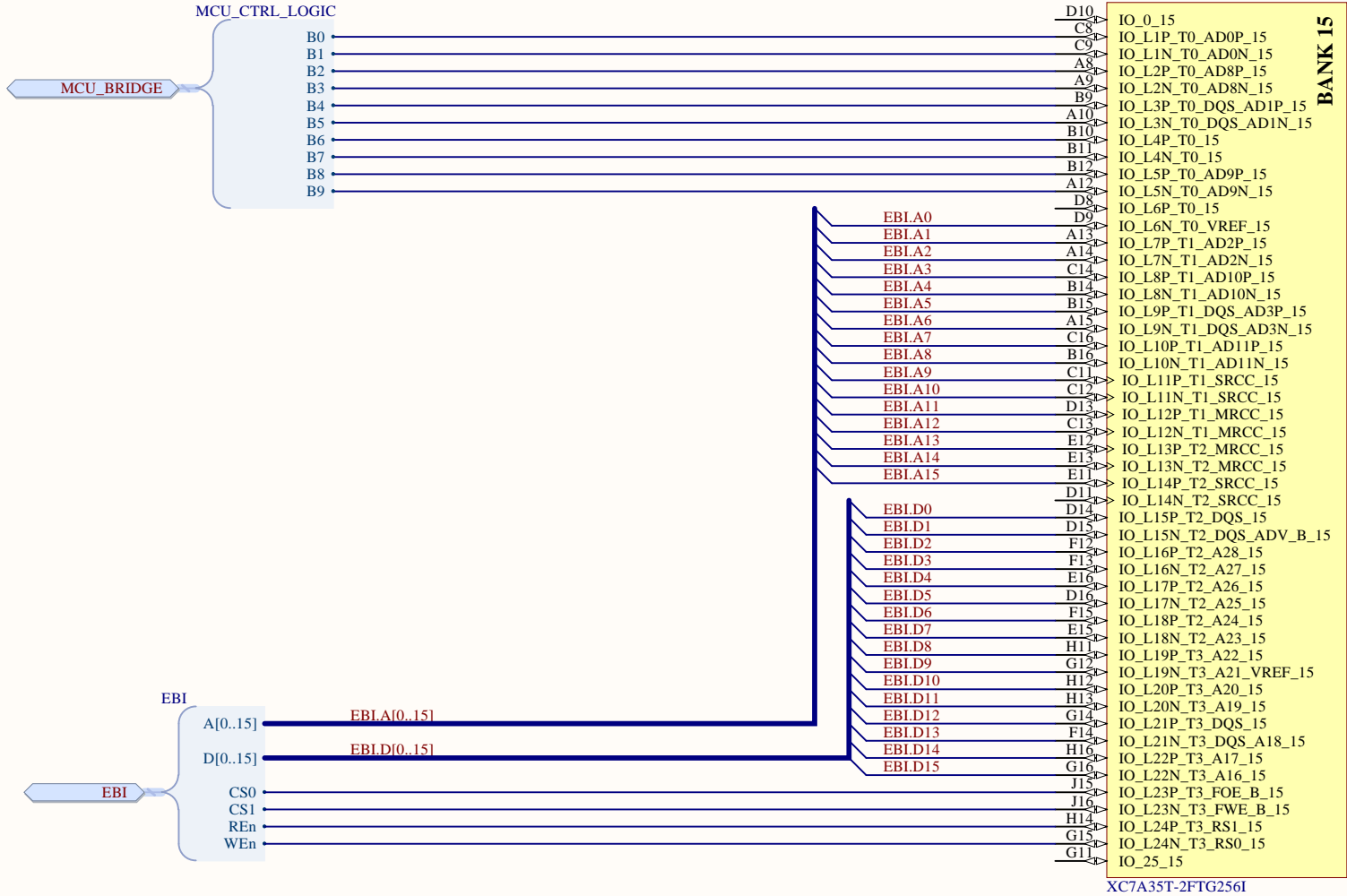
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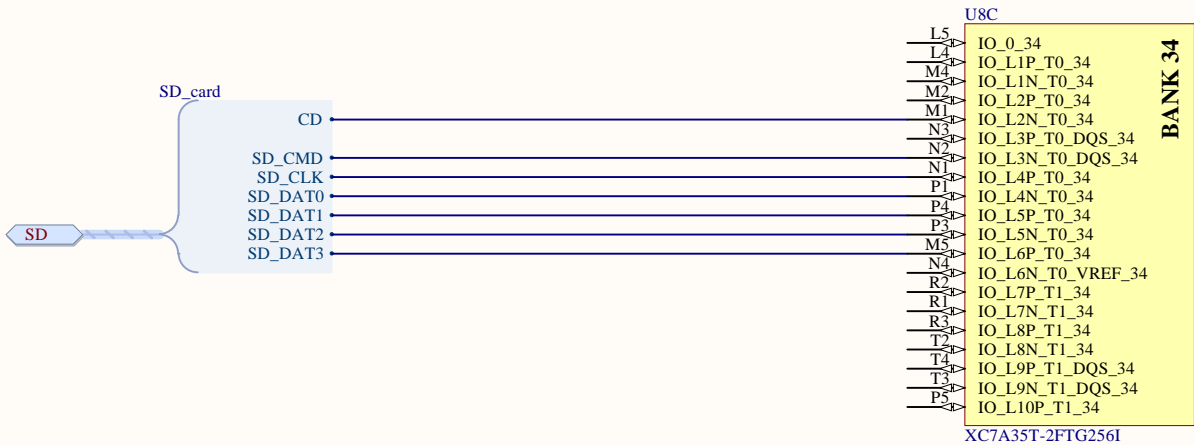
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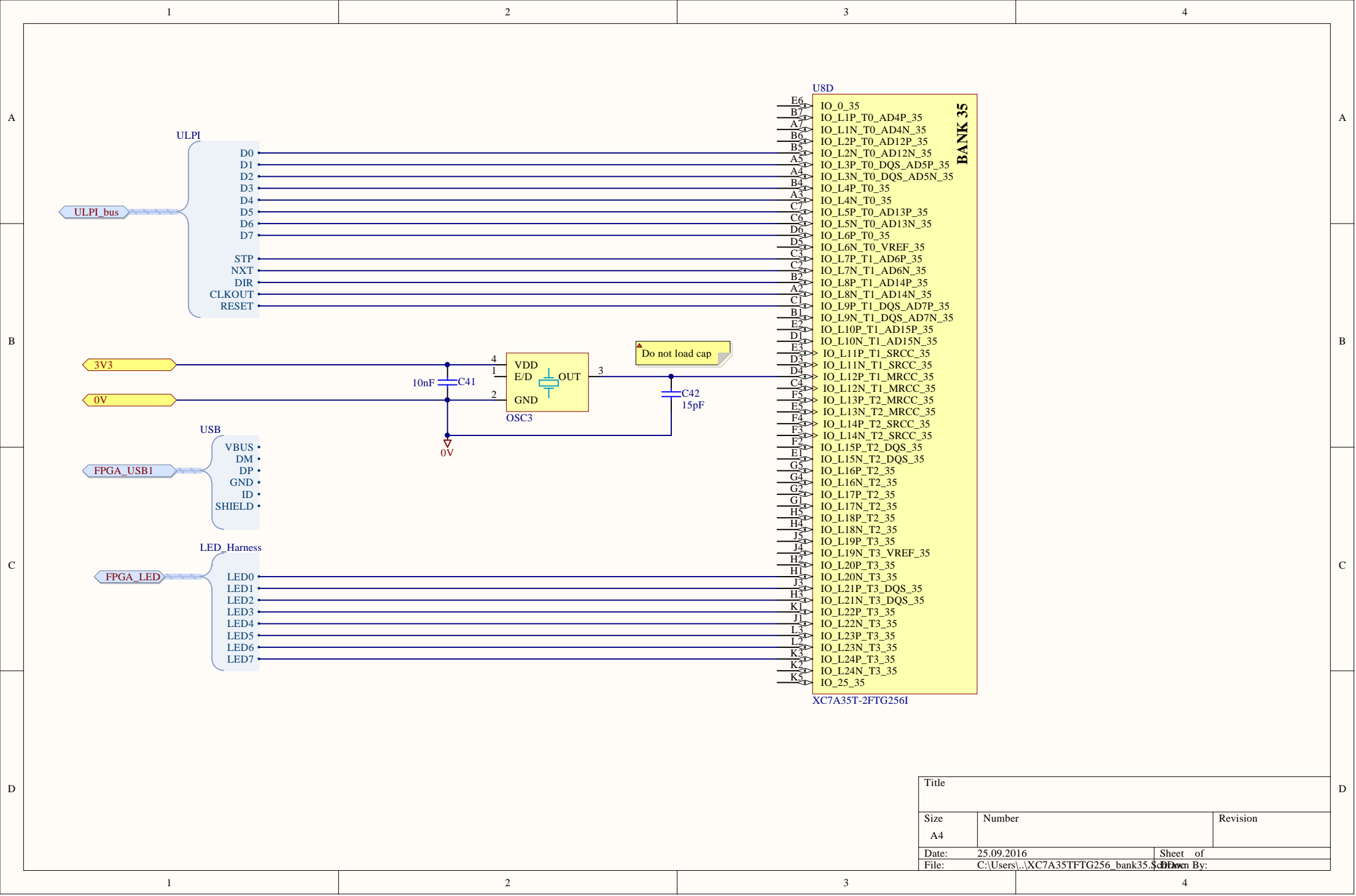
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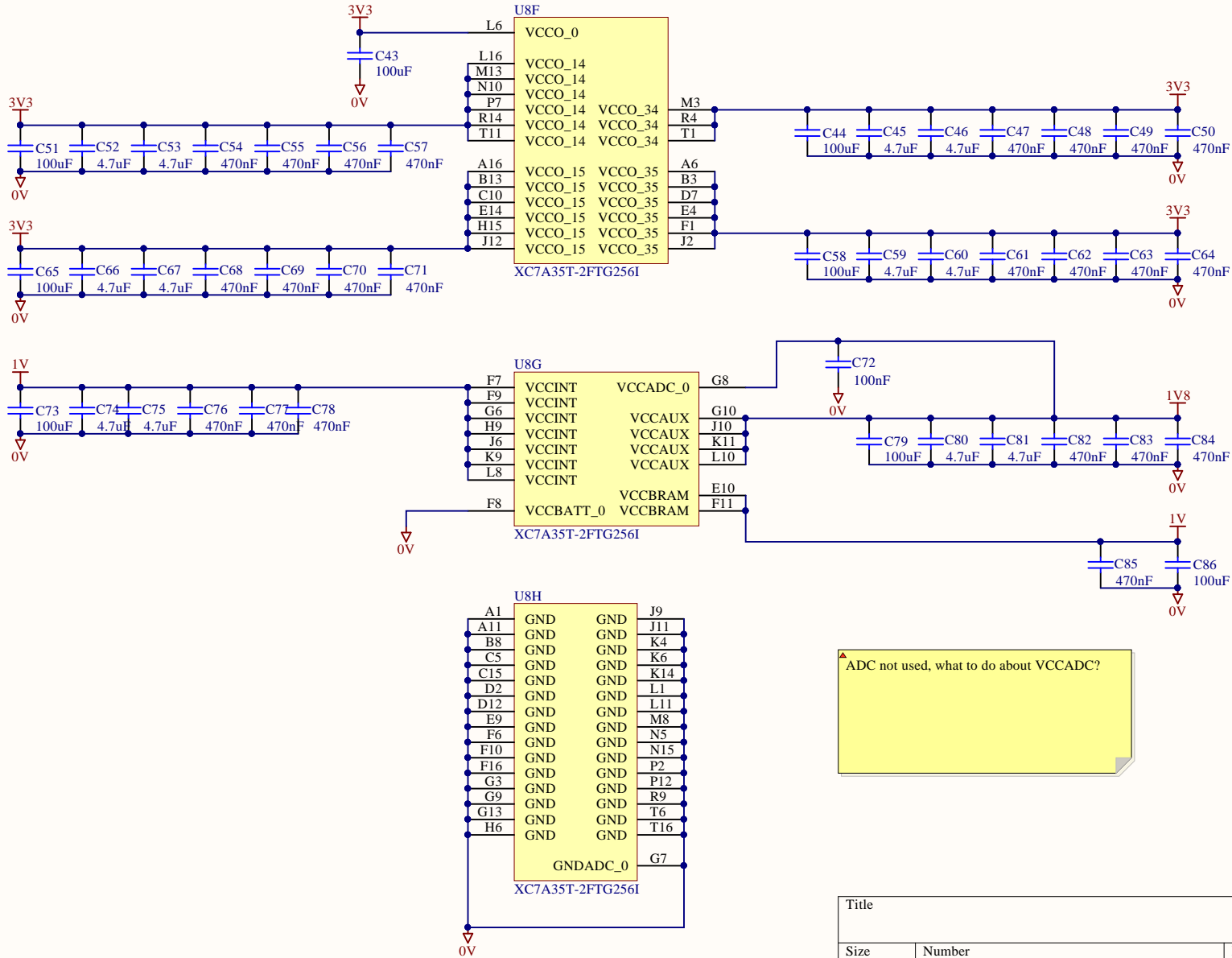
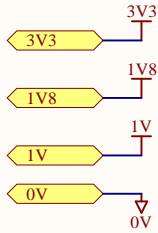
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ADC not used, what to do about VCCADC?

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