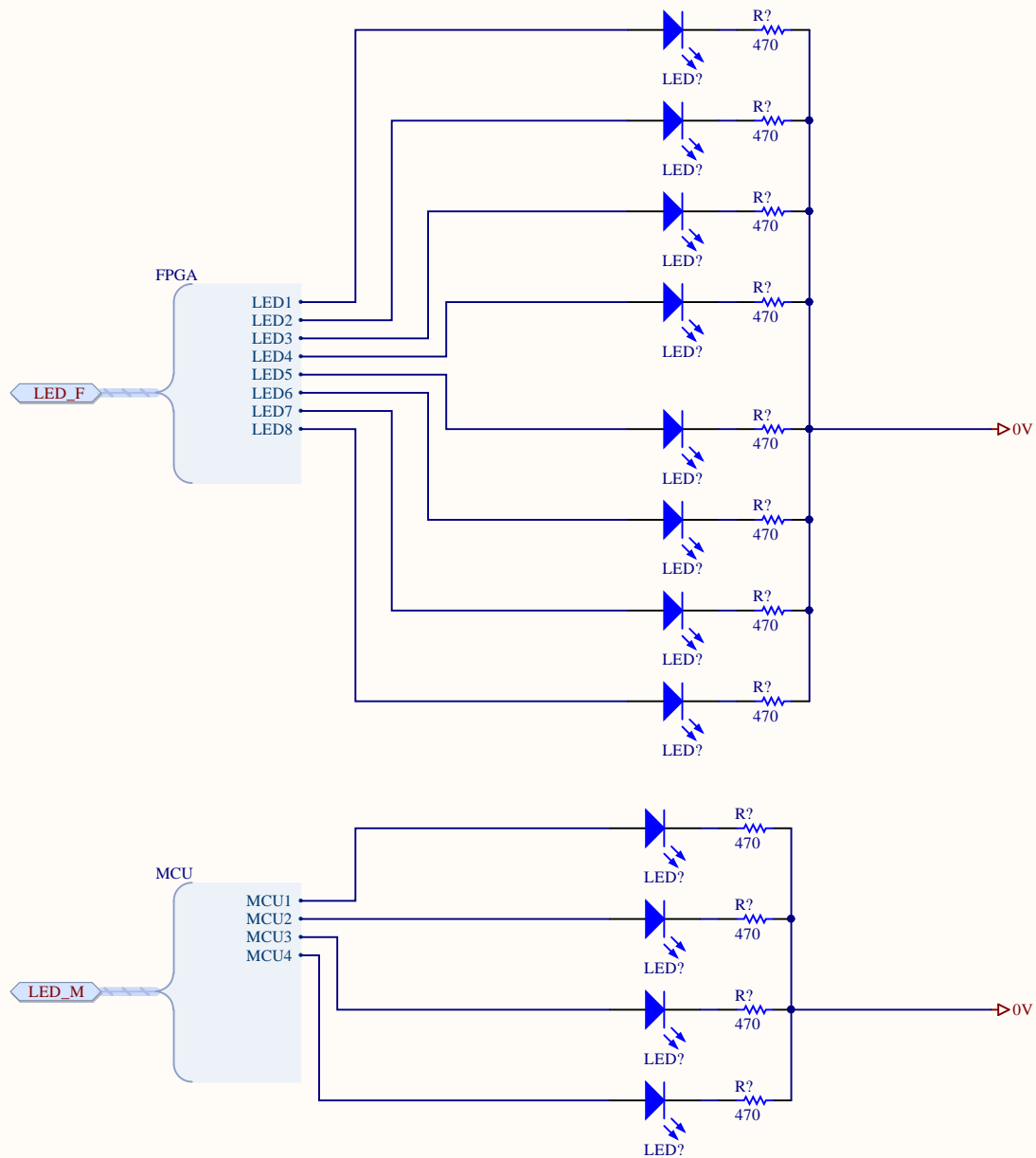


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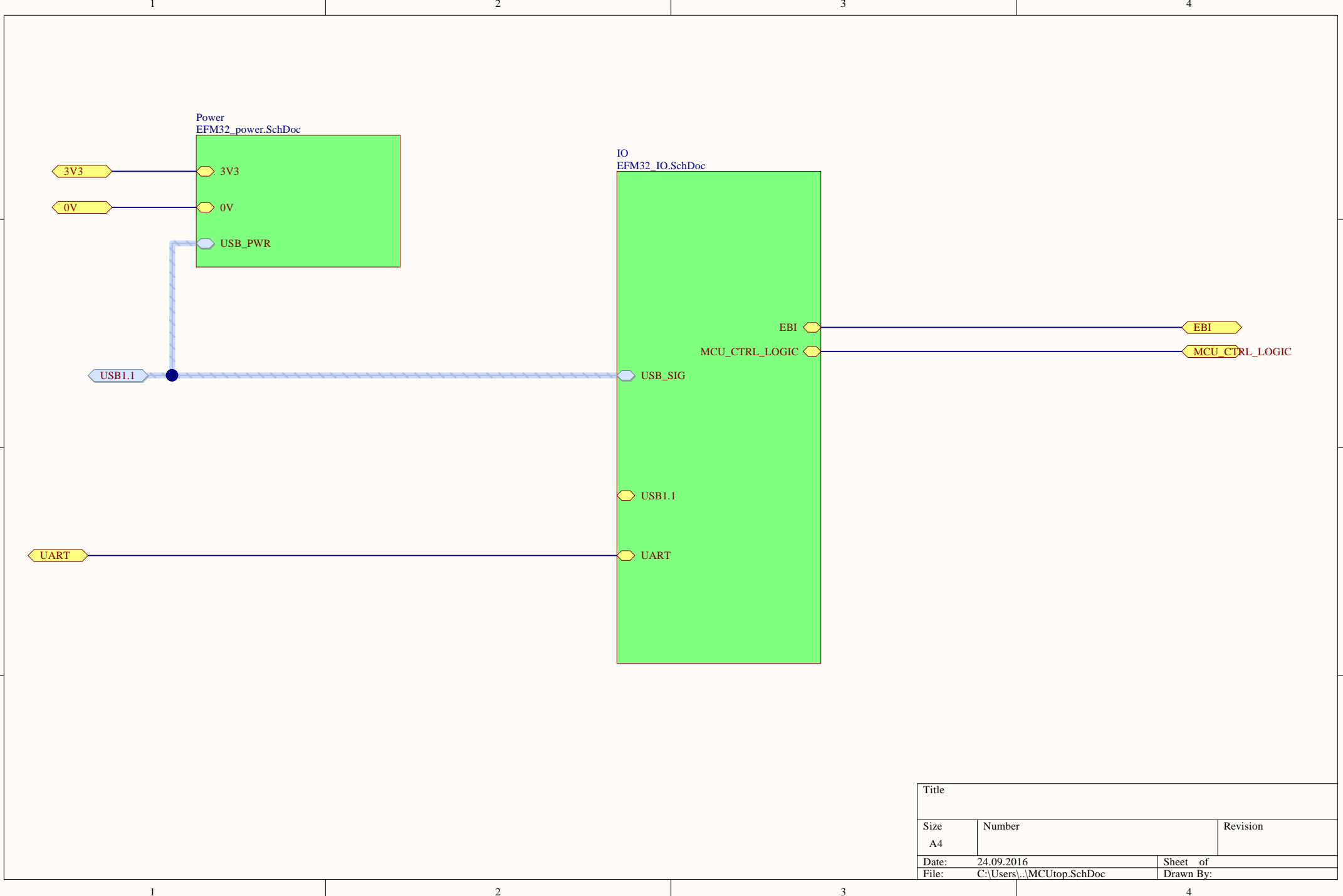
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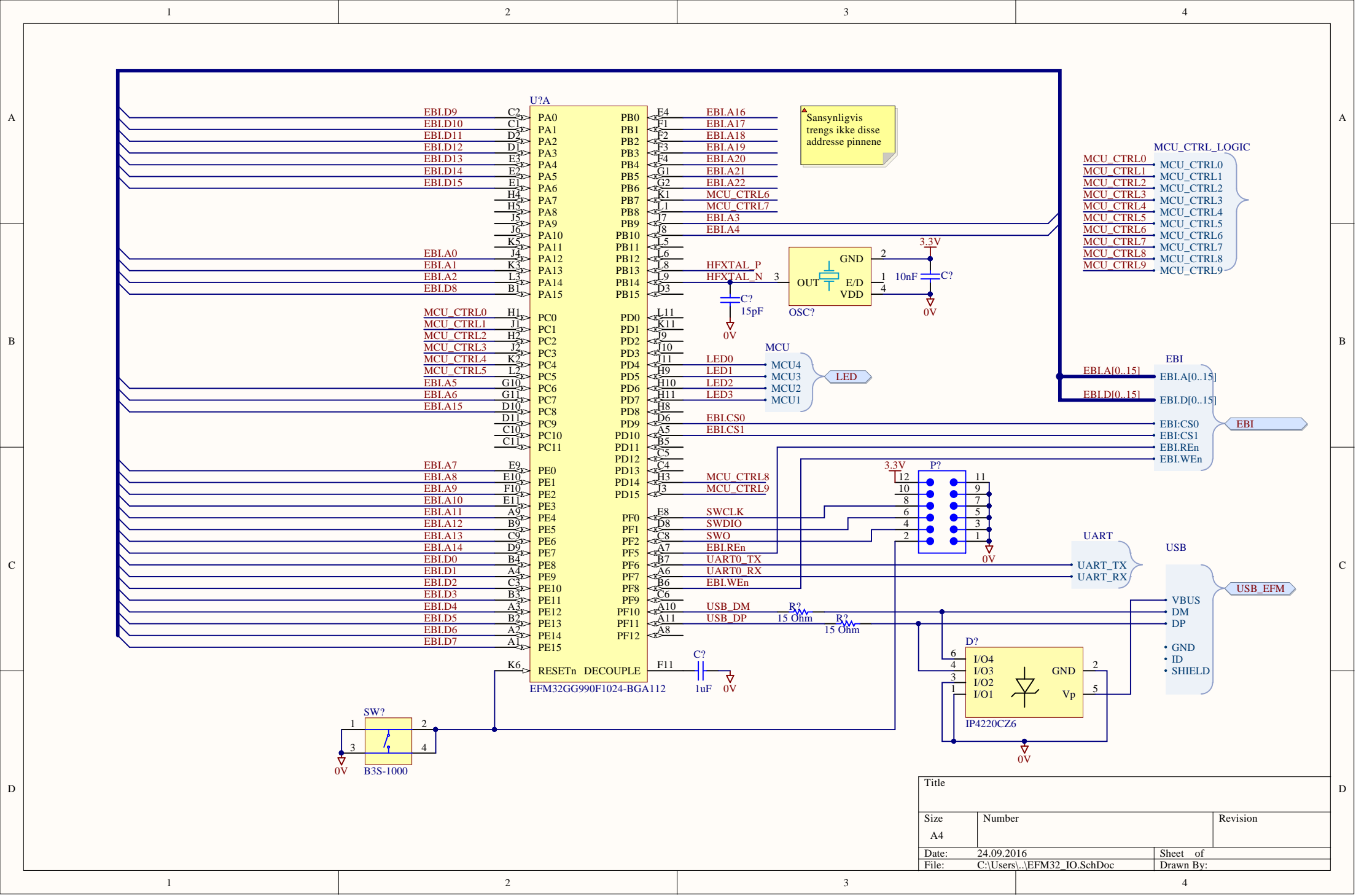
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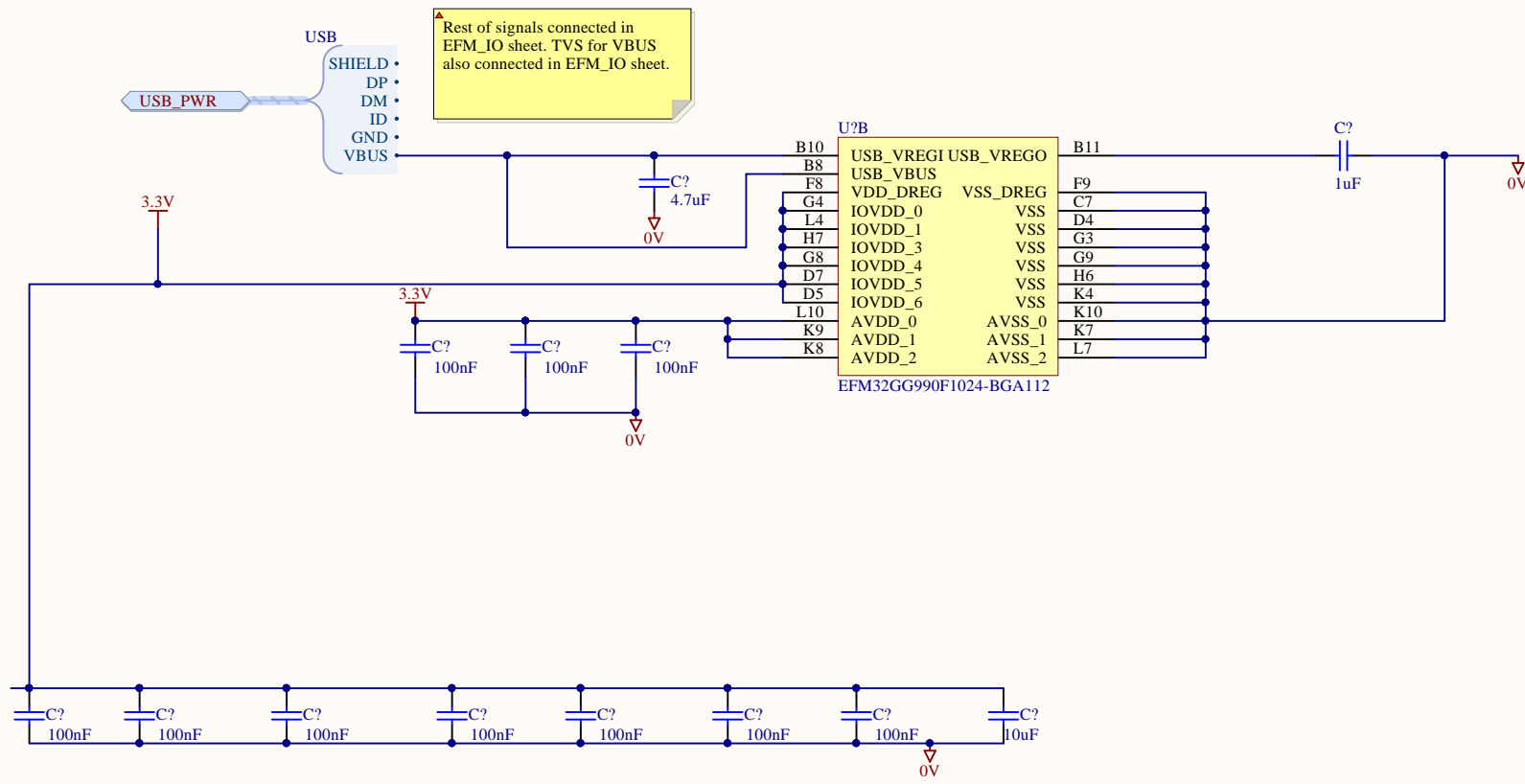
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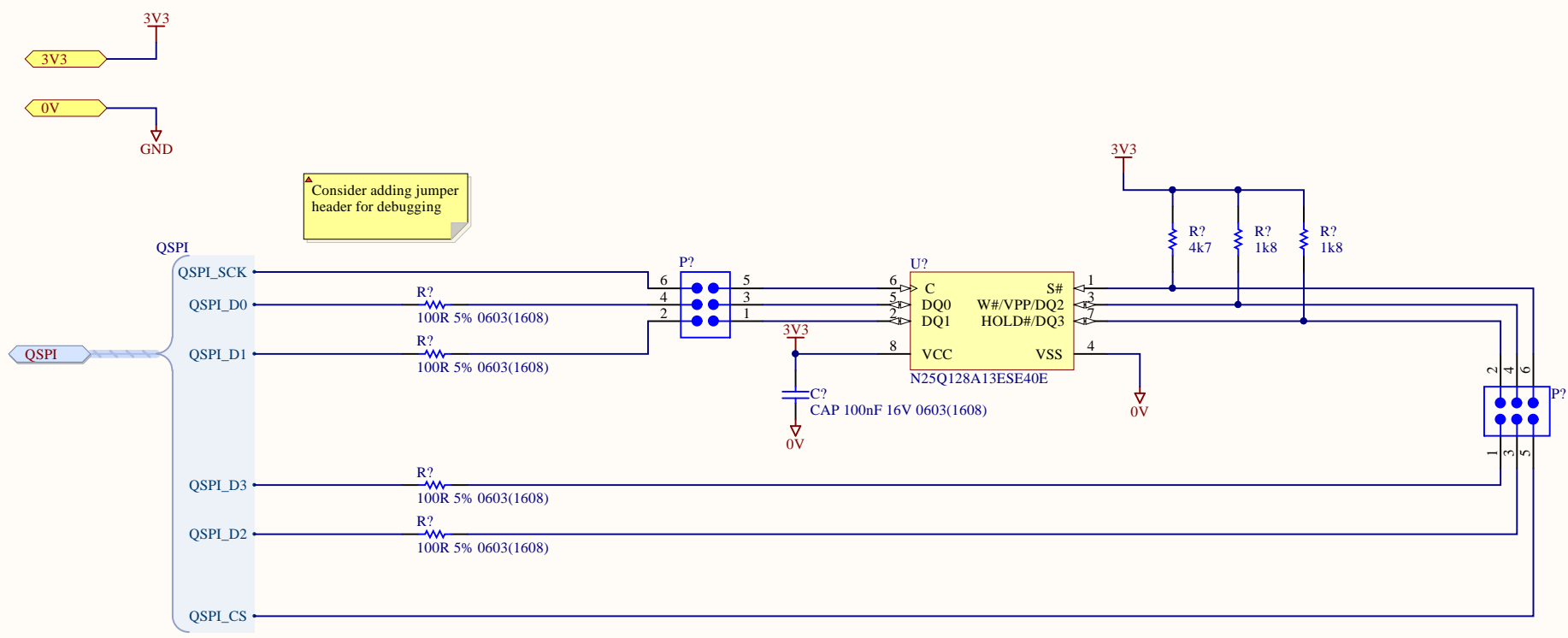


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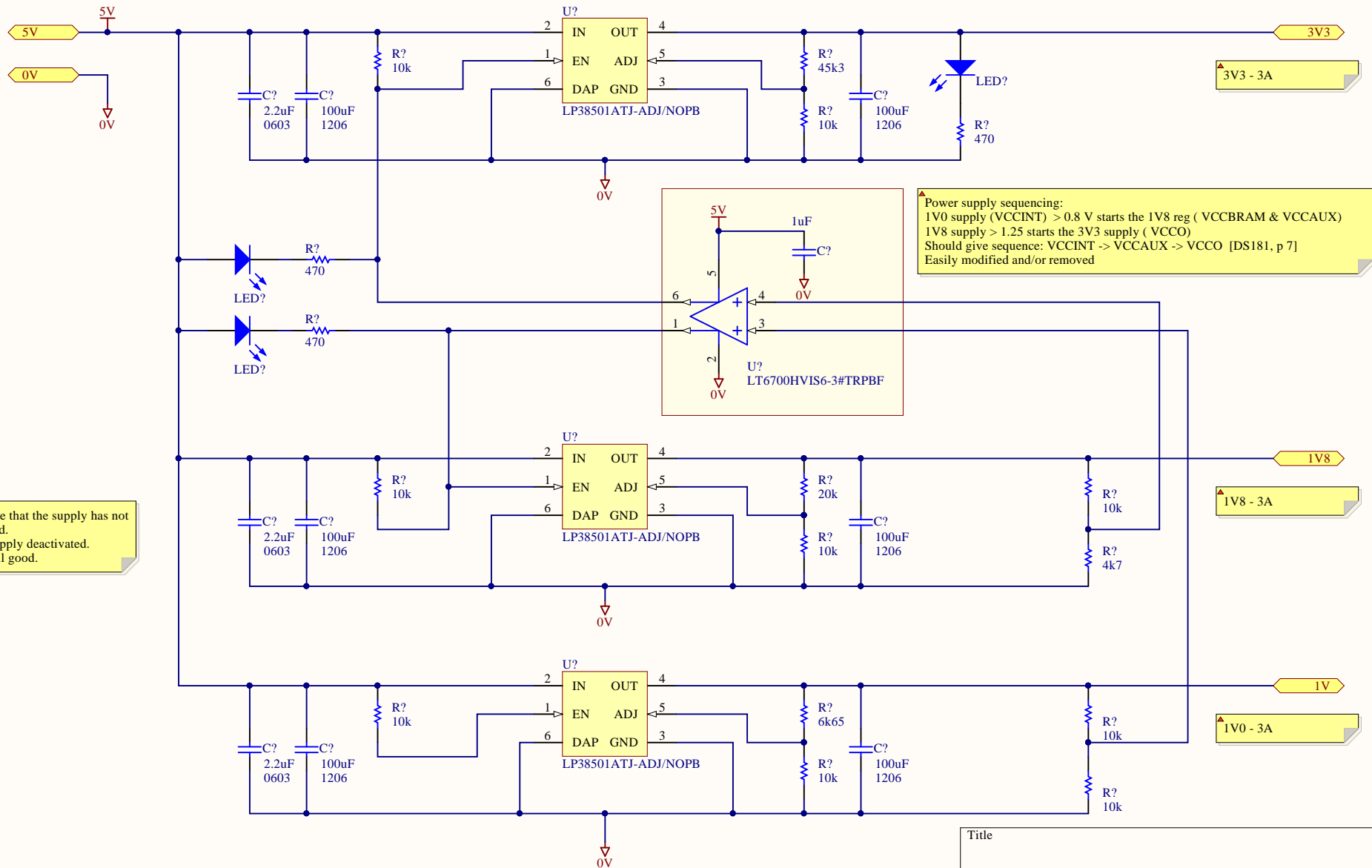




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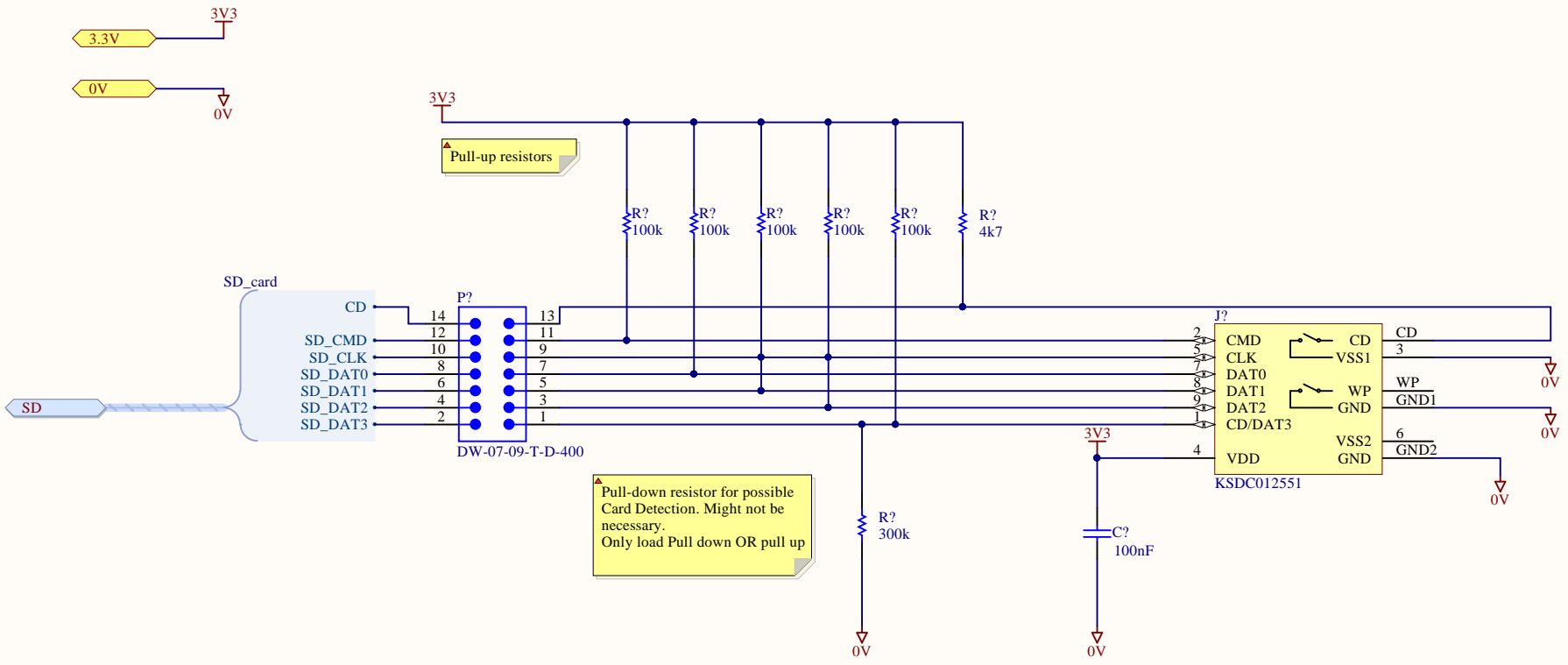
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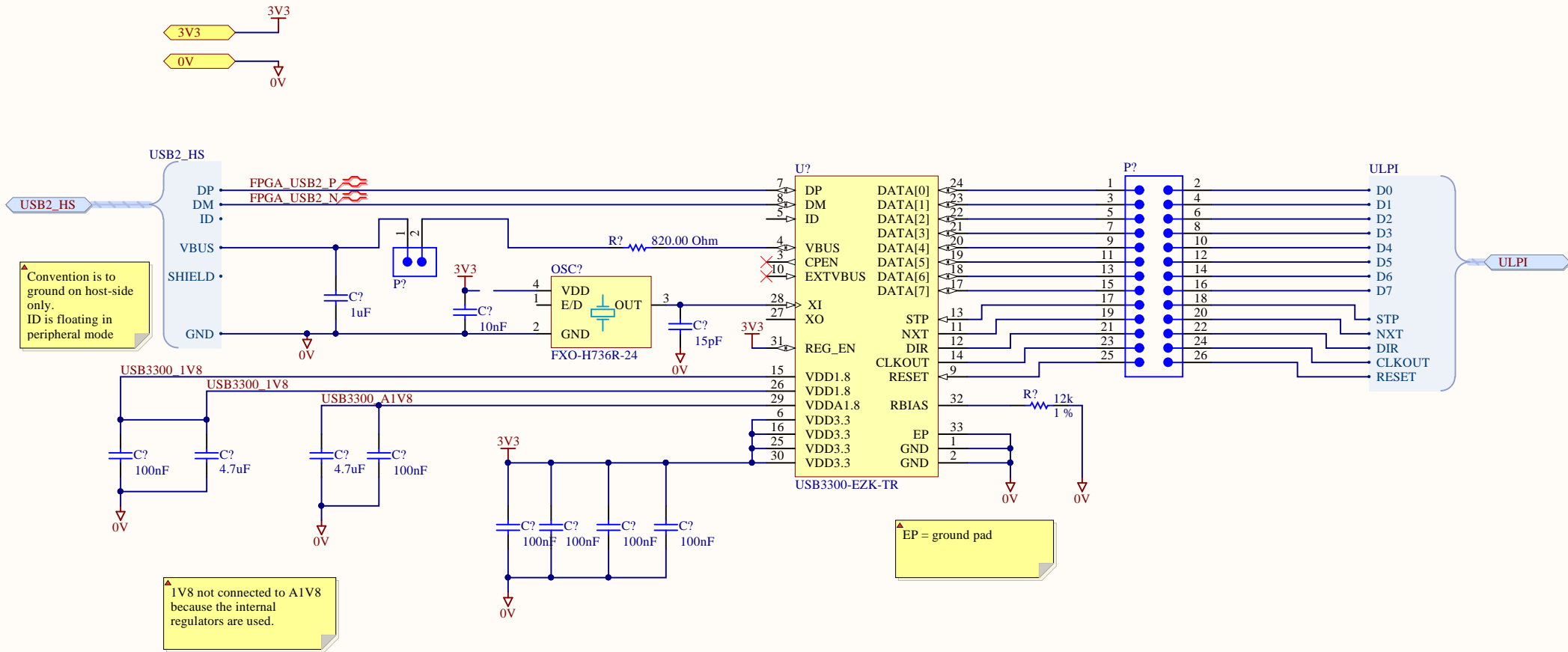
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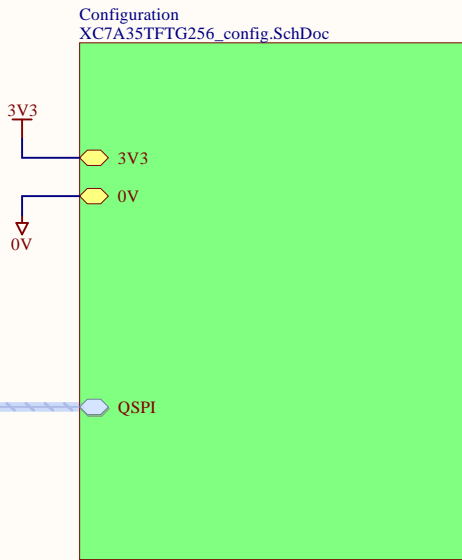
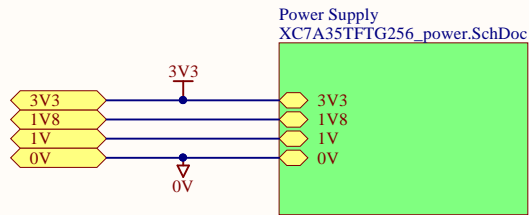
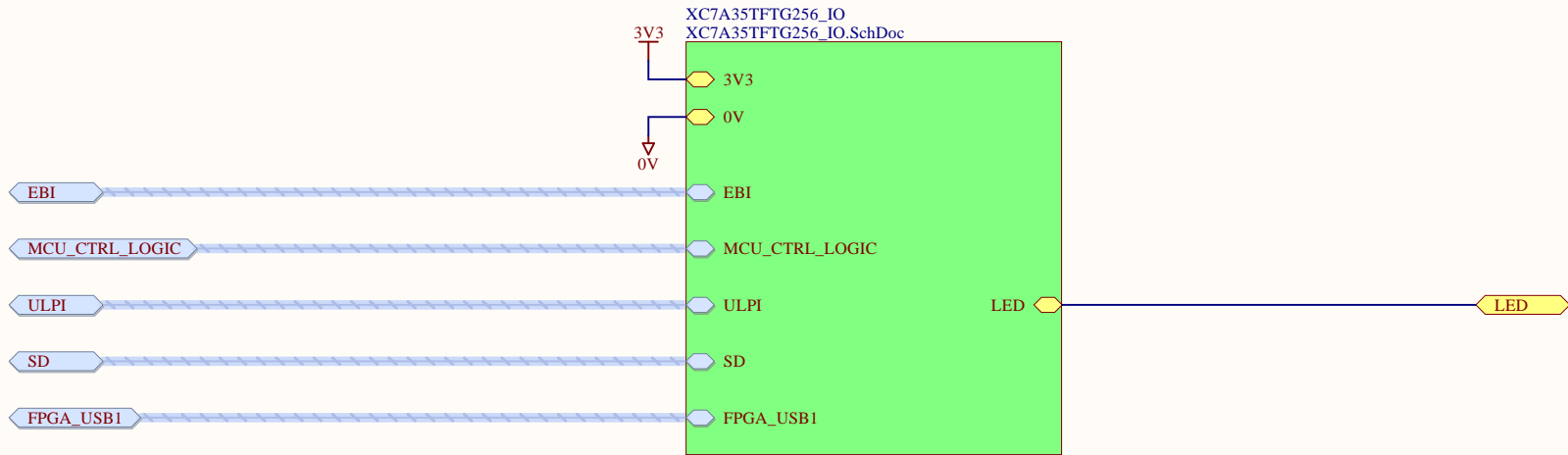
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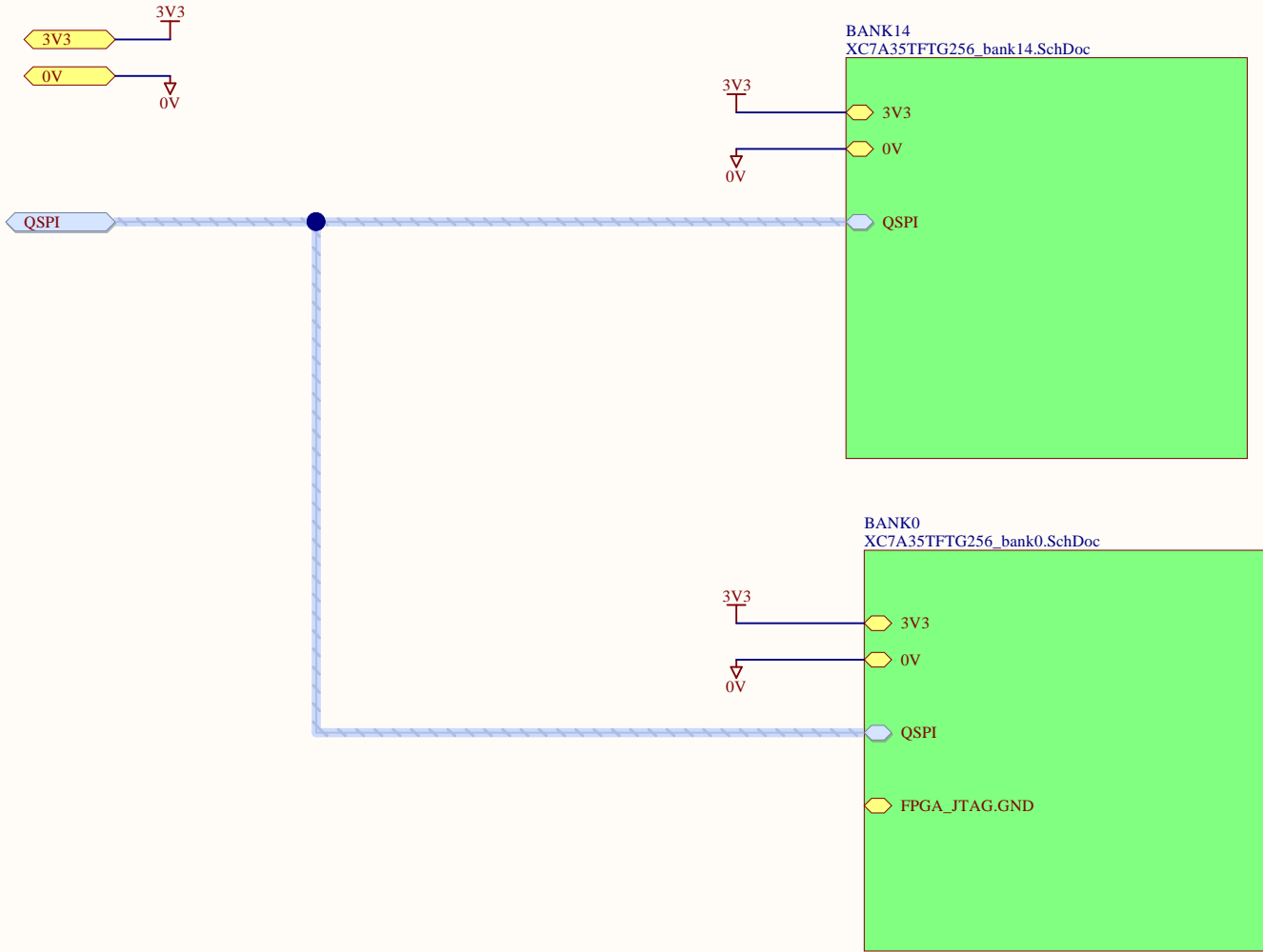
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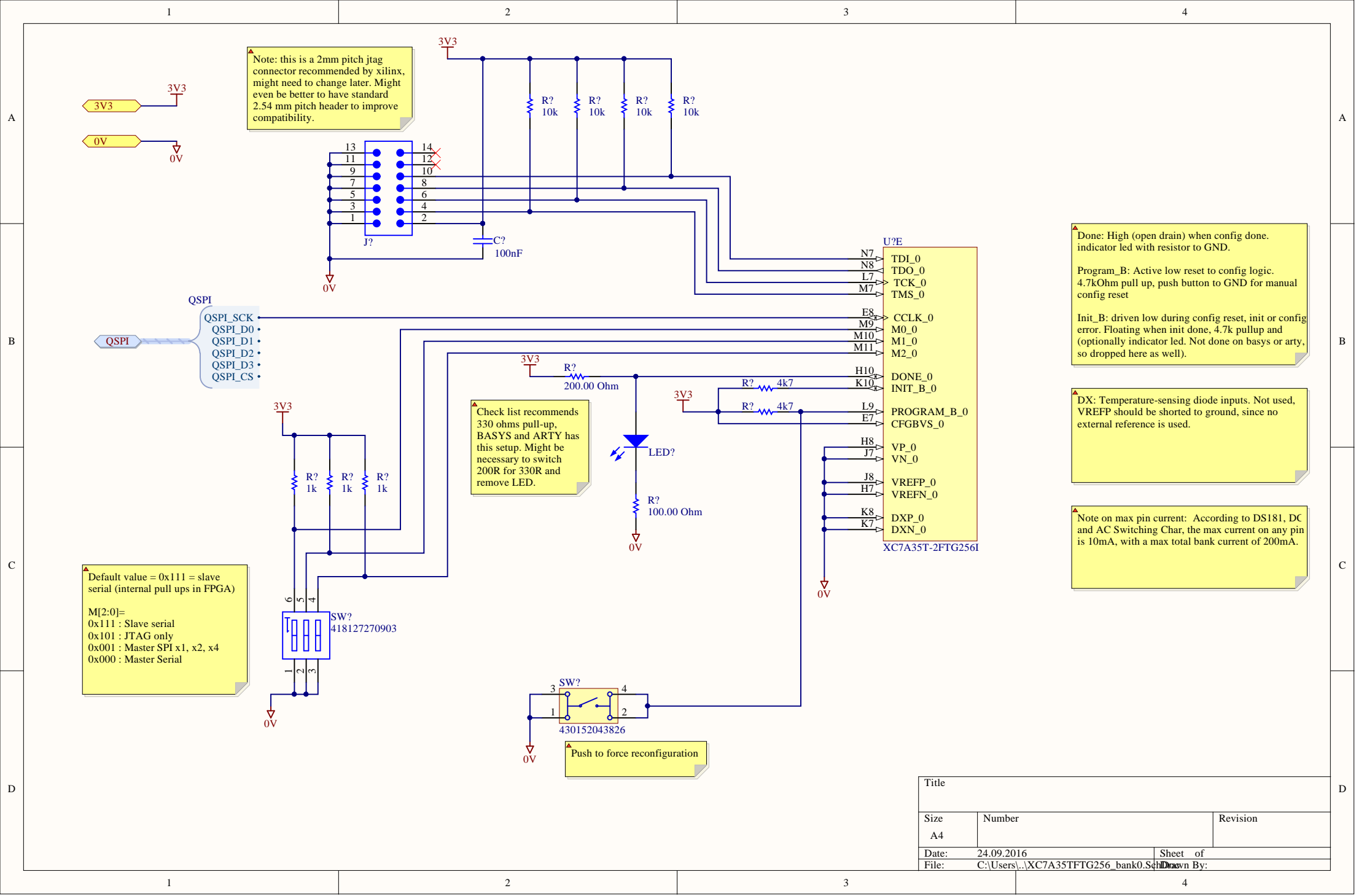
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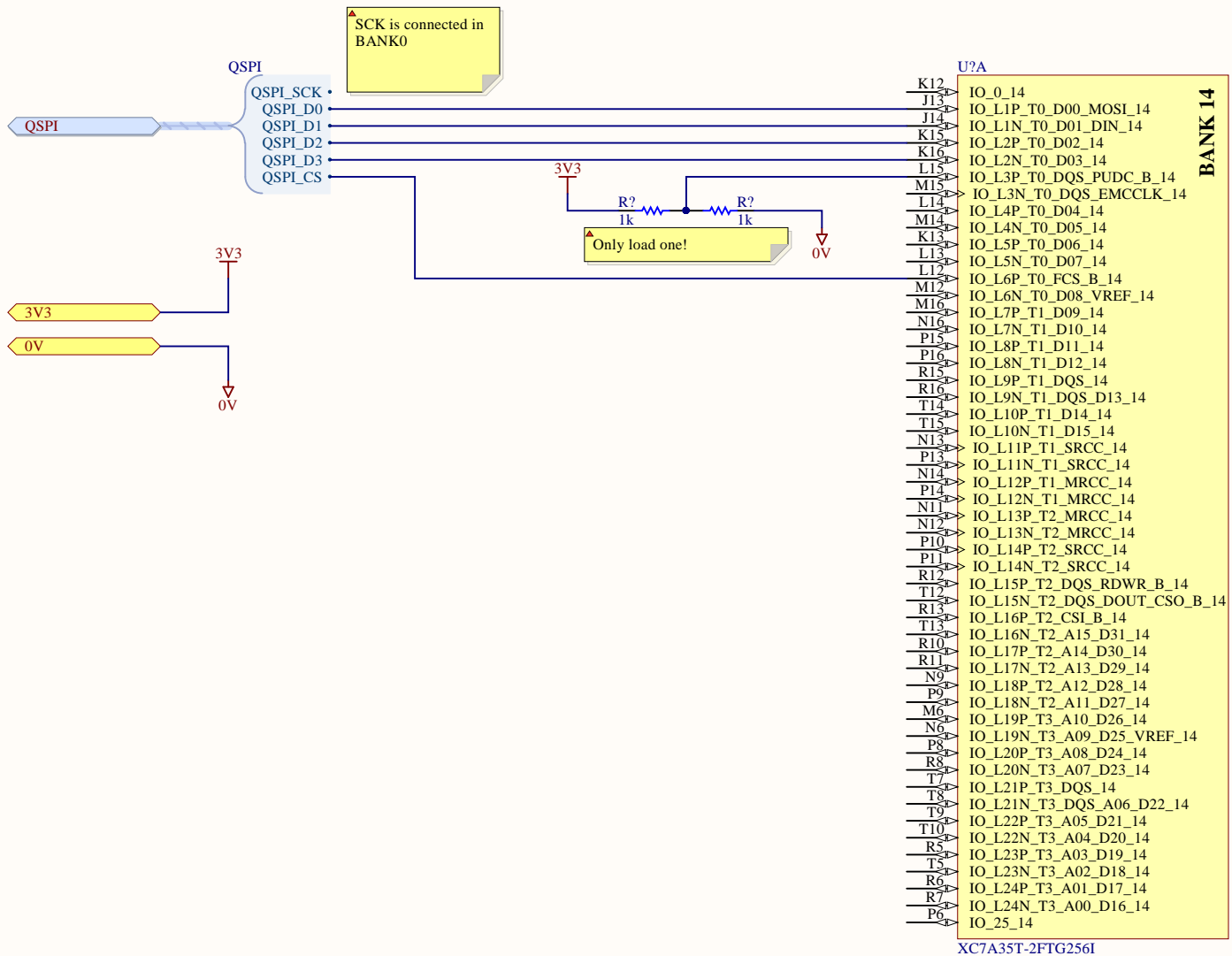
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▲ PUDC: PullUp During Config. Low = Pull-up on selectIO, High = no Pull-up on SelectIO. Must not float!

Seems like PUDC=0 means that all IO will have pull-ups during config. Could be nice to have. Most safe option is to insert resistors that will allow selection between them.

EMCCCLK: External Master Config Clock. Optional external clock source for config. Necessary?

▲ FCS_B = Flash chip select

▲ VREF = Input reference voltage for Single-ended I/O standards with differential input buffer. Se UG471, page 18.

Appears to be a reference for a schmitt-trigger or something, and can be sourced internally, if needed. However, that will mean that the max Vref = 0.9V. Not sure if this is a problem.

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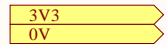
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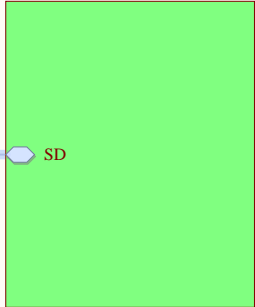
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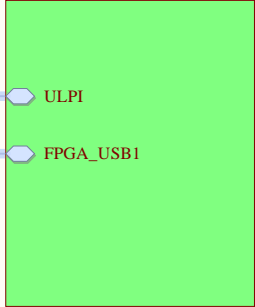
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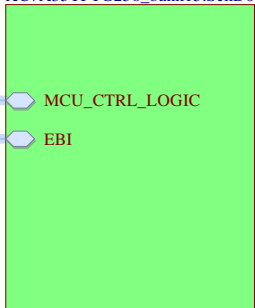
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Bank35
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Bank15
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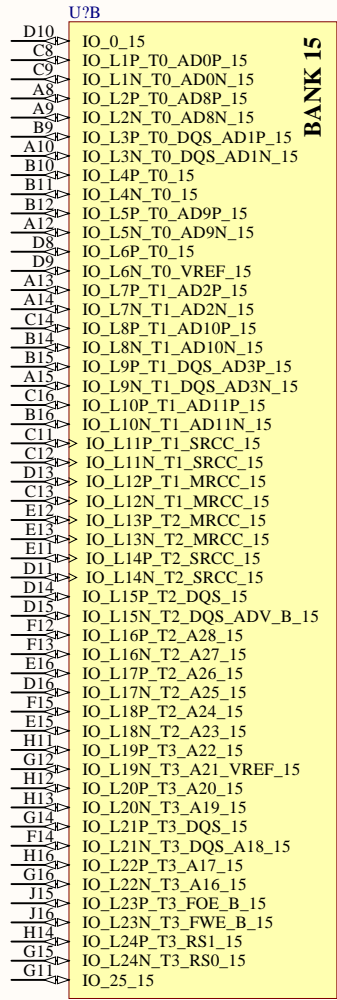
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MCU_CTRL_LOGIC

EBI



XC7A35T-2FTG256I

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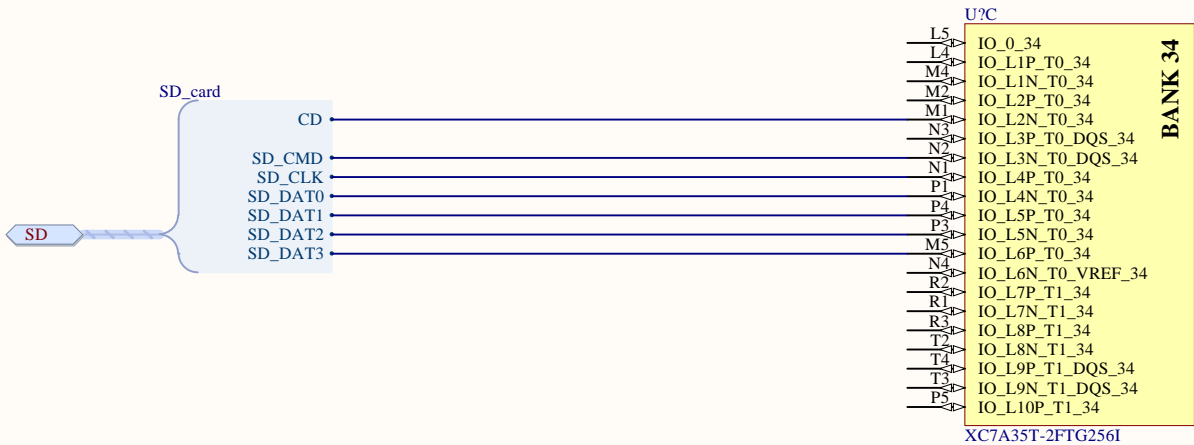
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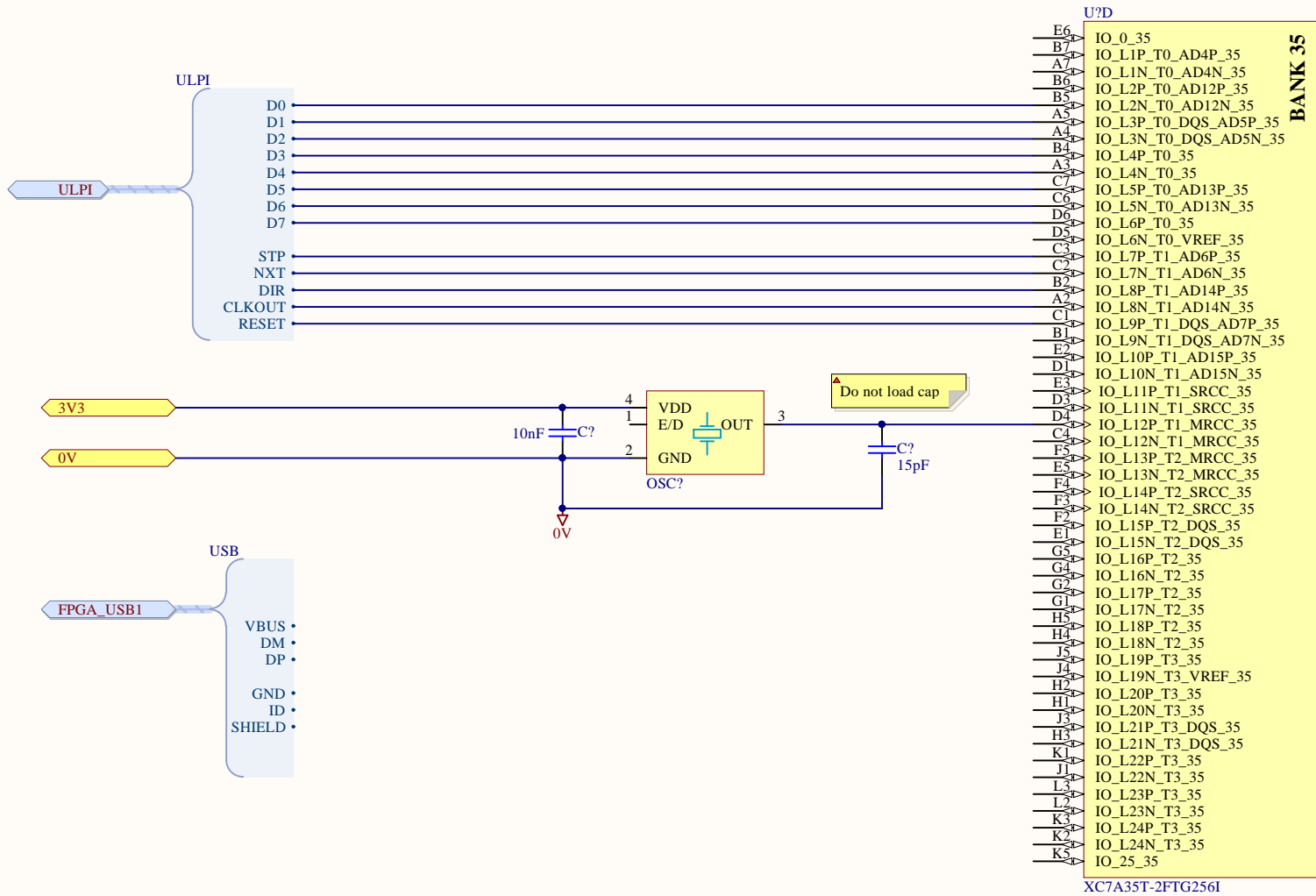
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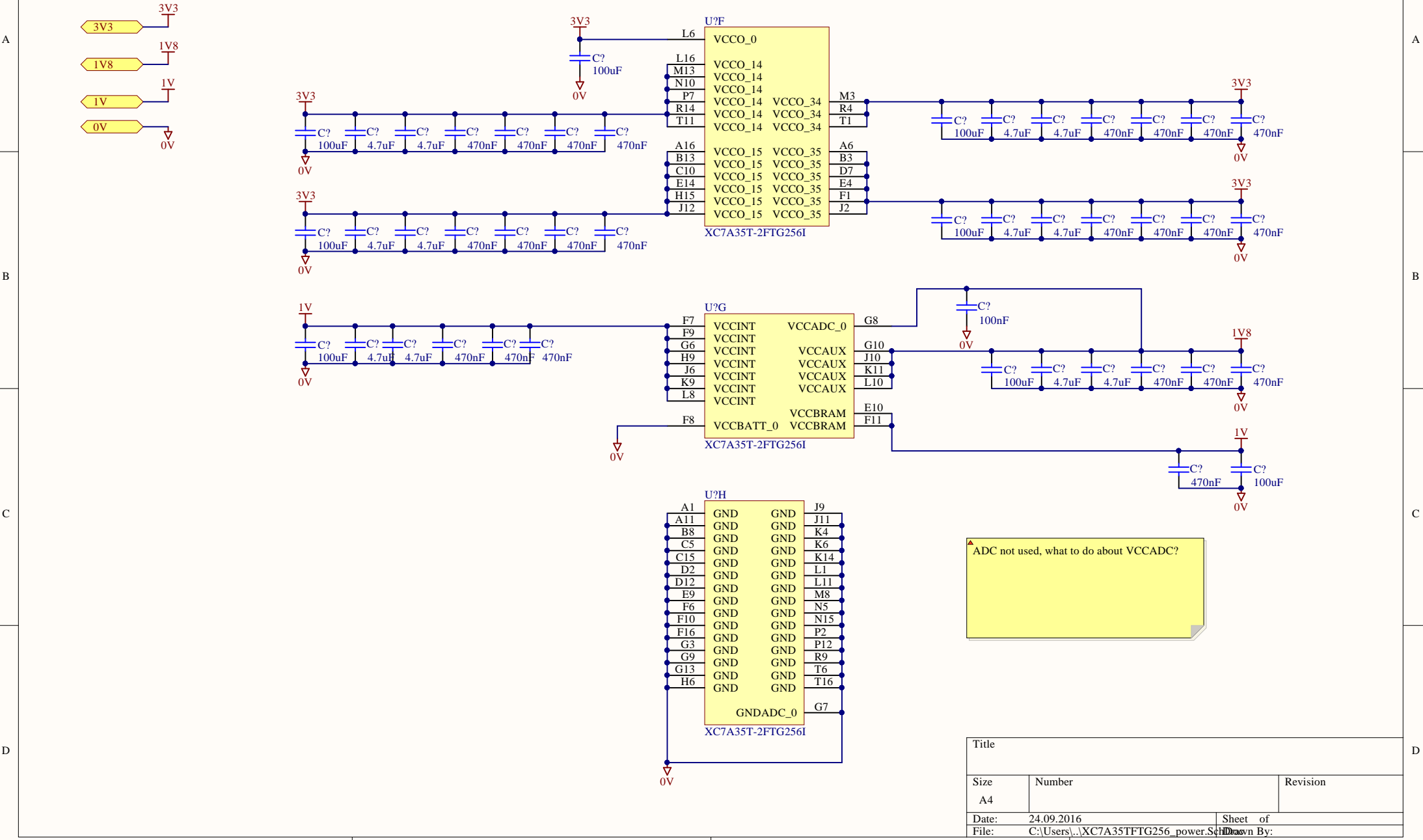
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