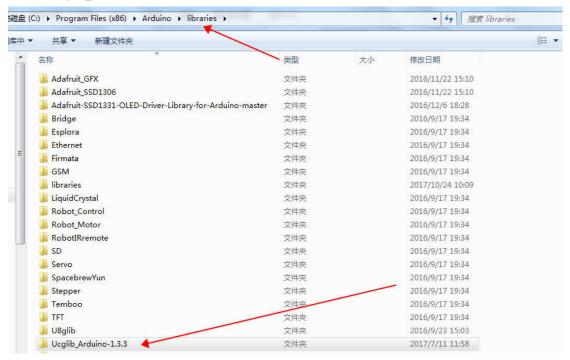
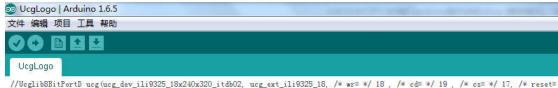
1. 将 Ucglib_Arduino-1.3.3 放 到 arduino 库 目 录 下 解 压



2. 打开 Ucglib_Arduino-1.3.3\examples\UcgLogo\UcgLogo.ino 文件

将 Ucglib_ST7735_18x128x160_SWSPI ucg(/*sclk=*/ 13, /*data=*/ 11, /*cd=*/ 9 , /*cs=*/

10, /*reset=*/ 8); 前 面 的 // 去 掉 ; 如 图 所 示



//Ucglib8BitPortD ucg(ucg_dev_ili9325_18x240x320_itdb02, ucg_ext_ili9325_18, /* wr= */ 18 , /* cd= */ 19 , /* cs= */ 17, /* reset= //Ucglib8Bit ucg(ucg_dev_ili9325_18x240x320_itdb02, ucg_ext_ili9325_18, 0, 1, 2, 3, 4, 5, 6, 7, /* wr= */ 18 , /* cd= */ 19 , /* cs

//Ucglib4WireSWSPI ucg(ucg_dev_ili9325_18x240x320_itdb02, ucg_ext_ili9325_18, /*sclk=*/ 13, /*data=*/ 11, /*cd=*/ 9 , /*cs=*/ 10, //Ucglib4WireSWSPI ucg(ucg_dev_ili9325_spi_18x240x320, ucg_ext_ili9325_spi_18, /*sclk=*/ 13, /*data=*/ 11, /*cd=*/ 9 , /*cs=*/ 10, //Ucglib3WireILI9325SWSPI ucg(ucg_dev_ili9325_spi_18x240x320, ucg_ext_ili9325_spi_18, /*sclk=*/ 13, /*data=*/ 11, /*cs=*/ 10, /*res*//Ucglib3WireILI9325SWSPI ucg(ucg_dev_ili9325_18x240x320_itdb02, ucg_ext_ili9325_18, /*sclk=*/ 13, /*data=*/ 11, /*cs=*/ 10, /*res*/ 10, /*re

Ucglib_SI7735_18x128x160_SWSPI ucg(/*sclk=*/ 13, /*data=*/ 11, /*cd=*/ 9 , /*cs=*/ 10, /*reset=*/ 8); //Ucglib_SI7735_18x128x160_HWSPI ucg(/*cd=*/ 9 , /*cs=*/ 10, /*reset=*/ 8);

//Marick TITO163 1801080108 CMCPT mar//wrall-e/ 7 /edata-e/ 6 /end-e/ 5 /enc-e/ 3 /enc-t-e/ 1).

- 3 编译下载程序
- 3. 编译下载程序
- 4. 接线说明
 - 1. GND=GND
 - 2. VCC=5V
 - 3. SCL=13
 - 4. SDA=11
 - 5. RES=8
 - 6. DC=9
 - 7. CS=10
 - 8. BLK 默认可以不接