24 Fall ECEN 704: VLSI Circuit Design Design Pre-lab Report

Lab9: Two-Stage Miller Operational Amplifier

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A _{v0}	> 50 dB > 316
CMRR	> 60 dB > 1000
GBW	> 2 MHz
PM	> 45°
Output Swing (peak-to-peak)	> 1 V
Load Capacitor	30 pF
Load Resistor	∞ (open circuit)
Power Dissipated	< 500 µW (including bias current source)
Power Supply	VDD = -VSS = 0.9 V

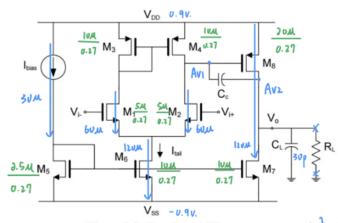


Figure 9-3: Two-Stage Miller op-amp

Avo = qm1 (rds2 || rds4) qm8 (rds7 || rds8)

$$qm1 = 648. q1 \, \mu \qquad qm8 = \sqrt{210 \, \mu \rho \omega x} \, \frac{\omega}{L_8} = 908.82 \, \mu$$

$$rds2 = \frac{1}{\lambda ID} = 114k \quad rds4 = 91k \quad 50.6k$$

$$rds7 = 57k \quad rds8 = 45k \quad 25.14k$$

Avo = 750.2

$$AcM = -\frac{1}{\lambda rds6 \, qm3} = 1.932 \, m \quad qm3 = 454 \, \mu$$

$$CMRR = \frac{Avo}{AcM} = 388k \quad 320 \, \rho F$$
(0.32 n F)

$$wGBW = \frac{qm1}{Cc} = \frac{648. \, q1 \, \mu}{Cc} > 2M \, Hz \quad Cc < 3.244 \, X10^{-10}$$

$$WGBW = 2.595 \, M$$

$$PM = 90^{\circ} - \tan^{-1} \frac{wGBW}{wpz} - \tan^{-1} \frac{wGBW}{wz}$$

$$WP2 = \frac{-qm8}{CL} = 30.2 M \qquad W2 = \frac{gm8}{CC} = 3.63 M$$

$$PM = 90^{\circ} - 4.91^{\circ} - 35.5^{\circ} = 49.59$$

$$Swiny: -v.9 + Veff7 \sim 0.9 - Veff8 = -v.716 \sim 0.64$$

$$\sqrt{\frac{2TD}{Kn}} = 0.184 \qquad \sqrt{\frac{2TD}{Kp}} = 0.26$$

Power: 1.8 X (3UM + 2 Itail) = 486 W

