```
----------------------------------------------------------------

  ***********        IMPORTANT RELEASE NOTES        ************

  You are using a version of the UVM library that has been compiled
  with `UVM_NO_DEPRECATED undefined.
  See http://www.eda.org/svdb/view.php?id=3313 for more details.

  You are using a version of the UVM library that has been compiled
  with `UVM_OBJECT_MUST_HAVE_CONSTRUCTOR undefined.
  See http://www.eda.org/svdb/view.php?id=3770 for more details.

      (Specify +UVM_NO_RELNOTES to turn off this notice)

UVM_INFO ../tb/top.sv(23) @ 0: reporter [TOP] Starting test
UVM_INFO @ 0: reporter [RNTST] Running test mix_sequence_test...
UVM_INFO @ 0: reporter [UVMTOP] UVM testbench topology:
----------------------------------------------------------
Name                     Type                    Size  Value
----------------------------------------------------------
uvm_test_top             mix_sequence_test        -    @2629
  driver                 htax_tx_driver_c         -    @3330
    rsp_port             uvm_analysis_port        -    @3429
    seq_item_port        uvm_seq_item_pull_port   -    @3380
  sequencer              htax_sequencer_c         -    @2691
    rsp_export           uvm_analysis_export      -    @2749
    seq_item_export      uvm_seq_item_pull_imp    -    @3299
    arbitration_queue    array                    0    -
    lock_queue           array                    0    -
    num_last_reqs        integral                 32   'd1
    num_last_rsps        integral                 32   'd1
----------------------------------------------------------
```

```
------------------------------------------------------

UVM_INFO ../tests/mix_sequence_test.sv(53) @ 0: uvm_test_top [mix_sequence_test] Starti
ng Mix Sequence Test
xmsim: *W,OLDURR: The default algorithm for $urandom_range and randcase is used. It can
 have distribution problems..
UVM_INFO ../tb/htax_seqs.sv(40) @ 0: uvm_test_top.sequencer@@fix_dest_port_seq_ [fix_de
st_port_seq] Executing fix destination port sequence with 5 transactions
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
--------------------------------------------------------------------------------------
------
Name                     Type            Size  Value
--------------------------------------------------------------------------------------
------
req                      htax_packet_c    -    @3563
  delay                  integral         32   'hd
  dest_port              integral         32   'h3
  vc                     integral         2    'h2
  length                 integral         32   'h5
  data                   da(integral)     5    -
    [0]                  integral         64   'h7168048db1e917b1
    [1]                  integral         64   'h1cd77e2ea666c3cd
    [2]                  integral         64   'he630395e5a725ea3
    [3]                  integral         64   'h865e28dec5b036da
    [4]                  integral         64   'h78065920eb7352d1
  begin_time             time             64   0
```

Name	Type	Size	Value
delay	integral	32	'hd
dest_port	integral	32	'h3
vc	integral	2	'h2
length	integral	32	'h5
data	da(integral)	5	-
[0]	integral	64	'h7168048db1e917b1
[1]	integral	64	'h1cd77e2ea666c3cd
[2]	integral	64	'he630395e5a725ea3
[3]	integral	64	'h865e28dec5b036da
[4]	integral	64	'h78065920eb7352d1
begin_time	time	64	0
depth	int	32	'd2
parent sequence (name)	string	18	fix_dest_port_seq_
parent sequence (full name)	string	41	uvm_test_top.sequencer.fix_dest_port_seq_
sequencer	string	22	uvm_test_top.sequencer

```
--------------------------------------------------------------------------------
------
UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
--------------------------------------------------------------------------------
```

Name	Type	Size	Value
req	htax_packet_c	-	@3682
delay	integral	32	'h14
dest_port	integral	32	'h3
vc	integral	2	'h1
length	integral	32	'h1f
data	da(integral)	31	-
[0]	integral	64	'h7ae91b12374ac8cd
[1]	integral	64	'h1d4f8eb8e76f3220
[2]	integral	64	'h2ebe4f1a4af2d4f4
[3]	integral	64	'h903a50e900eea8fc
[4]	integral	64	'h6708c0d8bdf11318
...	...	...	...
[26]	integral	64	'h77cc248e9d77d935
[27]	integral	64	'h1066ba2cf1a26b48
[28]	integral	64	'hf815490ff25d8e5c
[29]	integral	64	'hd63be16cf3455f66
[30]	integral	64	'h684e18496c82faca

```
        [1]                    integral        64      'h1d4f8eb8e76f3220

        [2]                    integral        64      'h2ebe4f1a4af2d4f4

        [3]                    integral        64      'h903a50e900eea8fc

        [4]                    integral        64      'h6708c0d8bdf11318

        ...                    ...             ...     ...

        [26]                   integral        64      'h77cc248e9d77d935

        [27]                   integral        64      'h1066ba2cf1a26b48

        [28]                   integral        64      'hf815490ff25d8e5c

        [29]                   integral        64      'hd63be16cf3455f66

        [30]                   integral        64      'h684e18496c82faca

    begin_time                 time            64      0

    depth                      int             32      'd2

    parent sequence (name)     string          18      fix_dest_port_seq_

    parent sequence (full name)  string        41      uvm_test_top.sequencer.fix_dest_por
t_seq_
    sequencer                  string          22      uvm_test_top.sequencer
------------------------------------------------------------------------------------
------

UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
------------------------------------------------------------------------------------
```

```
------------------------------------------------------------------------------------------
-----
Name                        Type            Size  Value
------------------------------------------------------------------------------------------
-----
req                         htax_packet_c   -     @3713

    delay                   integral        32    'h13

    dest_port               integral        32    'h1

    vc                      integral        2     'h3

    length                  integral        32    'h8

    data                    da(integral)    8     -

        [0]                 integral        64    'hcf2a07a2da12c134

        [1]                 integral        64    'h6e2a1449dbf86fcc

        [2]                 integral        64    'h3b06ba7a760f6cd0

        [3]                 integral        64    'h695277cdf7cf9273

        [4]                 integral        64    'h1af1af315e148c5e

        [5]                 integral        64    'h8029d68a63bb1680

        [6]                 integral        64    'hf6ce57b730594b14

        [7]                 integral        64    'h6c36991bc2bbc98f

    begin_time              time            64    0

    depth                   int             32    'd2
```

```
     length                        integral      32    'h8
     data                          da(integral)  8     -
       [0]                         integral      64    'hcf2a07a2da12c134
       [1]                         integral      64    'h6e2a1449dbf86fcc
       [2]                         integral      64    'h3b06ba7a760f6cd0
       [3]                         integral      64    'h695277cdf7cf9273
       [4]                         integral      64    'h1af1af315e148c5e
       [5]                         integral      64    'h8029d68a63bb1680
       [6]                         integral      64    'hf6ce57b730594b14
       [7]                         integral      64    'h6c36991bc2bbc98f
     begin_time                    time          64    0
     depth                         int           32    'd2
     parent sequence (name)        string        17    short_packet_seq_
     parent sequence (full name)   string        40    uvm_test_top.sequencer.short_packet
_seq_
     sequencer                     string        22    uvm_test_top.sequencer
--------------------------------------------------------------------------------------
-----
UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
--------------------------------------------------------------------------------------
```

```
--------------------------------------------------------------------------------------
----------------
Name                      Type          Size  Value
--------------------------------------------------------------------------------------
----------------
req                       htax_packet_c  -     @3720
  delay                   integral      32    'h3
  dest_port               integral      32    'h1
  vc                      integral      2     'h3
  length                  integral      32    'h30
  data                    da(integral)  48    -
    [0]                   integral      64    'h5f0c6a38ea85cb64
    [1]                   integral      64    'h7aca2a4b09533dc5
    [2]                   integral      64    'h177d09617ed22f55
    [3]                   integral      64    'h883b2e657064b731
    [4]                   integral      64    'h548a7de6b3d34819
    ...                   ...           ...   ...
    [43]                  integral      64    'h3dd561afe753863a
    [44]                  integral      64    'h2d326840c51b03b5
    [45]                  integral      64    'hf7414bd31b2e7008
    [46]                  integral      64    'h77a14b2cd83df015
```

```
   [1]                       integral      64    'h7aca2a4b09533dc5
   [2]                       integral      64    'h177d09617ed22f55
   [3]                       integral      64    'h883b2e657064b731
   [4]                       integral      64    'h548a7de6b3d34819
   ...                       ...           ...   ...
   [43]                      integral      64    'h3dd561afe753863a
   [44]                      integral      64    'h2d326840c51b03b5
   [45]                      integral      64    'hf7414bd31b2e7008
   [46]                      integral      64    'h77a14b2cd83df015
   [47]                      integral      64    'h8990f68d88e96dcb
  begin_time                 time          64    0
  depth                      int           32    'd2
  parent sequence (name)     string        28    long_packet_short_delay_seq_
  parent sequence (full name) string       51    uvm_test_top.sequencer.long_packet_
short_delay_seq_
  sequencer                  string        22    uvm_test_top.sequencer
-------------------------------------------------------------------------------------
----------------
UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
-------------------------------------------------------------------------------------
```

```
-------------------------------------------------------------------------------------
----------
Name                        Type          Size  Value
-------------------------------------------------------------------------------------
----------
req                         htax_packet_c  -    @3640
  delay                     integral      32    'h9
  dest_port                 integral      32    'h2
  vc                        integral      2     'h1
  length                    integral      32    'h23
  data                      da(integral)  35    -
   [0]                      integral      64    'he72c7b5946e942e7
   [1]                      integral      64    'ha7a04e12aca6b0b7
   [2]                      integral      64    'hfbb55cbad7aca700
   [3]                      integral      64    'h950f6df8647a6b4f
   [4]                      integral      64    'h52085d0c6ca0279c
   ...                      ...           ...   ...
   [30]                     integral      64    'h86226400bc45061c
   [31]                     integral      64    'ha9215a58da2bc237
   [32]                     integral      64    'h3bc1e673472938cd
   [33]                     integral      64    'h8134eccfcc2ec8d8
   [34]                     integral      64    'hbff487a1117b9b94
```

```
          [1]                     integral      64      'ha7a04e12aca6b0b7
          [2]                     integral      64      'hfbb55cbad7aca700
          [3]                     integral      64      'h950f6df8647a6b4f
          [4]                     integral      64      'h52085d0c6ca0279c
          ...                     ...           ...     ...
          [30]                    integral      64      'h86226400bc45061c
          [31]                    integral      64      'ha9215a58da2bc237
          [32]                    integral      64      'h3bc1e673472938cd
          [33]                    integral      64      'h8134eccfcc2ec8d8
          [34]                    integral      64      'hbff487a1117b9b94
      begin_time                  time          64      0
      depth                       int           32      'd2
      parent sequence (name)      string        22      med_packet_fix_vc_seq_
      parent sequence (full name) string        45      uvm_test_top.sequencer.med_packet_f
ix_vc_seq_
      sequencer                   string        22      uvm_test_top.sequencer
----------------------------------------------------------------------------------
----------
UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
----------------------------------------------------------------------------------
----------
```

```
----------------------------------------------------------------------------------
Name                            Type            Size    Value
----------------------------------------------------------------------------------
req                             htax_packet_c   -       @3736
  delay                         integral        32      'h8
  dest_port                     integral        32      'h3
  vc                            integral        2       'h2
  length                        integral        32      'h25
  data                          da(integral)    37      -
    [0]                         integral        64      'ha77750a847153534
    [1]                         integral        64      'hfbdf0a059376baf9
    [2]                         integral        64      'ha067eb0612374529
    [3]                         integral        64      'h953377edb33b3074
    [4]                         integral        64      'h5c38d7315db5406d
    ...                         ...             ...     ...
    [32]                        integral        64      'h892ae0d16aecf2e5
    [33]                        integral        64      'ha6f231180f2e0975
    [34]                        integral        64      'hdf68ac0bc603b25f
    [35]                        integral        64      'h590f62dd0024e849
    [36]                        integral        64      'h887bb0279b1f8baa
  begin_time                    time            64      0
  depth                         int             32      'd2
  parent sequence (name)        string          11      random_seq_
  parent sequence (full name)   string          34      uvm_test_top.sequencer.random_seq_
  sequencer                     string          22      uvm_test_top.sequencer
----------------------------------------------------------------------------------
UVM_INFO ../tb/htax_tx_driver_c.sv(43) @ 0: uvm_test_top.driver [htax_tx_driver_c] Ende
d Driving Data Packet to DUT
UVM_INFO ../tb/htax_tx_driver_c.sv(42) @ 0: uvm_test_top.driver [htax_tx_driver_c] Inpu
t Data Packet to DUT :
```

```
--- UVM Report catcher Summary ---


Number of demoted UVM_FATAL reports   :     0
Number of demoted UVM_ERROR reports   :     0
Number of demoted UVM_WARNING reports:     0
Number of caught UVM_FATAL reports    :     0
Number of caught UVM_ERROR reports    :     0
Number of caught UVM_WARNING reports :     0

--- UVM Report Summary ---

** Report counts by severity
UVM_INFO :    60
UVM_WARNING :     0
UVM_ERROR :     0
UVM_FATAL :     0
** Report counts by id
[RNTST]     1
[TEST_DONE]     1
[TOP]    1
[UVMTOP]     1
[fix_dest_port_seq]     1
[htax_tx_driver_c]    50
[long_packet_short_delay_seq]     1
[med_packet_fix_vc_seq]     1
[mix_sequence_test]     1
[random_seq]     1
[short_packet_seq]     1
Simulation complete via $finish(1) at time 5 US + 44
/opt/coe/cadence/XCELIUM/tools/methodology/UVM/CDNS-1.1d/sv/src/base/uvm_root.svh:457
    $finish;
xcelium> exit
TOOL:   xrun    22.03-s012: Exiting on Oct 15, 2024 at 22:40:20 CDT  (total: 00:00:02)
bash-4.4$
```