25 Spring ECEN 607: Advanced Analog Circuit Tech Design Pre-lab Report

Lab4: Op Amp Design - I

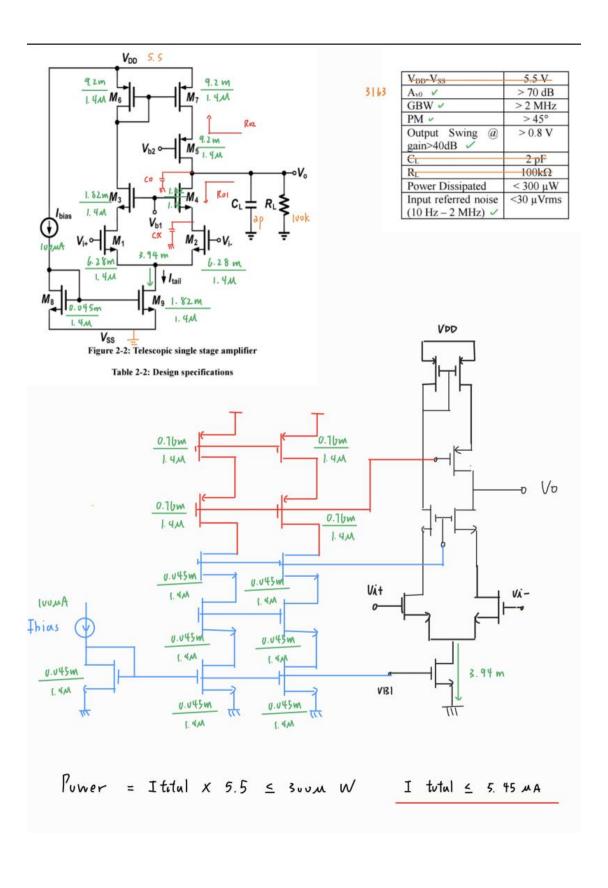
Name: Yu-Hao Chen

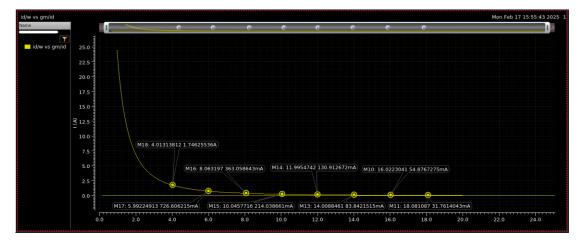
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Section:601

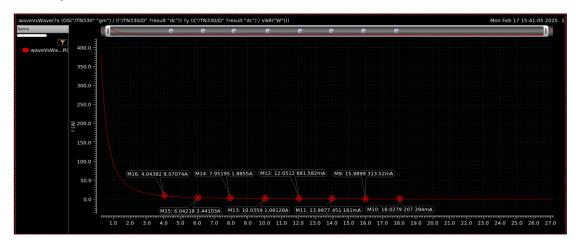
Professor: Jose Silva-Martinez

TA: Yoon, Sung J

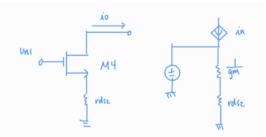




L=1.4u pmos Gm/ID



L= 1.4u nmos Gm/ID



M5 holse

M7 holse

$$105^{2} = \frac{V_{N1}^{2}}{(\frac{1}{q_{MS}} + rds7)}$$
 $107^{2} = 4kT \ qm7$

$$= \frac{4kT}{qm5} \times \frac{1}{(\frac{1}{q_{MS}} + rds7)}$$
 $107^{2} = 4kT \ qm4 \times \frac{1}{q_{MS}} \times \frac{1}{(\frac{1}{q_{MS}} + rds7)} \times \frac{1}{(\frac{1}{q_{MS}} + rds7)^{2}} \times \frac$

$$\frac{1}{3^{m+1}} \times \frac{1}{3^{m+1}} \times \frac{1}{3^{m+1}$$

Vin noise
$$\frac{1}{2} = \frac{10 \text{ total}^2}{6m^2} = \frac{4kT (9mT + 9mz)}{9m^2}$$

Vin vef =
$$\frac{K}{FWLCox}$$
 + $\frac{4kT (gm7+gmz)}{gm^2}$