Polyspace Code Verification

Developer Report for Project: I2C_MCAL_FunctionTest_20250103

Report Author: Pega_User

Polyspace Code Verification: Developer Report for Project: I2C_MCAL_FunctionTest_20250103

by Report Author: Pega_User

Published 03-Jan-2025 17:20:24

Verification Author(s): Pega_User Polyspace Version(s): Polyspace Code Prover 10.3 (R2020b)

Project Version(s): 1.0

Result Folder(s):

Table of Contents

Chapter 1. Polyspace Code Verification Summary	
Chapter 2. Polyspace Run-Time Checks Statistics	3
Run-Time Checks Summary for I2C_MCAL_FunctionTest_20250103 - CP_Result	3
Percentage of code checked for run-time errors	5
Chapter 3. Code Metrics	6
Code Metrics Summary for: I2C_MCAL_FunctionTest_20250103 - CP_Result	
Chapter 4. MISRA C:2012 Guidelines	
MISRA C:2012 Guidelines Summary - Violations by File	8
MISRA C:2012 Guidelines Violations	
Chapter 5. Polyspace Run-Time Checks Results	224
Proven Run-Time Errors	224
Proven Unreachable Code Branches	231
Unreachable Function Checks	251
Unproven Run-Time Errors	251
Chapter 6. Global Variables	296
Variable Checks for: I2C_MCAL_FunctionTest_20250103 - CP_Result	296
Unused variable	296
Used non-shared variable	309
Chapter 7. Appendix 1 - Configuration Settings	336
Polyspace Settings	336
Analysis Assumptions	
Coding Standard Configuration	
Files with compilation errors (files partially analyzed)	
Chapter 8. Appendix 2 - Definitions	

Chapter 1. Polyspace Code Verification Summary

Table 1.1. Code Metrics Summary

Polyspace Code Metrics	Enabled
Pass/Fail	

Table 1.2. Coding Standard Summary - MISRA C:2012 Guidelines

MISRA C:2012 Guidelines	Enabled
Violations	2325
Pass/Fail	

Table 1.3. Run-Time Checks Summary

Run-Time Checks	Enabled
Number of Red Checks	65
Number of Gray Checks	139
Number of Orange Checks	715
Number of Green Checks	6233
Proven	90.0%
Pass/Fail	

Table 1.4. Global Variable Summary

Category	Total
Unused variable	140
Used non-shared variable	387

Developer Name:		
beveroper Hame.		
Date Reviewed:		
Date Reviewed.		
Common ha		
Comments		
Annual Dec		
Approved By:		
••		
Approved Date:		
The same and same		
	2	

Chapter 2. Polyspace Run-Time Checks Statistics

Run-Time Checks Summary for I2C_MCAL_FunctionTest_20250103 - CP_Result

Globally Proven: 90.0%

File	Proven	Green	Red	Gray	Orange
CDD_Mcl.c	100.0%	90	0	11	0
CDD_I2c.c	100.0%	60	0	12	0
CDD_Uart.c	100.0%	53	0	13	0
polyspace_main.c	100.0%	53	0	0	0
Clock_Ip_IntOsc.c	100.0%	44	0	4	0
Gpt.c	100.0%	34	0	11	0
OsIf_Timer_System_Internal_Systick.c	100.0%	33	0	0	0
Clock_Ip_Data.c	100.0%	25	0	0	0
Power_Ip_Private.c	100.0%	23	0	0	0
Lpi2c_Ip_HwAccess.h	100.0%	18	0	0	0
Flexio_Mcl_Ip_Irq.c	100.0%	16	0	0	0
Port.c	100.0%	16	0	5	0
Power_Ip.c	100.0%	16	0	0	0
Power_Ip_PMC.c	100.0%	16	0	1	0
Dma_Ip.c	100.0%	15	10	1	0
OsIf_Timer.c	100.0%	15	0	5	0
Power_Ip_CortexM7.c	100.0%	12	0	0	0
Lpuart_Uart_Ip.c	100.0%	11	0	1	0
CDD_Rm.c	100.0%	10	0	5	0
Total	90.0%	6233	65	139	715

File	Proven	Green	Red	Gray	Orange
Mcu_Ipw.c	100.0%	10	0	2	0
Lpuart_Uart_Ip_HwAccess.h	100.0%	9	0	0	0
Pit_Ip.h	100.0%	9	0	1	0
Power_Ip_DCM_GPR.c	100.0%	5	1	0	0
Power_Ip_MC_ME.c	100.0%	5	0	0	0
Devassert.h	100.0%	4	0	0	0
Dma_Ip_Hw_Access.h	100.0%	3	0	0	0
Dma_Mux_Ip.c	100.0%	1	0	0	0
Siul2_Port_Ip_PBcfg.c	100.0%	1	0	0	0
Pit_Ip.c	98.4%	112	1	7	2
Power_Ip_MC_RGM.c	97.2%	135	2	4	4
Lpi2c_Ip.c	96.7%	54	3	1	2
Dem.c	96.3%	52	0	0	2
Mcu.c	96.3%	48	2	2	2
Det_stub.c	95.8%	138	0	0	6
Tspc_Port_Ip.c	94.1%	64	0	0	4
Dma_Ip_Hwv3_AccessInline.h	93.5%	371	5	1	26
Siul2_Port_Ip.c	93.0%	111	4	5	9
Clock_Ip_Pll.c	92.9%	100	1	4	8
Dem_stub.c	92.9%	156	0	0	12
Flexio_Mcl_Ip_HwAccess.c	92.8%	113	1	2	9
Clock_Ip.c	92.7%	96	1	4	8
Clock_Ip_Divider.c	92.4%	90	4	3	8
Det.c	92.3%	24	0	0	2
Clock_Ip_ProgFreqSwitch.c	91.9%	66	1	1	6
Clock_Ip_Selector.c	91.7%	209	6	7	20
Flexio_Mcl_Ip.c	91.3%	21	0	0	2
Clock_Ip_DividerTrigger.c	90.6%	43	3	2	5
Dma_Ip_Irq.c	89.8%	35	8	1	5
startup.c	89.5%	119	0	0	14
Total	90.0%	6233	65	139	715

File	Proven	Green	Red	Gray	Orange
Clock_Ip_Specific.c	89.5%	47	1	3	6
Clock_Ip_Gate.c	88.7%	52	2	1	7
Clock_Ip_Monitor.c	88.0%	186	1	4	26
Clock_Ip_ExtOsc.c	87.3%	127	2	9	20
SchM_Gpt.c	86.9%	1100	0	0	166
SchM_Mcl.c	86.8%	710	0	0	108
SchM_Rm.c	86.7%	260	0	0	40
SchM_Port.c	86.6%	460	0	0	71
Ram_Ip.c	85.9%	79	0	0	13
SchM_Uart.c	85.7%	60	0	0	10
SchM_Mcu.c	84.7%	50	0	0	9
Dma_Ip_Hw_Access.c	82.3%	219	0	0	47
Emios_Mcl_Ip.c	79.5%	87	1	5	24
Dma_Ip_Driver_State.c	76.7%	27	5	1	10
I2c_Ipw.c	71.4%	5	0	0	2
Total	90.0%	6233	65	139	715

Percentage of code checked for run-time errors

Result Set	% Checked
I2C_MCAL_FunctionTest_20250103 - CP_Result	68%

Chapter 3. Code Metrics

Code Metrics Summary for: I2C_MCAL_FunctionTest_20250103 - CP_Result

Table 3.1. Project Metrics

Metric	Value	Comments
Program Maximum Stack Usage	Not computed	
Number of Protected Shared Variables	0	
Program Minimum Stack Usage	Not computed	
Number of Potentially Unprotected Shared Variables	0	
Number of Recursions	0	
Number of Direct Recursions	0	
Number of Headers	350	
Number of Files	167	

Table 3.2. File Metrics

Metric	Values (Min Max)	Comments
Comment Density	3 100	
Estimated Function Coupling	-10 267	
Number of Lines	78 7721	
Number of Lines Without Comment	9 4523	

Table 3.3. Function Metrics

Metric	Values (Min Max)	Comments
Cyclomatic Complexity	119	
Higher Estimate of Size of Local Variables	0124	

Metric	Values (Min Max)	Comments
Language Scope	1.0 12.8	
Lower Estimate of Size of Local Variables	0124	
Maximum Stack Usage	069	
Minimum Stack Usage	053	
Number of Call Levels	16	
Number of Call Occurrences	020	
Number of Called Functions	017	
Number of Calling Functions	055	
Number of Executable Lines	080	
Number of Function Parameters	05	
Number of Goto Statements	0	
Number of Instructions	0 65	
Number of Lines Within Body	1 176	
Number of Local Non-Static Variables	016	
Number of Local Static Variables	01	
Number of Paths	1 92160	
Number of Return Statements	01	

Chapter 4. MISRA C:2012 Guidelines

MISRA C:2012 Guidelines Summary - Violations by File

File	Total
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	4
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	41
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	18
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	7
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	16
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	12
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	29
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	16
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	35
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	10
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	2
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	33
$D:\ \ D:\ \ \ \ D:\ \ \ \ \ \ \ \ \ \ \ $	24
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	23
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	52
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	24
$D: \label{lem:content} D: lem:conten$	4
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	35
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	4
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	21
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	30
Total	2325

File	Total
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	31
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	94
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	4
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	1
$D: \label{lem:control} D: lem:controllem:cont$	49
$D: \label{lem:decomposition} D: lem:$	4
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	17
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	2
$D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	4
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	1
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	7
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	12
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	12
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	9
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	1
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	3
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	11
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	2
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	3
$D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	2
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	4
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	17
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	552
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	10
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	404
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	30
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	254
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	132
$D: \label{lem:decomposition} D: lem:$	56
Total	2325

File	Total
$D:\label{lem:control} D:\label{lem:control} D:lem:control$	45
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	7
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	3
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	2
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	28
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	3
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	14
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	7
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
$D:\ \ D:\ \ \ \ D:\ \ \ \ \ \ \ \ \ \ \ $	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	5
$D:\ \ D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ $	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	3
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	5
$D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	1
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	3
$D:\ \ \ D:\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	6
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	2
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	5
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	2
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:	1
Total	2325

MISRA C:2012 Guidelines Violations

Table 4.1.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\Project_Settings\Startup_Code\n vic.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
629	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_SCB" is undefined	NVIC_SetPriorityGrouping()	96	4	No	Unset	Unreviewed	
630	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	NVIC_EnableIRQ()	107	4	No	Unset	Unreviewed	
631	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	NVIC_DisableIRQ()	118	4	No	Unset	Unreviewed	
632	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	NVIC_SetPriority()	131	4	No	Unset	Unreviewed	

Table 4.2. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\Project_Settings\Startup_Code\st artup.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
633	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'INIT_TABLE' that has external linkage should b e declared in a header file.	File Scope	52	14	No	Unset	Unreviewed	
642	21.2	A reserved identifier or macro name shall not be declared . The identifier '_INIT_TABLE' shall not be declared.	File Scope	52	14	No	Unset	Unreviewed	
17820	8.6	An identifier with external linkage shall have exactly one external definition. undefined global variable 'INIT_TABLE'	File Scope	52	14	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
635	8.5	An external object or function shall be declared once in o ne and only one file. Variable '_ZERO_TABLE' that has external linkage should be declared in a header file.	File Scope	53	14	No	Unset	Unreviewed	
641	21.2	A reserved identifier or macro name shall not be declared . The identifier '_ZERO_TABLE' shall not be declared.	File Scope	53	14	No	Unset	Unreviewed	
17821	8.6	An identifier with external linkage shall have exactly one external definition. undefined global variable '_ZERO_TABLE'	File Scope	53	14	No	Unset	Unreviewed	
636	21.2	A reserved identifier or macro name shall not be declared . The identifier 'INDEX_COPY_CORE2' shall not be declare d.	File Scope	54	14	No	Unset	Unreviewed	
638	8.5	An external object or function shall be declared once in o ne and only one file. Variable '_INDEX_COPY_CORE2' that has external linkage should be declared in a header file.	File Scope	54	14	No	Unset	Unreviewed	
17822	8.6	An identifier with external linkage shall have exactly one external definition. undefined global variable '_INDEX_COPY_CORE2'	File Scope	54	14	No	Unset	Unreviewed	
646	21.2	A reserved identifier or macro name shall not be declared . The identifier 'VECTOR_RAM' shall not be declared.	File Scope	58	18	No	Unset	Unreviewed	
648	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'VECTOR_RAM' that has external linkage shoul d be declared in a header file.	File Scope	58	18	No	Unset	Unreviewed	
637	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	init_data_bss()	112	46	No	Unset	Unreviewed	
640	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 16 bits) is as signed to an object with a narrower essential type (unsigned on 8 bits)	init_data_bss()	119	16	No	Unset	Unreviewed	
645	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as	init_data_bss()	120	14	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		signed to an object with a narrower essential type (unsign ed on 16 bits)							
41468	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	122	15	No	Unset	Unreviewed	
41469	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	122	15	No	Unset	Unreviewed	
41470	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	122	24	No	Unset	Unreviewed	
41471	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	122	24	No	Unset	Unreviewed	
644	11.8	A cast shall not remove any const or volatile qualification from the type pointed to by a pointer. Cast from type 'const uint32 *' to type 'uint8 *' removes qualifiers.	init_data_bss()	125	15	No	Unset	Unreviewed	
41472	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	129	16	No	Unset	Unreviewed	
41473	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	129	16	No	Unset	Unreviewed	
41474	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	129	26	No	Unset	Unreviewed	
41475	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	129	26	No	Unset	Unreviewed	
634	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	init_data_bss()	136	46	No	Unset	Unreviewed	
650	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	init_data_bss()	142	14	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41476	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	144	15	No	Unset	Unreviewed	
41477	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	144	15	No	Unset	Unreviewed	
41488	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss()	150	15	No	Unset	Unreviewed	
41489	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss()	150	15	No	Unset	Unreviewed	
639	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	init_data_bss_core2()	173	46	No	Unset	Unreviewed	
643	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 16 bits) is as signed to an object with a narrower essential type (unsigned on 8 bits)	init_data_bss_core2()	180	16	No	Unset	Unreviewed	
647	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	init_data_bss_core2()	181	14	No	Unset	Unreviewed	
41530	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss_core2()	183	15	No	Unset	Unreviewed	
41531	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss_core2()	183	15	No	Unset	Unreviewed	
41532	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss_core2()	183	24	No	Unset	Unreviewed	
41533	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss_core2()	183	24	No	Unset	Unreviewed	
649	11.8	A cast shall not remove any const or volatile qualification from the type pointed to by a pointer.	init_data_bss_core2()	185	15	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		Cast from type 'const uint32 *' to type 'uint8 *' removes qu alifiers.							
41548	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss_core2()	189	16	No	Unset	Unreviewed	
41549	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss_core2()	189	16	No	Unset	Unreviewed	
41550	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	init_data_bss_core2()	189	26	No	Unset	Unreviewed	
41551	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	init_data_bss_core2()	189	26	No	Unset	Unreviewed	

Table 4.3. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\Project_Settings\Startup_Code\system.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
666	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'INT_ITCM_START' that has external linkage sh ould be declared in a header file.	File Scope	124	15	No	Unset	Unreviewed	
651	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'ROM_CODE_START' that has external linkage s hould be declared in a header file.	File Scope	125	15	No	Unset	Unreviewed	
654	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'ROM_DATA_START' that has external linkage s hould be declared in a header file.	File Scope	126	15	No	Unset	Unreviewed	
657	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'INT_DTCM_START' that has external linkage sh ould be declared in a header file.	File Scope	127	15	No	Unset	Unreviewed	
655	8.5	An external object or function shall be declared once in o ne and only one file.	File Scope	128	15	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		Variable 'INT_SRAM_START' that has external linkage should be declared in a header file.							
660	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'RAM_NO_CACHEABLE_START' that has external linkage should be declared in a header file.	File Scope	129	15	No	Unset	Unreviewed	
656	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'RAM_SHAREABLE_START' that has external lin kage should be declared in a header file.	File Scope	130	15	No	Unset	Unreviewed	
663	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'RAM_CACHEABLE_SIZE' that has external link age should be declared in a header file.	File Scope	131	15	No	Unset	Unreviewed	
658	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'RAM_NO_CACHEABLE_SIZE' that has external l inkage should be declared in a header file.	File Scope	132	15	No	Unset	Unreviewed	
661	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'RAM_SHAREABLE_SIZE' that has external link age should be declared in a header file.	File Scope	133	15	No	Unset	Unreviewed	
664	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'rbar' variable has no previous declaration.	File Scope	174	18	No	Unset	Unreviewed	
662	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'rasr' variable has no previous declaration.	File Scope	175	18	No	Unset	Unreviewed	
667	D2.1	All source files shall compile without any compilation err ors. Identifier "IP_MSCM" is undefined	Sys_GetCoreID()	511	12	No	Unset	Unreviewed	
652	D2.1	All source files shall compile without any compilation err ors. Identifier "MSCM_CPXNUM_CPN_MASK" is undefined	Sys_GetCoreID()	511	30	No	Unset	Unreviewed	
659	D2.1	All source files shall compile without any compilation err ors. Identifier "MSCM_IRSPRC_M7_0_SHIFT" is undefined	SystemInit()	533	31	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
653	D2.1	All source files shall compile without any compilation err ors. Identifier "MSCM_IRSPRC_COUNT" is undefined	SystemInit()	556	20	No	Unset	Unreviewed	
665	D2.1	All source files shall compile without any compilation err ors. Identifier "IP_MSCM" is undefined	SystemInit()	558	8	No	Unset	Unreviewed	
668	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_SCB" is undefined	sys_m7_cache_disable()	758	4	No	Unset	Unreviewed	

Table 4.4. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_I2c.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
1	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'I2c_PBCfgVariantPredefined' that has external li nkage should be declared in a header file.	File Scope	178	36	No	Unset	Unreviewed	

Table 4.5. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Mcl.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
3	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 6078. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
7	14.3	Controlling expressions shall not be invariant. Expression is always false.	Mcl_SetDmaInstanceCom mand()	506	11	No	Unset	Unreviewed	
9	14.3	Controlling expressions shall not be invariant. Expression is always false.	Mcl_SetDmaChannelComm and()	561	11	No	Unset	Unreviewed	
8	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	Mcl_SetDmaChannelGlobal List()	635	104	No	Unset	Unreviewed	
10	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	Mcl_SetDmaChannelTransf erList()	671	108	No	Unset	Unreviewed	
11	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	Mcl_SetDmaChannelScatte rGatherList()	707	127	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
12	14.3	Controlling expressions shall not be invariant. Expression is always false.	Mcl_GetDmaChannelPara m()	724	11	No	Unset	Unreviewed	

Table 4.6. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Uart.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
14	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'Uart_pxPBcfgVariantPredefined' that has extern al linkage should be declared in a header file.	File Scope	143	36	No	Unset	Unreviewed	

Table 4.7. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42147	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [017]	Clock_Ip_DistributePll()	990	58	No	Unset	Unreviewed	
42148	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [017]	Clock_Ip_DistributePll()	990	58	No	Unset	Unreviewed	
42149	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_DistributePll()	990	60	No	Unset	Unreviewed	
42150	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_DistributePll()	990	60	No	Unset	Unreviewed	
42151	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [array size undefined]	Clock_Ip_DistributePll()	990	92	No	Unset	Unreviewed	
42152	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_DistributePll()	990	92	No	Unset	Unreviewed	
42153	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_DistributePll()	993	91	No	Unset	Unreviewed	
42154	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Clock_Ip_DistributePll()	993	91	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [086]							
42155	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_DistributePll()	993	93	No	Unset	Unreviewed	
42156	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_DistributePll()	993	93	No	Unset	Unreviewed	
42179	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_DisableClockMon itor()	1036	74	No	Unset	Unreviewed	
42180	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_DisableClockMon itor()	1036	74	No	Unset	Unreviewed	
42195	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_DisableModuleClock()	1066	75	No	Unset	Unreviewed	
42196	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_DisableModuleClock()	1066	75	No	Unset	Unreviewed	
42203	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_EnableModuleClock()	1082	75	No	Unset	Unreviewed	
42204	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_EnableModuleClock()	1082	75	No	Unset	Unreviewed	

Table 4.8. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Data.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
19	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3332	32	No	Unset	Unreviewed	
21	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3333	32	No	Unset	Unreviewed	
28	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3339	32	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
20	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3341	32	No	Unset	Unreviewed	
18	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3345	32	No	Unset	Unreviewed	
22	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3346	32	No	Unset	Unreviewed	
23	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3367	32	No	Unset	Unreviewed	
24	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3412	38	No	Unset	Unreviewed	
25	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3540	49	No	Unset	Unreviewed	
27	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3558	55	No	Unset	Unreviewed	
26	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3573	53	No	Unset	Unreviewed	
29	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	3575	53	No	Unset	Unreviewed	

Table 4.9. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Divider.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41512	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	171	49	No	Unset	Unreviewed	
41513	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	171	49	No	Unset	Unreviewed	
41526	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	180	38	No	Unset	Unreviewed	
41527	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	180	38	No	Unset	Unreviewed	
31499	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	180	63	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31500	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	180	63	No	Unset	Unreviewed	
41572	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	205	44	No	Unset	Unreviewed	
41573	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	205	44	No	Unset	Unreviewed	
31511	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	205	69	No	Unset	Unreviewed	
31512	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	205	69	No	Unset	Unreviewed	
30	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	216	80	No	Unset	Unreviewed	
31	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetCgmXDeDivSt atWithoutPhase()	220	80	No	Unset	Unreviewed	
41623	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetPllPll0divDeDi vOutput()	255	49	No	Unset	Unreviewed	
41624	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetPllPll0divDeDi vOutput()	255	49	No	Unset	Unreviewed	
41631	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetPllPll0divDeDi vOutput()	262	60	No	Unset	Unreviewed	
41632	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetPllPll0divDeDi vOutput()	262	60	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41633	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Clock_Ip_SetPllPll0divDeDi vOutput()	262	69	No	Unset	Unreviewed	
41634	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Clock_Ip_SetPllPll0divDeDi vOutput()	262	69	No	Unset	Unreviewed	
32	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllPll0divDeDi vOutput()	263	21	No	Unset	Unreviewed	
33	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllPll0divDeDi vOutput()	264	21	No	Unset	Unreviewed	
34	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllPll0divDeDi vOutput()	265	21	No	Unset	Unreviewed	
31521	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_SetPllPll0divDeDivOutput()	265	70	No	Unset	Unreviewed	
41645	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetPllPlldvOdiv2 Output()	288	49	No	Unset	Unreviewed	
41646	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetPllPlldvOdiv2 Output()	288	49	No	Unset	Unreviewed	
41655	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetPllPlldvOdiv2 Output()	291	56	No	Unset	Unreviewed	
41656	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetPllPlldvOdiv2 Output()	291	56	No	Unset	Unreviewed	
35	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essenti	Clock_Ip_SetPllPlldvOdiv2 Output()	292	17	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		al type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsign ed on 16 bits)							
36	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Output()	293	17	No	Unset	Unreviewed	
31522	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_SetPllPlldvOdiv2 Output()	293	60	No	Unset	Unreviewed	

Table 4.10.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_DividerTrigger.
c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41486	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	149	49	No	Unset	Unreviewed	
41487	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat 0	149	49	No	Unset	Unreviewed	
41496	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	155	27	No	Unset	Unreviewed	
41497	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	155	27	No	Unset	Unreviewed	
31488	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	155	52	No	Unset	Unreviewed	
31489	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat 0	155	52	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41502	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	159	27	No	Unset	Unreviewed	
41503	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	159	27	No	Unset	Unreviewed	
31492	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	159	52	No	Unset	Unreviewed	
31493	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ConfigureCgmXD ivTrigCtrlTctlHhenUpdStat ()	159	52	No	Unset	Unreviewed	
41536	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	183	49	No	Unset	Unreviewed	
41537	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	183	49	No	Unset	Unreviewed	
41552	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	189	27	No	Unset	Unreviewed	
41553	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	189	27	No	Unset	Unreviewed	
31505	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	189	52	No	Unset	Unreviewed	
31506	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUp dStat()	189	52	No	Unset	Unreviewed	

Table 4.11.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ExtOsc.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41522	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetFxoscOscon BypEocvGmSel()	178	44	No	Unset	Unreviewed	
41523	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetFxoscOscon BypEocvGmSel()	178	44	No	Unset	Unreviewed	
41528	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_ResetFxoscOscon BypEocvGmSel()	181	34	No	Unset	Unreviewed	
41529	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_ResetFxoscOscon BypEocvGmSel()	181	34	No	Unset	Unreviewed	
41563	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	196	44	No	Unset	Unreviewed	
41564	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	196	44	No	Unset	Unreviewed	
38	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	200	45	No	Unset	Unreviewed	
31509	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	203	64	No	Unset	Unreviewed	
37	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The left operand of the operator shall not have wider essential type (unsigned on 32 bits) than the right operand (unsigned on 16 bits) which is a composite expression	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	204	120	No	Unset	Unreviewed	
41610	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	240	44	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41611	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	240	44	No	Unset	Unreviewed	
39	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	256	36	No	Unset	Unreviewed	
41627	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	256	66	No	Unset	Unreviewed	
41628	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	256	66	No	Unset	Unreviewed	
41643	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_DisableFxoscOsc onBypEocvGmSel()	288	47	No	Unset	Unreviewed	
41644	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_DisableFxoscOsc onBypEocvGmSel()	288	47	No	Unset	Unreviewed	
41653	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_DisableFxoscOsc onBypEocvGmSel()	291	30	No	Unset	Unreviewed	
41654	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_DisableFxoscOsc onBypEocvGmSel()	291	30	No	Unset	Unreviewed	
41666	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	299	44	No	Unset	Unreviewed	
41667	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	299	44	No	Unset	Unreviewed	
41673	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	304	38	No	Unset	Unreviewed	
41674	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	304	38	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: pointer may be outside its bounds							
41684	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetSxoscOscon Eocv()	321	44	No	Unset	Unreviewed	
41685	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetSxoscOscon Eocv()	321	44	No	Unset	Unreviewed	
41686	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_ResetSxoscOscon Eocv()	324	34	No	Unset	Unreviewed	
41687	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_ResetSxoscOscon Eocv()	324	34	No	Unset	Unreviewed	
41694	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetSxoscOsconEo cv()	339	44	No	Unset	Unreviewed	
41695	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetSxoscOsconEo cv()	339	44	No	Unset	Unreviewed	
40	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetSxoscOsconEo cv()	343	45	No	Unset	Unreviewed	
31527	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_SetSxoscOsconEo	345	64	No	Unset	Unreviewed	
41726	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_CompleteSxoscOs conEocv()	370	44	No	Unset	Unreviewed	
41727	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_CompleteSxoscOs conEocv()	370	44	No	Unset	Unreviewed	
41	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CompleteSxoscOs conEocv()	380	32	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
41728	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CompleteSxoscOs conEocv()	380	62	No	Unset	Unreviewed	
41729	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CompleteSxoscOs conEocv()	380	62	No	Unset	Unreviewed	

Table 4.12. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Gate.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41506	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	167	72	No	Unset	Unreviewed	
41507	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	167	72	No	Unset	Unreviewed	
41524	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	178	63	No	Unset	Unreviewed	
41525	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	178	63	No	Unset	Unreviewed	
31501	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	183	60	No	Unset	Unreviewed	
31502	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	183	60	No	Unset	Unreviewed	
41582	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	208	63	No	Unset	Unreviewed	
41583	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	208	63	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31513	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	213	60	No	Unset	Unreviewed	
31514	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ClockSetGateMc MePartitionCollectionCloc kRequest()	213	60	No	Unset	Unreviewed	

Table 4.13. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_IntOsc.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
43	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	b()	264	30	No	Unset	Unreviewed	
42	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_SetFircDivSelHSE b()	267	25	No	Unset	Unreviewed	

Table 4.14.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Monitor.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41612	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	245	95	No	Unset	Unreviewed	
41613	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	245	95	No	Unset	Unreviewed	
41647	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	289	24	No	Unset	Unreviewed	
41648	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	289	24	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41708	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	351	70	No	Unset	Unreviewed	
41709	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	351	70	No	Unset	Unreviewed	
41712	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	355	19	No	Unset	Unreviewed	
41713	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	355	19	No	Unset	Unreviewed	
41714	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	28	No	Unset	Unreviewed	
41715	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	28	No	Unset	Unreviewed	
41716	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	70	No	Unset	Unreviewed	
41717	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	70	No	Unset	Unreviewed	
41718	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	28	No	Unset	Unreviewed	
41719	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	28	No	Unset	Unreviewed	
41720	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	70	No	Unset	Unreviewed	
41721	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand.	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	70	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		Warning: array index may be outside bounds : [array size undefined]							
41732	D4.1	Run-time failures shall be minimized. Warning: scalar division by zero may occur	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	390	47	No	Unset	Unreviewed	
41735	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	396	21	No	Unset	Unreviewed	
41736	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	396	21	No	Unset	Unreviewed	
41747	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	407	13	No	Unset	Unreviewed	
41748	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	407	13	No	Unset	Unreviewed	
41769	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	436	71	No	Unset	Unreviewed	
41770	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	436	71	No	Unset	Unreviewed	
41771	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	442	17	No	Unset	Unreviewed	
41772	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	442	17	No	Unset	Unreviewed	
41775	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	446	17	No	Unset	Unreviewed	
41776	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_EnableCmuFcFce RefCntLfrefHfref()	446	17	No	Unset	Unreviewed	
41793	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [06]	Clock_Ip_CMU_GetInterrup tStatus()	471	33	No	Unset	Unreviewed	
41794	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Clock_Ip_CMU_GetInterrup tStatus()	471	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [06]							
41795	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CMU_GetInterrup tStatus()	471	43	No	Unset	Unreviewed	
41796	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CMU_GetInterrup tStatus()	471	43	No	Unset	Unreviewed	
41814	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CMU_ClockFailInt 0	494	47	No	Unset	Unreviewed	
41815	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CMU_ClockFailInt ()	494	47	No	Unset	Unreviewed	

Table 4.15. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Pll.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41542	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	185	44	No	Unset	Unreviewed	
41543	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	185	44	No	Unset	Unreviewed	
56	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	190	73	No	Unset	Unreviewed	
41557	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	193	45	No	Unset	Unreviewed	
41558	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	193	45	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
54	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_ResetPllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	193	53	No	Unset	Unreviewed	
41580	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	208	44	No	Unset	Unreviewed	
41581	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	208	44	No	Unset	Unreviewed	
41591	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	213	57	No	Unset	Unreviewed	
41592	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	213	57	No	Unset	Unreviewed	
57	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	221	18	No	Unset	Unreviewed	
58	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	223	18	No	Unset	Unreviewed	
31517	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	223	67	No	Unset	Unreviewed	
59	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The left operand of the operator shall not have wider ess	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	228	125	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ential type (unsigned on 32 bits) than the right operand (unsigned on 16 bits) which is a composite expression							
60	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSpre adctlStepnoStepsize()	237	57	No	Unset	Unreviewed	
41625	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize ()	256	47	No	Unset	Unreviewed	
41626	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize ()	256	47	No	Unset	Unreviewed	
41629	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize 0	258	52	No	Unset	Unreviewed	
41630	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize 0	258	52	No	Unset	Unreviewed	
41651	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	289	44	No	Unset	Unreviewed	
41652	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	289	44	No	Unset	Unreviewed	
41660	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	295	49	No	Unset	Unreviewed	
41661	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	295	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
55	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()		57	No	Unset	Unreviewed	

Table 4.16.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41578	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_CgmXPcfsSdurDi vcDiveDivs()	208	42	No	Unset	Unreviewed	
41579	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	208	42	No	Unset	Unreviewed	
41584	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_CgmXPcfsSdurDi vcDiveDivs()	208	68	No	Unset	Unreviewed	
41585	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	208	68	No	Unset	Unreviewed	
41587	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	209	48	No	Unset	Unreviewed	
41588	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	209	48	No	Unset	Unreviewed	
41595	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	215	19	No	Unset	Unreviewed	
41596	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	215	19	No	Unset	Unreviewed	
44	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category.	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	219	19	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsign ed on 16 bits)							
50	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	221	18	No	Unset	Unreviewed	
41601	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	221	21	No	Unset	Unreviewed	
41602	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	221	21	No	Unset	Unreviewed	
41603	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [array size undefined]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	221	63	No	Unset	Unreviewed	
41604	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	221	63	No	Unset	Unreviewed	
45	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	234	21	No	Unset	Unreviewed	
46	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The right operand of the * operator shall not have wider essential type (unsigned on 32 bits) than the left operand (unsigned on 16 bits) which is a composite expression	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	234	77	No	Unset	Unreviewed	
47	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The left operand of the / operator shall not have wider ess	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	234	87	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ential type (unsigned on 32 bits) than the right operand (unsigned on 16 bits) which is a composite expression							
31519	D4.1	Run-time failures shall be minimized. Error: scalar division by zero occurs	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	234	87	No	Unset	Unreviewed	
51	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	260	17	No	Unset	Unreviewed	
49	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	294	44	No	Unset	Unreviewed	
48	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The right operand of the operator shall not have wider essential type (unsigned on 32 bits) than the left operand (unsigned on 16 bits) which is a composite expression	Clock_Ip_CgmXPcfsSdurDivcDiveDivs()	294	103	No	Unset	Unreviewed	
52	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDi vcDiveDivs()	295	44	No	Unset	Unreviewed	
53	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_CgmXPcfsSdurDi vcDiveDivs()	296	44	No	Unset	Unreviewed	

Table 4.17. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Selector.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41514	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetCgmXCscCss ClkswSwip()	173	49	No	Unset	Unreviewed	
41515	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetCgmXCscCss ClkswSwip()	173	49	No	Unset	Unreviewed	
41520	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	23	No	Unset	Unreviewed	
41521	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	23	No	Unset	Unreviewed	
31496	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	48	No	Unset	Unreviewed	
31497	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	48	No	Unset	Unreviewed	
41574	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetCgmXCscCssCl kswSwip()	207	49	No	Unset	Unreviewed	
41575	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetCgmXCscCssCl kswSwip()	207	49	No	Unset	Unreviewed	
41576	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCl kswSwip()	207	56	No	Unset	Unreviewed	
41577	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCl kswSwip()	207	56	No	Unset	Unreviewed	
41589	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [017]	Clock_Ip_SetCgmXCscCssCl kswSwip()	209	63	No	Unset	Unreviewed	
41590	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Clock_Ip_SetCgmXCscCssCl kswSwip()	209	63	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [017]							
41597	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_SetCgmXCscCssCl kswSwip()	215	46	No	Unset	Unreviewed	
41598	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_SetCgmXCscCssCl kswSwip()	215	46	No	Unset	Unreviewed	
31515	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCl kswSwip()	215	71	No	Unset	Unreviewed	
31516	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCl kswSwip()	215	71	No	Unset	Unreviewed	
41675	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		49	No	Unset	Unreviewed	
41676	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		49	No	Unset	Unreviewed	
41679	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		23	No	Unset	Unreviewed	
41680	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		23	No	Unset	Unreviewed	
31525	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		48	No	Unset	Unreviewed	
31526	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		48	No	Unset	Unreviewed	
41696	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()		49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41697	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	339	49	No	Unset	Unreviewed	
41700	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	339	56	No	Unset	Unreviewed	
41701	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	339	56	No	Unset	Unreviewed	
41702	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [017]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	341	63	No	Unset	Unreviewed	
41703	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [017]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	341	63	No	Unset	Unreviewed	
41706	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	347	46	No	Unset	Unreviewed	
41707	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	347	46	No	Unset	Unreviewed	
31528	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	347	71	No	Unset	Unreviewed	
31529	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCl kswRampupRampdownSw ip()	347	71	No	Unset	Unreviewed	
41777	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_ResetCgmXCscCss CsGrip()	446	54	No	Unset	Unreviewed	
41778	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_ResetCgmXCscCss CsGrip()	446	54	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41781	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_ResetCgmXCscCss CsGrip()	453	23	No	Unset	Unreviewed	
41782	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_ResetCgmXCscCss CsGrip()	453	23	No	Unset	Unreviewed	
31530	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss CsGrip()	453	48	No	Unset	Unreviewed	
31531	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_ResetCgmXCscCss CsGrip()	453	48	No	Unset	Unreviewed	
41828	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [086]	Clock_Ip_SetCgmXCscCssCs Grip()	511	49	No	Unset	Unreviewed	
41829	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [086]	Clock_Ip_SetCgmXCscCssCs Grip()	511	49	No	Unset	Unreviewed	
41830	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCs Grip()	511	56	No	Unset	Unreviewed	
41831	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetCgmXCscCssCs Grip()	511	56	No	Unset	Unreviewed	
41836	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [017]	Clock_Ip_SetCgmXCscCssCs Grip()	513	63	No	Unset	Unreviewed	
41837	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [017]	Clock_Ip_SetCgmXCscCssCs Grip()	513	63	No	Unset	Unreviewed	
41838	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Clock_Ip_SetCgmXCscCssCs Grip()	518	23	No	Unset	Unreviewed	
41839	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Clock_Ip_SetCgmXCscCssCs Grip()	518	23	No	Unset	Unreviewed	
31532	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCs Grip()	518	48	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31533	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Clock_Ip_SetCgmXCscCssCs Grip()	518	48	No	Unset	Unreviewed	
41898	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [017]	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	69	No	Unset	Unreviewed	
41899	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [017]	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	69	No	Unset	Unreviewed	
41900	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	76	No	Unset	Unreviewed	
41901	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	76	No	Unset	Unreviewed	

Table 4.18.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
108	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits))	227	57	No	Unset	Unreviewed	
31518	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Clock_Ip_PllPowerClockIp()	227	90	No	Unset	Unreviewed	
101	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	dules()	287	57	No	Unset	Unreviewed	
104	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as		311	57	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		signed to an object with a narrower essential type (unsign ed on 16 bits)							
100	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Clock_Ip_PowerClockIpMo dules()	358	57	No	Unset	Unreviewed	
41737	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Clock_Ip_CodeInRamSetFla shWaitStates()	397	27	No	Unset	Unreviewed	
41738	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Clock_Ip_CodeInRamSetFla shWaitStates()	397	27	No	Unset	Unreviewed	
41739	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_CodeInRamSetFla shWaitStates()	397	69	No	Unset	Unreviewed	
41740	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [array size undefined]	Clock_Ip_CodeInRamSetFla shWaitStates()	397	69	No	Unset	Unreviewed	
102	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	399	8	No	Unset	Unreviewed	
103	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	401	12	No	Unset	Unreviewed	
105	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	405	17	No	Unset	Unreviewed	
106	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	409	17	No	Unset	Unreviewed	
107	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	420	12	No	Unset	Unreviewed	
109	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	424	17	No	Unset	Unreviewed	
110	14.3	Controlling expressions shall not be invariant. Expression is always true.	Clock_Ip_CodeInRamSetFla shWaitStates()	428	17	No	Unset	Unreviewed	
41842	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	DisableFircInStandbyMod e()	519	29	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41843	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	DisableFircInStandbyMod e()	519	29	No	Unset	Unreviewed	
41848	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	EnableFircInStandbyMode ()	527	29	No	Unset	Unreviewed	
41849	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	EnableFircInStandbyMode ()	527	29	No	Unset	Unreviewed	
41856	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	DisableSircInStandbyMode ()	533	29	No	Unset	Unreviewed	
41857	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	DisableSircInStandbyMode 0	533	29	No	Unset	Unreviewed	
41862	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	EnableSircInStandbyMode ()	541	29	No	Unset	Unreviewed	
41863	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	EnableSircInStandbyMode ()	541	29	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41614	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_ReportErrorStatusPr eExtData()	246	39	No	Unset	Unreviewed	
41615	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_ReportErrorStatusPr eExtData()	246	39	No	Unset	Unreviewed	
41619	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [0126]	Dem_ReportErrorStatusPr eExtData()	255	30	No	Unset	Unreviewed	
41620	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [0126]	Dem_ReportErrorStatusPr eExtData()	255	30	No	Unset	Unreviewed	

Table 4.20. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dem_stub.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
89	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (Boolean)	Dem_TestLastReportError Status()	108	21	No	Unset	Unreviewed	
92	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (signed)		114	20	No	Unset	Unreviewed	
41464	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	Dem_TestLastReportError Status()	114	41	No	Unset	Unreviewed	
41465	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_TestLastReportError Status()	114	41	No	Unset	Unreviewed	
41466	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [0126]	Dem_TestLastReportError Status()	117	43	No	Unset	Unreviewed	
41467	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [0126]	Dem_TestLastReportError Status()	117	43	No	Unset	Unreviewed	
93	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Dem_TestLastReportError Status()	120	17	No	Unset	Unreviewed	
94	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (Boolean)	Dem_TestNoError()	147	21	No	Unset	Unreviewed	
41490	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_TestNoError()	151	48	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41491	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_TestNoError()	151	48	No	Unset	Unreviewed	
41492	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [0126]	Dem_TestNoError()	153	68	No	Unset	Unreviewed	
41493	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [0126]	Dem_TestNoError()	153	68	No	Unset	Unreviewed	
96	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (Boolean)	Dem_TestNoError()	155	19	No	Unset	Unreviewed	
97	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (Boolean)	Dem_TestLastReportError StatusPreExtData()	177	24	No	Unset	Unreviewed	
90	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (signed)	Dem_TestLastReportError StatusPreExtData()	184	20	No	Unset	Unreviewed	
41540	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_TestLastReportError StatusPreExtData()	184	41	No	Unset	Unreviewed	
41541	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_TestLastReportError StatusPreExtData()	184	41	No	Unset	Unreviewed	
41544	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [0126]	Dem_TestLastReportError StatusPreExtData()	187	50	No	Unset	Unreviewed	
41545	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [0126]	Dem_TestLastReportError StatusPreExtData()	187	50	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
98	15.6	The body of an iteration-statement or a selection-statemen t shall be a compound-statement. An if (expression) construct shall be followed by a compound statement.	Dem_TestLastReportError StatusPreExtData()	193	12	No	Unset	Unreviewed	
41567	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [099]	Dem_TestLastReportError StatusPreExtData()	196	80	No	Unset	Unreviewed	
41568	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [099]	Dem_TestLastReportError StatusPreExtData()	196	80	No	Unset	Unreviewed	
41605	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	Dem_ClearEvents()	234	22	No	Unset	Unreviewed	
41606	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_ClearEvents()	234	22	No	Unset	Unreviewed	
91	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Dem_GetEvent()	250	21	No	Unset	Unreviewed	
41617	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_GetEvent()	253	39	No	Unset	Unreviewed	
41618	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_GetEvent()	253	39	No	Unset	Unreviewed	
41621	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [0126]	Dem_GetEvent()	255	41	No	Unset	Unreviewed	
41622	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [0126]	Dem_GetEvent()	255	41	No	Unset	Unreviewed	
95	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Dem_GetEvent()	260	15	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41638	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_GetEventCount()	277	29	No	Unset	Unreviewed	
41639	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_GetEventCount()	277	29	No	Unset	Unreviewed	
99	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category unsigned) is assigned to an object with a different essential type category (Boolean)	Dem_BufferOverflow()	298	11	No	Unset	Unreviewed	
41662	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Dem_BufferOverflow()	298	28	No	Unset	Unreviewed	
41663	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Dem_BufferOverflow()	298	28	No	Unset	Unreviewed	

Table 4.21. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Det.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41593	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_ReportError()	214	16	No	Unset	Unreviewed	
41594	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_ReportError()	214	16	No	Unset	Unreviewed	
41635	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_ReportTransientFault()	268	25	No	Unset	Unreviewed	
41636	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_ReportTransientFault()	268	25	No	Unset	Unreviewed	

Table 4.22. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Det_stub.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41462	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestLastReportError()	111	26	No	Unset	Unreviewed	
41463	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestLastReportError()	111	26	No	Unset	Unreviewed	
41478	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestLastReportRuntim eError()	144	33	No	Unset	Unreviewed	
41479	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestLastReportRuntim eError()	144	33	No	Unset	Unreviewed	
41518	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestLastReportTransie ntFault()	176	35	No	Unset	Unreviewed	
41519	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestLastReportTransie ntFault()	176	35	No	Unset	Unreviewed	
41570	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestNoError()	198	25	No	Unset	Unreviewed	
41571	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestNoError()	198	25	No	Unset	Unreviewed	
113	10.1	Operands shall not be of an inappropriate essential type. The left operand of the operator is of an inappropriate essential type category unsigned. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoError()	198	37	No	Unset	Unreviewed	
115	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoError()	198	66	No	Unset	Unreviewed	
116	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoError()	198	90	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41599	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestNoRuntimeError()	219	32	No	Unset	Unreviewed	
41600	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestNoRuntimeError()	219	32	No	Unset	Unreviewed	
118	10.1	Operands shall not be of an inappropriate essential type. The left operand of the operator is of an inappropriate essential type category unsigned. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoRuntimeError()	219	44	No	Unset	Unreviewed	
119	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoRuntimeError()	219	80	No	Unset	Unreviewed	
120	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoRuntimeError()	219	111	No	Unset	Unreviewed	
41608	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	Det_TestNoTransientFault(240	34	No	Unset	Unreviewed	
41609	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	Det_TestNoTransientFault()	240	34	No	Unset	Unreviewed	
121	10.1	Operands shall not be of an inappropriate essential type. The left operand of the operator is of an inappropriate essential type category unsigned. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoTransientFault()	240	46	No	Unset	Unreviewed	
117	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoTransientFault()	240	84	No	Unset	Unreviewed	
114	10.1	Operands shall not be of an inappropriate essential type. The right operand of the operator is of an inappropriat e essential type category unsigned.	Det_TestNoTransientFault()	240	117	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
112	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4235. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
31523	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_ConvertLogicChTo HwCh()	298	34	No	Unset	Unreviewed	
31524	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_ConvertLogicChTo HwCh()	298	34	No	Unset	Unreviewed	
124	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	412	23	No	Unset	Unreviewed	
125	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	414	23	No	Unset	Unreviewed	
126	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	418	23	No	Unset	Unreviewed	
128	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	424	23	No	Unset	Unreviewed	
129	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assig	Static_Dma_Ip_SetLogicCh annelTransferConfig()	430	23	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ned to an object with a different essential type category (unsigned)							
130	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	432	23	No	Unset	Unreviewed	
127	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	434	23	No	Unset	Unreviewed	
131	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	Static_Dma_Ip_SetLogicCh annelTransferConfig()	459	23	No	Unset	Unreviewed	
31534	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicInstanceC ommand()	689	74	No	Unset	Unreviewed	
31535	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicInstanceC ommand()	689	74	No	Unset	Unreviewed	
31536	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_GetLogicInstanceS tatus()	743	74	No	Unset	Unreviewed	
31537	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_GetLogicInstanceS tatus()	743	74	No	Unset	Unreviewed	
31548	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelC ommand()	1006	73	No	Unset	Unreviewed	
31549	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelC ommand()	1006	73	No	Unset	Unreviewed	
31550	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_GetLogicChannelS tatus()	1103	73	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31551	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_GetLogicChannelS tatus()	1103	73	No	Unset	Unreviewed	
31552	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelG lobalList()	1173	73	No	Unset	Unreviewed	
31553	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelG lobalList()	1173	73	No	Unset	Unreviewed	
31554	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelT ransferList()	1285	73	No	Unset	Unreviewed	
31555	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelT ransferList()	1285	73	No	Unset	Unreviewed	
31556	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelS catterGatherList()	1425	73	No	Unset	Unreviewed	
31557	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelS catterGatherList()	1425	73	No	Unset	Unreviewed	
31558	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_GetLogicChannelP aram()	1559	73	No	Unset	Unreviewed	
31559	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_GetLogicChannelP aram()	1559	73	No	Unset	Unreviewed	
17843	5.1	External identifiers shall be distinct. External function 'Dma_Ip_SetLogicChannelScatterGather Config' conflicts with the external identifier 'Dma_Ip_SetLogicChannelScatterGatherList' (Dma_Ip.c line 1405).	File Scope	1590	18	No	Unset	Unreviewed	
31561	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelS catterGatherConfig()	1618	73	No	Unset	Unreviewed	
31562	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetLogicChannelS catterGatherConfig()	1618	73	No	Unset	Unreviewed	

Table 4.24.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
132	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4191. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
41480	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Re setEvent()	144	42	No	Unset	Unreviewed	
41481	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Re setEvent()	144	42	No	Unset	Unreviewed	
41482	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	Dma_Ip_SetHwChannel_Re setEvent()	144	53	No	Unset	Unreviewed	
41483	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	Dma_Ip_SetHwChannel_Re setEvent()	144	53	No	Unset	Unreviewed	
31486	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Re setEvent()	144	62	No	Unset	Unreviewed	
31487	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Re setEvent()	144	62	No	Unset	Unreviewed	
41498	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Dma_Ip_SetHwChannel_Re adyEvent()	157	42	No	Unset	Unreviewed	
41499	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Re adyEvent()	157	42	No	Unset	Unreviewed	
41500	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	Dma_Ip_SetHwChannel_Re adyEvent()	157	53	No	Unset	Unreviewed	
41501	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	Dma_Ip_SetHwChannel_Re adyEvent()	157	53	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31490	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Re adyEvent()	157	62	No	Unset	Unreviewed	
31491	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Re adyEvent()	157	62	No	Unset	Unreviewed	
41508	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	42	No	Unset	Unreviewed	
41509	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	42	No	Unset	Unreviewed	
41510	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	53	No	Unset	Unreviewed	
41511	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	53	No	Unset	Unreviewed	
31494	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	62	No	Unset	Unreviewed	
31495	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Tr ansferEvent()	170	62	No	Unset	Unreviewed	
41534	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	42	No	Unset	Unreviewed	
41535	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	42	No	Unset	Unreviewed	
41538	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	53	No	Unset	Unreviewed	
41539	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	53	No	Unset	Unreviewed	
31503	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	62	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
31504	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Sc atterGatherEvent()	183	62	No	Unset	Unreviewed	
41561	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Dma_Ip_SetHwChannel_Er rorEvent()	196	42	No	Unset	Unreviewed	
41562	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Dma_Ip_SetHwChannel_Er rorEvent()	196	42	No	Unset	Unreviewed	
41565	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	Dma_Ip_SetHwChannel_Er rorEvent()	196	53	No	Unset	Unreviewed	
41566	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	Dma_Ip_SetHwChannel_Er rorEvent()	196	53	No	Unset	Unreviewed	
31507	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Er rorEvent()	196	62	No	Unset	Unreviewed	
31508	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Dma_Ip_SetHwChannel_Er rorEvent()	196	62	No	Unset	Unreviewed	

Table 4.25.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Hw_Access.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
133	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this translation unit: 4211. The limit is 4095.	1	1	1	No	Unset	Unreviewed	
41724	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	hwAccDmaInst_CmdCance lTransfer()	366	73	No	Unset	Unreviewed	
41725	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	hwAccDmaInst_CmdCance lTransfer()	366	73	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41730	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	hwAccDmaInst_CmdCance lTransferWithError()	387	82	No	Unset	Unreviewed	
41731	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	hwAccDmaInst_CmdCance lTransferWithError()	387	82	No	Unset	Unreviewed	
41749	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	hwAccDmaInst_CmdHalt()	408	63	No	Unset	Unreviewed	
41750	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	hwAccDmaInst_CmdHalt()	408	63	No	Unset	Unreviewed	
41759	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	hwAccDmaInst_CmdResu me()	429	65	No	Unset	Unreviewed	
41760	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	hwAccDmaInst_CmdResu me()	429	65	No	Unset	Unreviewed	
41808	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_CmdSetHw Request()	493	65	No	Unset	Unreviewed	
41809	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_CmdSetHw Request()	493	65	No	Unset	Unreviewed	
41810	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_CmdSetHw Request()	493	76	No	Unset	Unreviewed	
41811	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_CmdSetHw Request()	493	76	No	Unset	Unreviewed	
41832	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_CmdClearH wRequest()	511	67	No	Unset	Unreviewed	
41833	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_CmdClearH wRequest()	511	67	No	Unset	Unreviewed	
41834	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_CmdClearH wRequest()	511	78	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41835	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_CmdClearH wRequest()	511	78	No	Unset	Unreviewed	
41850	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_CmdSetSw Request()	529	65	No	Unset	Unreviewed	
41851	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_CmdSetSw Request()	529	65	No	Unset	Unreviewed	
41852	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_CmdSetSw Request()	529	76	No	Unset	Unreviewed	
41853	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_CmdSetSw Request()	529	76	No	Unset	Unreviewed	
41864	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_CmdClearD one()	547	64	No	Unset	Unreviewed	
41865	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_CmdClearD one()	547	64	No	Unset	Unreviewed	
41866	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_CmdClearD one()	547	75	No	Unset	Unreviewed	
41867	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_CmdClearD one()	547	75	No	Unset	Unreviewed	
41878	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_CmdClearE rror()	565	65	No	Unset	Unreviewed	
41879	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_CmdClearE rror()	565	65	No	Unset	Unreviewed	
41880	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_CmdClearE rror()	565	76	No	Unset	Unreviewed	
41881	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	HwAccDmaCh_CmdClearE rror()	565	76	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [031]							
41927	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	84	No	Unset	Unreviewed	
41928	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	84	No	Unset	Unreviewed	
41929	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	95	No	Unset	Unreviewed	
41930	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	95	No	Unset	Unreviewed	
41945	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	79	No	Unset	Unreviewed	
41946	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	79	No	Unset	Unreviewed	
41947	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	90	No	Unset	Unreviewed	
41948	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	90	No	Unset	Unreviewed	
41956	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetRequest _EnRequest()	683	74	No	Unset	Unreviewed	
41957	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetRequest _EnRequest()	683	74	No	Unset	Unreviewed	
41958	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetRequest _EnRequest()	683	85	No	Unset	Unreviewed	
41959	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetRequest _EnRequest()	683	85	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41975	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetInterrup t_EnError()	700	72	No	Unset	Unreviewed	
41976	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetInterrup t_EnError()	700	72	No	Unset	Unreviewed	
41977	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetInterrup t_EnError()	700	83	No	Unset	Unreviewed	
41978	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetInterrup t_EnError()	700	83	No	Unset	Unreviewed	
42001	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetPriority_ Group()	739	91	No	Unset	Unreviewed	
42002	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetPriority_ Group()	739	91	No	Unset	Unreviewed	
42011	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetPriority_ Level()	758	69	No	Unset	Unreviewed	
42012	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetPriority_ Level()	758	69	No	Unset	Unreviewed	
42013	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetPriority_ Level()	758	80	No	Unset	Unreviewed	
42014	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetPriority_ Level()	758	80	No	Unset	Unreviewed	
42027	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetPriority_ EnPreemption()	777	76	No	Unset	Unreviewed	
42028	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetPriority_ EnPreemption()	777	76	No	Unset	Unreviewed	
42029	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetPriority_ EnPreemption()	777	87	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42030	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetPriority_ EnPreemption()	777	87	No	Unset	Unreviewed	
42041	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_SetPriority_ DisPreempt()	797	74	No	Unset	Unreviewed	
42042	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_SetPriority_ DisPreempt()	797	74	No	Unset	Unreviewed	
42043	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_SetPriority_ DisPreempt()	797	85	No	Unset	Unreviewed	
42044	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_SetPriority_ DisPreempt()	797	85	No	Unset	Unreviewed	
137	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	HwAccDmaCh_SetControl_ EnStart()	1243	61	No	Unset	Unreviewed	
42361	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetSourceA ddress()	1408	68	No	Unset	Unreviewed	
42362	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetSourceA ddress()	1408	68	No	Unset	Unreviewed	
42363	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetSourceA ddress()	1408	79	No	Unset	Unreviewed	
42364	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetSourceA ddress()	1408	79	No	Unset	Unreviewed	
42377	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetDestinat ionAddress()	1433	73	No	Unset	Unreviewed	
42378	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	HwAccDmaCh_GetDestinat ionAddress()	1433	73	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [00]							
42379	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetDestinat ionAddress()	1433	84	No	Unset	Unreviewed	
42380	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetDestinat ionAddress()	1433	84	No	Unset	Unreviewed	
42391	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetBeginIte rCount()	1456	69	No	Unset	Unreviewed	
42392	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetBeginIte rCount()	1456	69	No	Unset	Unreviewed	
42393	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetBeginIte rCount()	1456	80	No	Unset	Unreviewed	
42394	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetBeginIte rCount()	1456	80	No	Unset	Unreviewed	
42403	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetCurrent IterCount()	1474	71	No	Unset	Unreviewed	
42404	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetCurrent IterCount()	1474	71	No	Unset	Unreviewed	
42405	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetCurrent IterCount()	1474	82	No	Unset	Unreviewed	
42406	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetCurrent IterCount()	1474	82	No	Unset	Unreviewed	
42417	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetStoreDst Address()	1496	70	No	Unset	Unreviewed	
42418	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetStoreDst Address()	1496	70	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42419	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetStoreDst Address()	1496	81	No	Unset	Unreviewed	
42420	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetStoreDst Address()	1496	81	No	Unset	Unreviewed	
42423	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	HwAccDmaCh_GetStoreDst Address()	1500	24	No	Unset	Unreviewed	
42424	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	HwAccDmaCh_GetStoreDst Address()	1500	24	No	Unset	Unreviewed	
42431	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetMasterId()	1521	63	No	Unset	Unreviewed	
42432	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetMasterI d()	1521	63	No	Unset	Unreviewed	
42433	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetMasterId()	1521	74	No	Unset	Unreviewed	
42434	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetMasterI d()	1521	74	No	Unset	Unreviewed	
42441	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetIntMajo r()	1540	63	No	Unset	Unreviewed	
42442	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetIntMajo r()	1540	63	No	Unset	Unreviewed	
42443	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetIntMajo r()	1540	74	No	Unset	Unreviewed	
42444	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetIntMajo r()	1540	74	No	Unset	Unreviewed	
42451	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	HwAccDmaCh_GetIntHalf Major()	1558	67	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42452	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	HwAccDmaCh_GetIntHalf Major()	1558	67	No	Unset	Unreviewed	
42453	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [031]	HwAccDmaCh_GetIntHalf Major()	1558	78	No	Unset	Unreviewed	
42454	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [031]	HwAccDmaCh_GetIntHalf Major()	1558	78	No	Unset	Unreviewed	

Table 4.26. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Irq.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
134	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4210. The limit is 4095.	-	1	1	No	Unset	Unreviewed	
41722	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	IrqHandler()	362	73	No	Unset	Unreviewed	
41723	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	IrqHandler()	362	73	No	Unset	Unreviewed	
136	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	-	373	71	No	Unset	Unreviewed	

Table 4.27.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Multicore.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
135	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme	File Scope	1	1	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4187. The limit is 4095.							

Table 4.28.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Emios_Mcl_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
155	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Emios_Ip_paxBase' variable has no previous declaration.	File Scope	97	12	No	Unset	Unreviewed	
41546	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_Init()	187	55	No	Unset	Unreviewed	
41547	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Init()	187	55	No	Unset	Unreviewed	
152	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	203	22	No	Unset	Unreviewed	
157	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	204	22	No	Unset	Unreviewed	
156	10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essent ial type. The right operand of the operator shall not have wider essential type (unsigned on 32 bits) than the left operand (unsigned on 16 bits) which is a composite expression	Emios_Mcl_Ip_Init()	204	114	No	Unset	Unreviewed	
31510	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Emios_Mcl_Ip_Init()	204	198	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
154	12.2	The right hand operand of a shift operator shall lie in the range zero to one less than the width in bits of the essential type of the left hand operand.	Emios_Mcl_Ip_Init()	205	102	No	Unset	Unreviewed	
159	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	212	91	No	Unset	Unreviewed	
165	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	216	88	No	Unset	Unreviewed	
158	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	247	87	No	Unset	Unreviewed	
160	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Init()	267	18	No	Unset	Unreviewed	
41641	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_EnableChan nel()	288	39	No	Unset	Unreviewed	
41642	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_EnableChan nel()	288	39	No	Unset	Unreviewed	
41650	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Emios_Mcl_Ip_EnableChan nel()	289	39	No	Unset	Unreviewed	
41668	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_DisableCha nnel()	300	39	No	Unset	Unreviewed	
41669	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Emios_Mcl_Ip_DisableCha nnel()	300	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [01]							
41670	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Emios_Mcl_Ip_DisableCha nnel()	301	38	No	Unset	Unreviewed	
41681	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Comparator TransferEnable()	312	39	No	Unset	Unreviewed	
41682	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Comparator TransferEnable()	312	39	No	Unset	Unreviewed	
18028	5.1	External identifiers shall be distinct. External function 'Emios_Mcl_Ip_ComparatorTransferDisa ble' conflicts with the external identifier 'Emios_Mcl_Ip_C omparatorTransferEnable' (Emios_Mcl_Ip.c line 305).	File Scope	318	5	No	Unset	Unreviewed	
41688	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Comparator TransferDisable()	325	39	No	Unset	Unreviewed	
41689	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Comparator TransferDisable()	325	39	No	Unset	Unreviewed	
41698	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_Deinit()	339	55	No	Unset	Unreviewed	
41699	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_Deinit()	339	55	No	Unset	Unreviewed	
162	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Deinit()	352	22	No	Unset	Unreviewed	
161	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Deinit()	353	22	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
163	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Deinit()	362	72	No	Unset	Unreviewed	
164	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_Deinit()	363	72	No	Unset	Unreviewed	
41741	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_SetReloadIn terval()	398	20	No	Unset	Unreviewed	
41742	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_SetReloadIn terval()	398	20	No	Unset	Unreviewed	
41743	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [023]	Emios_Mcl_Ip_SetReloadIn terval()	398	39	No	Unset	Unreviewed	
41744	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [023]	Emios_Mcl_Ip_SetReloadIn terval()	398	39	No	Unset	Unreviewed	
41751	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_ValidateCha nnel()	411	31	No	Unset	Unreviewed	
41752	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_ValidateCha nnel()	411	31	No	Unset	Unreviewed	
41753	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [023]	Emios_Mcl_Ip_ValidateCha nnel()	411	43	No	Unset	Unreviewed	
41754	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [023]	Emios_Mcl_Ip_ValidateCha nnel()	411	43	No	Unset	Unreviewed	
41763	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_SetCounter BusPeriod()	432	29	No	Unset	Unreviewed	
41764	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Emios_Mcl_Ip_SetCounter BusPeriod()	432	29	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [01]							
41765	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [023]	Emios_Mcl_Ip_SetCounter BusPeriod()	432	41	No	Unset	Unreviewed	
41766	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [023]	Emios_Mcl_Ip_SetCounter BusPeriod()	432	41	No	Unset	Unreviewed	
41789	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_GetCounter BusPeriod()	468	56	No	Unset	Unreviewed	
41790	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_GetCounter BusPeriod()	468	56	No	Unset	Unreviewed	
41791	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [023]	Emios_Mcl_Ip_GetCounter BusPeriod()	468	66	No	Unset	Unreviewed	
41792	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [023]	Emios_Mcl_Ip_GetCounter BusPeriod()	468	66	No	Unset	Unreviewed	
41818	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Emios_Mcl_Ip_ConfigureGl obalTimebase()	497	39	No	Unset	Unreviewed	
41819	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Emios_Mcl_Ip_ConfigureGl obalTimebase()	497	39	No	Unset	Unreviewed	
153	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_ConfigureGl obalTimebase()	501	18	No	Unset	Unreviewed	
166	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Emios_Mcl_Ip_ConfigureGl obalTimebase()	505	18	No	Unset	Unreviewed	

Table 4.29. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41484	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Flexio_Mcl_Ip_InitDevice()	148	32	No	Unset	Unreviewed	
41485	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Flexio_Mcl_Ip_InitDevice()	148	32	No	Unset	Unreviewed	
41516	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Flexio_Mcl_Ip_DeinitDevic e()	176	32	No	Unset	Unreviewed	
41517	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Flexio_Mcl_Ip_DeinitDevic e()	176	32	No	Unset	Unreviewed	

Table 4.30.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip_HwAccess. c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
168	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Flexio_Mcl_Ip_SetDebugEn able()	179	13	No	Unset	Unreviewed	
31498	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Flexio_Mcl_Ip_SetDebugEn able()	179	62	No	Unset	Unreviewed	
169	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Flexio_Mcl_Ip_GetShifterSt atus()	209	11	No	Unset	Unreviewed	
41586	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Flexio_Mcl_Ip_GetShifterSt atus()	209	35	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
170	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Flexio_Mcl_Ip_ClearShifter Status()	234	24	No	Unset	Unreviewed	
41607	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[031]	Flexio_Mcl_Ip_ClearShifter Status()	234	30	No	Unset	Unreviewed	
173	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)		248	11	No	Unset	Unreviewed	
41616	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]		248	34	No	Unset	Unreviewed	
171	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Flexio_Mcl_Ip_ClearShifter ErrorStatus()	273	23	No	Unset	Unreviewed	
41637	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[031]	Flexio_Mcl_Ip_ClearShifter ErrorStatus()	273	29	No	Unset	Unreviewed	
172	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Flexio_Mcl_Ip_GetTimerSt atus()	289	11	No	Unset	Unreviewed	
41649	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Flexio_Mcl_Ip_GetTimerSt atus()	289	33	No	Unset	Unreviewed	
174	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assig	Flexio_Mcl_Ip_GetTimerInt erruptEnable()	295	11	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ned to an object with a different essential type category (B oolean)							
41659	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Flexio_Mcl_Ip_GetTimerInt erruptEnable()	295	32	No	Unset	Unreviewed	
175	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)		320	22	No	Unset	Unreviewed	
41683	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[031]	Flexio_Mcl_Ip_ClearTimer Status()	320	28	No	Unset	Unreviewed	
18085	5.1	External identifiers shall be distinct. External function 'Flexio_Mcl_Ip_GetAllShifterErrorInterr upt' conflicts with the external identifier 'Flexio_Mcl_Ip_G etAllShifterErrorStatus' (Flexio_Mcl_Ip_HwAccess.h line 3 15).	File Scope	339	8	No	Unset	Unreviewed	

Table 4.31. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Uart_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
176	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Flexio_Uart_Ip_apStateStructure' variable has no previous declaration.	File Scope	171	34	No	Unset	Unreviewed	
177	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Flexio_Uart_Ip_apUserConfig' variable has no previous declaration.	File Scope	195	38	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41494	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [05]	I2c_Ipw_SlaveCallback()	154	31	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41495	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [05]	I2c_Ipw_SlaveCallback()	154	31	No	Unset	Unreviewed	
41504	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [05]	I2c_Ipw_SlaveCallback()	159	31	No	Unset	Unreviewed	
41505	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [05]	I2c_Ipw_SlaveCallback()	159	31	No	Unset	Unreviewed	

Table 4.33. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Igf_Port_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
187	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5417. The limit is 4095.	1	1	1	No	Unset	Unreviewed	

Table 4.34. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\IntCtrl_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
199	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'INT_SRAM_START' that has external linkage sh ould be declared in a header file.	File Scope	77	14	No	Unset	Unreviewed	
194	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_SCB" is undefined	IntCtrl_Ip_InstallHandlerP rivileged()	207	14	No	Unset	Unreviewed	
195	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	IntCtrl_Ip_EnableIrqPrivile ged()	273	4	No	Unset	Unreviewed	
196	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	IntCtrl_Ip_DisableIrqPrivil eged()	303	4	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
197	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	IntCtrl_Ip_SetPriorityPrivil eged()	327	4	No	Unset	Unreviewed	
198	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	IntCtrl_Ip_GetPriorityPrivil eged()	383	23	No	Unset	Unreviewed	
200	D2.1	All source files shall compile without any compilation err ors. Identifier "S32_NVIC" is undefined	IntCtrl_Ip_ClearPendingPri vileged()	443	4	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
201	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4845. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
31546	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Lpi2c_Ip_MasterComplete DMATransfer()	971	14	No	Unset	Unreviewed	
31547	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Lpi2c_Ip_MasterComplete DMATransfer()	971	14	No	Unset	Unreviewed	
42501	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [01]	Lpi2c_Ip_MasterSetBaudR ate()	1710	30	No	Unset	Unreviewed	
42502	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Lpi2c_Ip_MasterSetBaudR ate()	1710	30	No	Unset	Unreviewed	
31563	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Lpi2c_Ip_MasterSetBaudR ate()	1717	13	No	Unset	Unreviewed	
31564	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Lpi2c_Ip_MasterSetBaudR ate()	1717	13	No	Unset	Unreviewed	
42599	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Lpi2c_Ip_MasterGetBaudR ate()	2008	30	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42600	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Lpi2c_Ip_MasterGetBaudR ate()	2008	30	No	Unset	Unreviewed	
31565	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Lpi2c_Ip_MasterGetBaudR ate()	2020	38	No	Unset	Unreviewed	
31566	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Lpi2c_Ip_MasterGetBaudR ate()	2020	38	No	Unset	Unreviewed	
17827	5.1	External identifiers shall be distinct. External function 'Lpi2c_Ip_DmaCompleteNotification1' c onflicts with the external identifier 'Lpi2c_Ip_DmaComple teNotification0' (Lpi2c_Ip.c line 3183).	File Scope	3240	5	No	Unset	Unreviewed	

Table 4.36. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpuart_Uart_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
212	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Lpuart_Uart_Ip_apStateStructure' variable has no previous declaration.	File Scope	154	34	No	Unset	Unreviewed	
207	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Lpuart_Uart_Ip_apUserConfig' variable has no previous declaration.	-	157	38	No	Unset	Unreviewed	
205	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volatile-qualified type		348	12	No	Unset	Unreviewed	
208	13.5	The right hand operand of a logical && or $\mid\mid$ operator shal l not contain persistent side effects.	Lpuart_Uart_Ip_Deinit()	348	56	No	Unset	Unreviewed	
206	13.5	The right hand operand of a logical && or $\mid \mid$ operator shal l not contain persistent side effects.	Lpuart_Uart_Ip_SyncSend()	431	40	No	Unset	Unreviewed	
210	13.5	The right hand operand of a logical && or $\mid\mid$ operator shal l not contain persistent side effects.	Lpuart_Uart_Ip_SyncSend()	435	90	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
204	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evalu ation because of multiple read accesses to objects of volati le-qualified type	Lpuart_Uart_Ip_SyncRecei ve()	719	19	No	Unset	Unreviewed	
213	13.5	The right hand operand of a logical && or $ \ $ operator shall not contain persistent side effects.	Lpuart_Uart_Ip_StartGetDa ta()	762	40	No	Unset	Unreviewed	
211	13.5	The right hand operand of a logical && or operator shall not contain persistent side effects.	Lpuart_Uart_Ip_StartGetDa ta()	766	105	No	Unset	Unreviewed	
214	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evalu ation because of multiple read accesses to objects of volati le-qualified type	Lpuart_Uart_Ip_SetBaudRa te()	1024	14	No	Unset	Unreviewed	
215	13.5	The right hand operand of a logical && or $ \ $ operator shal l not contain persistent side effects.	Lpuart_Uart_Ip_CompleteS endDataUsingInt()	1595	75	No	Unset	Unreviewed	
209	13.5	The right hand operand of a logical && or $ \ $ operator shall not contain persistent side effects.	Lpuart_Uart_Ip_CompleteR eceiveDataUsingInt()	1882	81	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
203	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5894. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
42075	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Mcu_InitClock()	849	54	No	Unset	Unreviewed	
42076	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Mcu_InitClock()	849	54	No	Unset	Unreviewed	
31542	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Mcu_InitClock()	867	47	No	Unset	Unreviewed	
31543	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Mcu_InitClock()	867	47	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Error: pointer is outside its bounds							
42113	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [00]	Mcu_SetMode()	902	48	No	Unset	Unreviewed	
42114	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [00]	Mcu_SetMode()	902	48	No	Unset	Unreviewed	
31544	D4.1	Run-time failures shall be minimized. Error: pointer is outside its bounds	Mcu_SetMode()	917	46	No	Unset	Unreviewed	
31545	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Error: pointer is outside its bounds	Mcu_SetMode()	917	46	No	Unset	Unreviewed	

Table 4.38.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu_Dem_Wrapper.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
219	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5851. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

Table 4.39. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu_Ipw.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
221	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 9659. The limit is 4095.	1	1	1	No	Unset	Unreviewed	

Table 4.40. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\OsIf_Timer_System_Internal_Systick.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
229	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	_	116	8	No	Unset	Unreviewed	
227	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	al_Init()	159	45	No	Unset	Unreviewed	
228	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	al_Init()	160	45	No	Unset	Unreviewed	

Table 4.41. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Pit_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
230	9.3	Arrays shall not be partially initialized.	File Scope	197	84	No	Unset	Unreviewed	
231	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Pit_Ip_Reset()	565	37	No	Unset	Unreviewed	
232	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Pit_Ip_Reset()	597	42	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
233	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Pit_Ip_IsChannelRunning()	628	18	No	Unset	Unreviewed	
41939	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Pit_Ip_SetCounterValue()	658	15	No	Unset	Unreviewed	
41940	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Pit_Ip_SetCounterValue()	658	15	No	Unset	Unreviewed	
42031	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Pit_Ip_GetUpperLifetimerV alue()	784	24	No	Unset	Unreviewed	
42032	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Pit_Ip_GetUpperLifetimerV alue()	784	24	No	Unset	Unreviewed	
234	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (Boolean)	Pit_Ip_GetInterruptEnable Flag()	928	19	No	Unset	Unreviewed	
235	12.2	The right hand operand of a shift operator shall lie in the range zero to one less than the width in bits of the essential type of the left hand operand.	Pit_Ip_GetLifetimeTimer()	1595	37	No	Unset	Unreviewed	
31560	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[031]	Pit_Ip_GetLifetimeTimer()	1595	37	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
236	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 7687. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
238	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'Port_Config' that has external linkage should be declared in a header file.	File Scope	134	33	No	Unset	Unreviewed	

Table 4.43. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Port_Ipw.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
240	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 7710. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
243	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Port_Ipw_SetInoutMode()	788	34	No	Unset	Unreviewed	
244	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Port_Ipw_SetInputMode()	862	34	No	Unset	Unreviewed	

Table 4.44. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_DCM_GPR.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
245	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	lPadkeepingConfig()	246	14	No	Unset	Unreviewed	
31520	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Power_Ip_DCM_GPR_Globa lPadkeepingConfig()	246	51	No	Unset	Unreviewed	

Table 4.45. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_RGM.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41803	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]		484	37	No	Unset	Unreviewed	
41926	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]		642	41	No	Unset	Unreviewed	
41955	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]		681	41	No	Unset	Unreviewed	
41962	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]		686	47	No	Unset	Unreviewed	

Table 4.46. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Ram_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41640	D4.1	Run-time failures shall be minimized. Warning: scalar division by zero may occur	Ram_Ip_InitRamSection()	284	90	No	Unset	Unreviewed	
41657	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	294	18	No	Unset	Unreviewed	
41658	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	294	18	No	Unset	Unreviewed	
41664	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	299	18	No	Unset	Unreviewed	
41665	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	299	18	No	Unset	Unreviewed	
41671	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	304	18	No	Unset	Unreviewed	
41672	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	Ram_Ip_InitRamSection()	304	18	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: pointer may be outside its bounds							
41677	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	309	18	No	Unset	Unreviewed	
41678	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	309	18	No	Unset	Unreviewed	
41690	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	329	69	No	Unset	Unreviewed	
41691	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	329	69	No	Unset	Unreviewed	
41692	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	337	69	No	Unset	Unreviewed	
41693	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	337	69	No	Unset	Unreviewed	
41704	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	345	69	No	Unset	Unreviewed	
41705	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	345	69	No	Unset	Unreviewed	
41710	D4.1	Run-time failures shall be minimized. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	353	69	No	Unset	Unreviewed	
41711	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: pointer may be outside its bounds	Ram_Ip_InitRamSection()	353	69	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
369	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type.	File Scope	95	16	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.							
373	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volatile-qualified type	File Scope	95	16	No	Unset	Unreviewed	
474	D4.3	Assembly language shall be encapsulated and isolated.	Gpt_schm_read_msr()	418	8	No	Unset	Unreviewed	
368	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Gpt_schm_read_msr()	421	19	No	Unset	Unreviewed	
41816	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_00()	496	49	No	Unset	Unreviewed	
41817	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_00()	496	49	No	Unset	Unreviewed	
374	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_00()	503	8	No	Unset	Unreviewed	
41824	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_00()	510	33	No	Unset	Unreviewed	
41825	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_00()	510	33	No	Unset	Unreviewed	
41844	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_00()	519	39	No	Unset	Unreviewed	
41845	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_00()	519	39	No	Unset	Unreviewed	
420	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_00()	520	116	No	Unset	Unreviewed	
18083	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	529	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_01' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
41858	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_01()	534	49	No	Unset	Unreviewed	
41859	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_01()	534	49	No	Unset	Unreviewed	
413	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_01()	541	8	No	Unset	Unreviewed	
41868	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_01()	548	33	No	Unset	Unreviewed	
41869	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_01()	548	33	No	Unset	Unreviewed	
17869	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _01' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	553	5	No	Unset	Unreviewed	
41874	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_01()	557	39	No	Unset	Unreviewed	
41875	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_01()	557	39	No	Unset	Unreviewed	
377	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_01()	558	116	No	Unset	Unreviewed	
17976	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_02' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	567	5	No	Unset	Unreviewed	
41884	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_02()	572	49	No	Unset	Unreviewed	
41885	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_02()	572	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
370	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_02()	579	8	No	Unset	Unreviewed	
41888	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_02()	586	33	No	Unset	Unreviewed	
41889	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_02()	586	33	No	Unset	Unreviewed	
18026	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _02' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	591	5	No	Unset	Unreviewed	
41896	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_02()	595	39	No	Unset	Unreviewed	
41897	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_02()	595	39	No	Unset	Unreviewed	
381	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_02()	596	116	No	Unset	Unreviewed	
17951	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_03' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	605	5	No	Unset	Unreviewed	
41908	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_03()	610	49	No	Unset	Unreviewed	
41909	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_03()	610	49	No	Unset	Unreviewed	
386	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		617	8	No	Unset	Unreviewed	
41916	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_03()	624	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41917	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_03()	624	33	No	Unset	Unreviewed	
18123	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _03' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	629	5	No	Unset	Unreviewed	
41922	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_03()	633	39	No	Unset	Unreviewed	
41923	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_03()	633	39	No	Unset	Unreviewed	
455	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_03()	634	116	No	Unset	Unreviewed	
18060	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_04' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	643	5	No	Unset	Unreviewed	
41933	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_04()	648	49	No	Unset	Unreviewed	
41934	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_04()	648	49	No	Unset	Unreviewed	
384	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_04()	655	8	No	Unset	Unreviewed	
41941	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_04()	662	33	No	Unset	Unreviewed	
41942	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_04()	662	33	No	Unset	Unreviewed	
18059	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	667	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_04' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
41951	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_04()	671	39	No	Unset	Unreviewed	
41952	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_04()	671	39	No	Unset	Unreviewed	
376	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_04()	672	116	No	Unset	Unreviewed	
17927	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_05' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	681	5	No	Unset	Unreviewed	
41963	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_05()	686	49	No	Unset	Unreviewed	
41964	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_05()	686	49	No	Unset	Unreviewed	
407	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_05()	693	8	No	Unset	Unreviewed	
41973	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_05()	700	33	No	Unset	Unreviewed	
41974	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_05()	700	33	No	Unset	Unreviewed	
18043	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _05' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	705	5	No	Unset	Unreviewed	
41985	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_05()	709	39	No	Unset	Unreviewed	
41986	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_05()	709	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
469	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_05()	710	116	No	Unset	Unreviewed	
17969	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_06' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	719	5	No	Unset	Unreviewed	
41991	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_06()	724	49	No	Unset	Unreviewed	
41992	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_06()	724	49	No	Unset	Unreviewed	
472	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		731	8	No	Unset	Unreviewed	
41997	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_06()	738	33	No	Unset	Unreviewed	
41998	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_06()	738	33	No	Unset	Unreviewed	
17889	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _06' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	743	5	No	Unset	Unreviewed	
42007	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_06()	747	39	No	Unset	Unreviewed	
42008	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_06()	747	39	No	Unset	Unreviewed	
382	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_06()	748	116	No	Unset	Unreviewed	
17975	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	757	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_07' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42017	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_07()	762	49	No	Unset	Unreviewed	
42018	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_07()	762	49	No	Unset	Unreviewed	
393	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_07()	769	8	No	Unset	Unreviewed	
42023	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_07()	776	33	No	Unset	Unreviewed	
42024	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_07()	776	33	No	Unset	Unreviewed	
18090	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _07' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	781	5	No	Unset	Unreviewed	
42037	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_07()	785	39	No	Unset	Unreviewed	
42038	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_07()	785	39	No	Unset	Unreviewed	
390	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_07()	786	116	No	Unset	Unreviewed	
17967	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_10' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	795	5	No	Unset	Unreviewed	
42047	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_10()	800	49	No	Unset	Unreviewed	
42048	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_10()	800	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
452	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_10()	807	8	No	Unset	Unreviewed	
42053	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_10()	814	33	No	Unset	Unreviewed	
42054	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_10()	814	33	No	Unset	Unreviewed	
17941	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _10' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	819	5	No	Unset	Unreviewed	
42059	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_10()	823	39	No	Unset	Unreviewed	
42060	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_10()	823	39	No	Unset	Unreviewed	
394	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_10()	824	116	No	Unset	Unreviewed	
18121	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_11' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	833	5	No	Unset	Unreviewed	
42065	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_11()	838	49	No	Unset	Unreviewed	
42066	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_11()	838	49	No	Unset	Unreviewed	
395	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		845	8	No	Unset	Unreviewed	
42079	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_11()	852	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42080	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_11()	852	33	No	Unset	Unreviewed	
17960	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _11' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	857	5	No	Unset	Unreviewed	
42089	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_11()	861	39	No	Unset	Unreviewed	
42090	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_11()	861	39	No	Unset	Unreviewed	
409	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_11()	862	116	No	Unset	Unreviewed	
17966	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_12' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	871	5	No	Unset	Unreviewed	
42097	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_12()	876	49	No	Unset	Unreviewed	
42098	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_12()	876	49	No	Unset	Unreviewed	
440	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_12()	883	8	No	Unset	Unreviewed	
42105	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_12()	890	33	No	Unset	Unreviewed	
42106	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_12()	890	33	No	Unset	Unreviewed	
18057	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	895	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_12' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42111	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_12()	899	39	No	Unset	Unreviewed	
42112	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_12()	899	39	No	Unset	Unreviewed	
389	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_12()	900	116	No	Unset	Unreviewed	
17964	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_17' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	909	5	No	Unset	Unreviewed	
42117	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_17()	914	49	No	Unset	Unreviewed	
42118	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_17()	914	49	No	Unset	Unreviewed	
478	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_17()	921	8	No	Unset	Unreviewed	
42121	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_17()	928	33	No	Unset	Unreviewed	
42122	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_17()	928	33	No	Unset	Unreviewed	
18018	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _17' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	933	5	No	Unset	Unreviewed	
42125	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_17()	937	39	No	Unset	Unreviewed	
42126	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_17()	937	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
414	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_17()	938	116	No	Unset	Unreviewed	
17962	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_21' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	947	5	No	Unset	Unreviewed	
42129	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_21()	952	49	No	Unset	Unreviewed	
42130	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_21()	952	49	No	Unset	Unreviewed	
387	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_21()	959	8	No	Unset	Unreviewed	
42133	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_21()	966	33	No	Unset	Unreviewed	
42134	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_21()	966	33	No	Unset	Unreviewed	
17921	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _21' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	971	5	No	Unset	Unreviewed	
42139	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_21()	975	39	No	Unset	Unreviewed	
42140	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_21()	975	39	No	Unset	Unreviewed	
392	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_21()	976	116	No	Unset	Unreviewed	
18023	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	985	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_22' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42145	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_22()	990	49	No	Unset	Unreviewed	
42146	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_22()	990	49	No	Unset	Unreviewed	
400	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_22()	997	8	No	Unset	Unreviewed	
42161	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_22()	1004	33	No	Unset	Unreviewed	
42162	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_22()	1004	33	No	Unset	Unreviewed	
18109	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _22' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1009	5	No	Unset	Unreviewed	
42167	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_22()	1013	39	No	Unset	Unreviewed	
42168	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_22()	1013	39	No	Unset	Unreviewed	
396	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_22()	1014	116	No	Unset	Unreviewed	
18065	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_23' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1023	5	No	Unset	Unreviewed	
42173	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_23()	1028	49	No	Unset	Unreviewed	
42174	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_23()	1028	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
372	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_23()	1035	8	No	Unset	Unreviewed	
42181	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_23()	1042	33	No	Unset	Unreviewed	
42182	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_23()	1042	33	No	Unset	Unreviewed	
18080	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _23' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1047	5	No	Unset	Unreviewed	
42187	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_23()	1051	39	No	Unset	Unreviewed	
42188	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_23()	1051	39	No	Unset	Unreviewed	
398	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_23()	1052	116	No	Unset	Unreviewed	
17925	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_24' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1061	5	No	Unset	Unreviewed	
42193	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_24()	1066	49	No	Unset	Unreviewed	
42194	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_24()	1066	49	No	Unset	Unreviewed	
405	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1073	8	No	Unset	Unreviewed	
42201	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_24()	1080	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42202	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_24()	1080	33	No	Unset	Unreviewed	
18008	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _24' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1085	5	No	Unset	Unreviewed	
42209	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_24()	1089	39	No	Unset	Unreviewed	
42210	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_24()	1089	39	No	Unset	Unreviewed	
466	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_24()	1090	116	No	Unset	Unreviewed	
17950	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_25' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1099	5	No	Unset	Unreviewed	
42215	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_25()	1104	49	No	Unset	Unreviewed	
42216	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_25()	1104	49	No	Unset	Unreviewed	
375	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_25()	1111	8	No	Unset	Unreviewed	
42221	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_25()	1118	33	No	Unset	Unreviewed	
42222	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_25()	1118	33	No	Unset	Unreviewed	
17904	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	1123	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_25' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42227	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_25()	1127	39	No	Unset	Unreviewed	
42228	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_25()	1127	39	No	Unset	Unreviewed	
415	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_25()	1128	116	No	Unset	Unreviewed	
17943	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_26' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1137	5	No	Unset	Unreviewed	
42233	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_26()	1142	49	No	Unset	Unreviewed	
42234	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_26()	1142	49	No	Unset	Unreviewed	
403	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_26()	1149	8	No	Unset	Unreviewed	
42239	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_26()	1156	33	No	Unset	Unreviewed	
42240	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_26()	1156	33	No	Unset	Unreviewed	
18114	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _26' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1161	5	No	Unset	Unreviewed	
42245	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_26()	1165	39	No	Unset	Unreviewed	
42246	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_26()	1165	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
477	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_26()	1166	116	No	Unset	Unreviewed	
18125	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_27' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1175	5	No	Unset	Unreviewed	
42251	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_27()	1180	49	No	Unset	Unreviewed	
42252	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_27()	1180	49	No	Unset	Unreviewed	
418	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_27()	1187	8	No	Unset	Unreviewed	
42257	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_27()	1194	33	No	Unset	Unreviewed	
42258	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_27()	1194	33	No	Unset	Unreviewed	
17860	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _27' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1199	5	No	Unset	Unreviewed	
42263	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_27()	1203	39	No	Unset	Unreviewed	
42264	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_27()	1203	39	No	Unset	Unreviewed	
421	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_27()	1204	116	No	Unset	Unreviewed	
17948	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	1213	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_28' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42269	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_28()	1218	49	No	Unset	Unreviewed	
42270	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_28()	1218	49	No	Unset	Unreviewed	
422	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_28()	1225	8	No	Unset	Unreviewed	
42275	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_28()	1232	33	No	Unset	Unreviewed	
42276	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_28()	1232	33	No	Unset	Unreviewed	
18016	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _28' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1237	5	No	Unset	Unreviewed	
42281	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_28()	1241	39	No	Unset	Unreviewed	
42282	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_28()	1241	39	No	Unset	Unreviewed	
428	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_28()	1242	116	No	Unset	Unreviewed	
17944	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_29' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1251	5	No	Unset	Unreviewed	
42287	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_29()	1256	49	No	Unset	Unreviewed	
42288	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_29()	1256	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
423	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_29()	1263	8	No	Unset	Unreviewed	
42293	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_29()	1270	33	No	Unset	Unreviewed	
42294	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_29()	1270	33	No	Unset	Unreviewed	
18006	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _29' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1275	5	No	Unset	Unreviewed	
42299	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_29()	1279	39	No	Unset	Unreviewed	
42300	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_29()	1279	39	No	Unset	Unreviewed	
385	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_29()	1280	116	No	Unset	Unreviewed	
17947	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_30' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1289	5	No	Unset	Unreviewed	
42305	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_30()	1294	49	No	Unset	Unreviewed	
42306	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_30()	1294	49	No	Unset	Unreviewed	
433	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_30()	1301	8	No	Unset	Unreviewed	
42311	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_30()	1308	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42312	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_30()	1308	33	No	Unset	Unreviewed	
18019	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _30' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1313	5	No	Unset	Unreviewed	
42317	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_30()	1317	39	No	Unset	Unreviewed	
42318	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_30()	1317	39	No	Unset	Unreviewed	
438	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_30()	1318	116	No	Unset	Unreviewed	
18040	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_31' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1327	5	No	Unset	Unreviewed	
42323	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_31()	1332	49	No	Unset	Unreviewed	
42324	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_31()	1332	49	No	Unset	Unreviewed	
402	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_31()	1339	8	No	Unset	Unreviewed	
42329	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_31()	1346	33	No	Unset	Unreviewed	
42330	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_31()	1346	33	No	Unset	Unreviewed	
17871	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	1351	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_31' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42335	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_31()	1355	39	No	Unset	Unreviewed	
42336	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_31()	1355	39	No	Unset	Unreviewed	
371	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_31()	1356	116	No	Unset	Unreviewed	
17854	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_35' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1365	5	No	Unset	Unreviewed	
42341	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_35()	1370	49	No	Unset	Unreviewed	
42342	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_35()	1370	49	No	Unset	Unreviewed	
412	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1377	8	No	Unset	Unreviewed	
42347	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_35()	1384	33	No	Unset	Unreviewed	
42348	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_35()	1384	33	No	Unset	Unreviewed	
18039	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _35' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1389	5	No	Unset	Unreviewed	
42353	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_35()	1393	39	No	Unset	Unreviewed	
42354	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_35()	1393	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
436	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_35()	1394	116	No	Unset	Unreviewed	
17931	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_36' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1403	5	No	Unset	Unreviewed	
42359	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_36()	1408	49	No	Unset	Unreviewed	
42360	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_36()	1408	49	No	Unset	Unreviewed	
380	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_36()	1415	8	No	Unset	Unreviewed	
42369	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_36()	1422	33	No	Unset	Unreviewed	
42370	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_36()	1422	33	No	Unset	Unreviewed	
17907	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _36' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1427	5	No	Unset	Unreviewed	
42375	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_36()	1431	39	No	Unset	Unreviewed	
42376	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_36()	1431	39	No	Unset	Unreviewed	
446	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_36()	1432	116	No	Unset	Unreviewed	
17932	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	1441	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_38' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42385	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_38()	1446	49	No	Unset	Unreviewed	
42386	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_38()	1446	49	No	Unset	Unreviewed	
479	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_38()	1453	8	No	Unset	Unreviewed	
42395	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_38()	1460	33	No	Unset	Unreviewed	
42396	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_38()	1460	33	No	Unset	Unreviewed	
17999	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _38' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1465	5	No	Unset	Unreviewed	
42401	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_38()	1469	39	No	Unset	Unreviewed	
42402	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_38()	1469	39	No	Unset	Unreviewed	
425	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_38()	1470	116	No	Unset	Unreviewed	
17955	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_39' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1479	5	No	Unset	Unreviewed	
42411	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_39()	1484	49	No	Unset	Unreviewed	
42412	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_39()	1484	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
429	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_39()	1491	8	No	Unset	Unreviewed	
42421	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_39()	1498	33	No	Unset	Unreviewed	
42422	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_39()	1498	33	No	Unset	Unreviewed	
18069	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _39' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1503	5	No	Unset	Unreviewed	
42427	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_39()	1507	39	No	Unset	Unreviewed	
42428	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_39()	1507	39	No	Unset	Unreviewed	
391	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_39()	1508	116	No	Unset	Unreviewed	
17886	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_40' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1517	5	No	Unset	Unreviewed	
42435	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_40()	1522	49	No	Unset	Unreviewed	
42436	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_40()	1522	49	No	Unset	Unreviewed	
411	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1529	8	No	Unset	Unreviewed	
42439	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_40()	1536	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42440	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_40()	1536	33	No	Unset	Unreviewed	
18004	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _40' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1541	5	No	Unset	Unreviewed	
42447	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_40()	1545	39	No	Unset	Unreviewed	
42448	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_40()	1545	39	No	Unset	Unreviewed	
383	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_40()	1546	116	No	Unset	Unreviewed	
17840	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_41' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1555	5	No	Unset	Unreviewed	
42455	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_41()	1560	49	No	Unset	Unreviewed	
42456	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_41()	1560	49	No	Unset	Unreviewed	
449	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_41()	1567	8	No	Unset	Unreviewed	
42459	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_41()	1574	33	No	Unset	Unreviewed	
42460	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_41()	1574	33	No	Unset	Unreviewed	
17828	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	1579	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_41' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42463	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_41()	1583	39	No	Unset	Unreviewed	
42464	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_41()	1583	39	No	Unset	Unreviewed	
430	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_41()	1584	116	No	Unset	Unreviewed	
18061	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_42' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1593	5	No	Unset	Unreviewed	
42467	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_42()	1598	49	No	Unset	Unreviewed	
42468	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_42()	1598	49	No	Unset	Unreviewed	
447	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_42()	1605	8	No	Unset	Unreviewed	
42471	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_42()	1612	33	No	Unset	Unreviewed	
42472	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_42()	1612	33	No	Unset	Unreviewed	
18003	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _42' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1617	5	No	Unset	Unreviewed	
42475	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_42()	1621	39	No	Unset	Unreviewed	
42476	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_42()	1621	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
416	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_42()	1622	116	No	Unset	Unreviewed	
17972	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_43' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1631	5	No	Unset	Unreviewed	
42479	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_43()	1636	49	No	Unset	Unreviewed	
42480	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_43()	1636	49	No	Unset	Unreviewed	
467	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_43()	1643	8	No	Unset	Unreviewed	
42483	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_43()	1650	33	No	Unset	Unreviewed	
42484	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_43()	1650	33	No	Unset	Unreviewed	
18017	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _43' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1655	5	No	Unset	Unreviewed	
42487	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_43()	1659	39	No	Unset	Unreviewed	
42488	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_43()	1659	39	No	Unset	Unreviewed	
410	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_43()	1660	116	No	Unset	Unreviewed	
17938	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	1669	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_44' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42491	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_44()	1674	49	No	Unset	Unreviewed	
42492	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_44()	1674	49	No	Unset	Unreviewed	
427	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_44()	1681	8	No	Unset	Unreviewed	
42495	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_44()	1688	33	No	Unset	Unreviewed	
42496	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_44()	1688	33	No	Unset	Unreviewed	
18054	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _44' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1693	5	No	Unset	Unreviewed	
42499	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_44()	1697	39	No	Unset	Unreviewed	
42500	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_44()	1697	39	No	Unset	Unreviewed	
432	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_44()	1698	116	No	Unset	Unreviewed	
17957	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_45' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1707	5	No	Unset	Unreviewed	
42505	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_45()	1712	49	No	Unset	Unreviewed	
42506	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_45()	1712	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
443	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_45()	1719	8	No	Unset	Unreviewed	
42509	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_45()	1726	33	No	Unset	Unreviewed	
42510	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_45()	1726	33	No	Unset	Unreviewed	
18013	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _45' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1731	5	No	Unset	Unreviewed	
42513	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_45()	1735	39	No	Unset	Unreviewed	
42514	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_45()	1735	39	No	Unset	Unreviewed	
434	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_45()	1736	116	No	Unset	Unreviewed	
17937	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_46' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1745	5	No	Unset	Unreviewed	
42517	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_46()	1750	49	No	Unset	Unreviewed	
42518	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_46()	1750	49	No	Unset	Unreviewed	
435	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1757	8	No	Unset	Unreviewed	
42521	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_46()	1764	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42522	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_46()	1764	33	No	Unset	Unreviewed	
17968	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _46' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1769	5	No	Unset	Unreviewed	
42525	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_46()	1773	39	No	Unset	Unreviewed	
42526	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_46()	1773	39	No	Unset	Unreviewed	
437	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_46()	1774	116	No	Unset	Unreviewed	
18055	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_50' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1783	5	No	Unset	Unreviewed	
42529	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_50()	1788	49	No	Unset	Unreviewed	
42530	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_50()	1788	49	No	Unset	Unreviewed	
459	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_50()	1795	8	No	Unset	Unreviewed	
42533	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_50()	1802	33	No	Unset	Unreviewed	
42534	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_50()	1802	33	No	Unset	Unreviewed	
18000	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	1807	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_50' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42537	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_50()	1811	39	No	Unset	Unreviewed	
42538	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_50()	1811	39	No	Unset	Unreviewed	
461	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_50()	1812	116	No	Unset	Unreviewed	
17958	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_51' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1821	5	No	Unset	Unreviewed	
42541	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_51()	1826	49	No	Unset	Unreviewed	
42542	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_51()	1826	49	No	Unset	Unreviewed	
444	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_51()	1833	8	No	Unset	Unreviewed	
42545	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_51()	1840	33	No	Unset	Unreviewed	
42546	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_51()	1840	33	No	Unset	Unreviewed	
17996	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _51' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1845	5	No	Unset	Unreviewed	
42549	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_51()	1849	39	No	Unset	Unreviewed	
42550	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_51()	1849	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
388	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_51()	1850	116	No	Unset	Unreviewed	
18010	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_52' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1859	5	No	Unset	Unreviewed	
42553	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_52()	1864	49	No	Unset	Unreviewed	
42554	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_52()	1864	49	No	Unset	Unreviewed	
439	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_52()	1871	8	No	Unset	Unreviewed	
42557	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_52()	1878	33	No	Unset	Unreviewed	
42558	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_52()	1878	33	No	Unset	Unreviewed	
18082	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _52' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1883	5	No	Unset	Unreviewed	
42561	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_52()	1887	39	No	Unset	Unreviewed	
42562	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_52()	1887	39	No	Unset	Unreviewed	
419	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_52()	1888	116	No	Unset	Unreviewed	
17887	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	1897	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_53' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42565	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_53()	1902	49	No	Unset	Unreviewed	
42566	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_53()	1902	49	No	Unset	Unreviewed	
442	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_53()	1909	8	No	Unset	Unreviewed	
42569	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_53()	1916	33	No	Unset	Unreviewed	
42570	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_53()	1916	33	No	Unset	Unreviewed	
17990	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _53' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1921	5	No	Unset	Unreviewed	
42573	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_53()	1925	39	No	Unset	Unreviewed	
42574	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_53()	1925	39	No	Unset	Unreviewed	
378	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_53()	1926	116	No	Unset	Unreviewed	
18072	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_54' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1935	5	No	Unset	Unreviewed	
42577	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_54()	1940	49	No	Unset	Unreviewed	
42578	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_54()	1940	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
458	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_54()	1947	8	No	Unset	Unreviewed	
42581	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_54()	1954	33	No	Unset	Unreviewed	
42582	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_54()	1954	33	No	Unset	Unreviewed	
18091	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _54' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1959	5	No	Unset	Unreviewed	
42585	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_54()	1963	39	No	Unset	Unreviewed	
42586	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_54()	1963	39	No	Unset	Unreviewed	
473	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_54()	1964	116	No	Unset	Unreviewed	
17971	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_55' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	1973	5	No	Unset	Unreviewed	
42589	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_55()	1978	49	No	Unset	Unreviewed	
42590	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_55()	1978	49	No	Unset	Unreviewed	
468	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1985	8	No	Unset	Unreviewed	
42593	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_55()	1992	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42594	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_55()	1992	33	No	Unset	Unreviewed	
17993	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _55' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	1997	5	No	Unset	Unreviewed	
42597	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_55()	2001	39	No	Unset	Unreviewed	
42598	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_55()	2001	39	No	Unset	Unreviewed	
445	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_55()	2002	116	No	Unset	Unreviewed	
18105	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_56' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2011	5	No	Unset	Unreviewed	
42603	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_56()	2016	49	No	Unset	Unreviewed	
42604	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_56()	2016	49	No	Unset	Unreviewed	
408	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		2023	8	No	Unset	Unreviewed	
42605	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_56()	2030	33	No	Unset	Unreviewed	
42606	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_56()	2030	33	No	Unset	Unreviewed	
18012	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	2035	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_56' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42609	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_56()	2039	39	No	Unset	Unreviewed	
42610	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_56()	2039	39	No	Unset	Unreviewed	
470	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_56()	2040	116	No	Unset	Unreviewed	
17953	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_60' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2049	5	No	Unset	Unreviewed	
42613	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_60()	2054	49	No	Unset	Unreviewed	
42614	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_60()	2054	49	No	Unset	Unreviewed	
448	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_60()	2061	8	No	Unset	Unreviewed	
42617	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_60()	2068	33	No	Unset	Unreviewed	
42618	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_60()	2068	33	No	Unset	Unreviewed	
18021	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _60' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2073	5	No	Unset	Unreviewed	
42621	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_60()	2077	39	No	Unset	Unreviewed	
42622	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_60()	2077	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
450	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_60()	2078	116	No	Unset	Unreviewed	
17881	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_61' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2087	5	No	Unset	Unreviewed	
42625	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_61()	2092	49	No	Unset	Unreviewed	
42626	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_61()	2092	49	No	Unset	Unreviewed	
462	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		2099	8	No	Unset	Unreviewed	
42629	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_61()	2106	33	No	Unset	Unreviewed	
42630	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_61()	2106	33	No	Unset	Unreviewed	
17956	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _61' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2111	5	No	Unset	Unreviewed	
42633	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_61()	2115	39	No	Unset	Unreviewed	
42634	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_61()	2115	39	No	Unset	Unreviewed	
451	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_61()	2116	116	No	Unset	Unreviewed	
18036	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	2125	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_62' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42637	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_62()	2130	49	No	Unset	Unreviewed	
42638	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_62()	2130	49	No	Unset	Unreviewed	
426	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_62()	2137	8	No	Unset	Unreviewed	
42641	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_62()	2144	33	No	Unset	Unreviewed	
42642	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_62()	2144	33	No	Unset	Unreviewed	
18120	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _62' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2149	5	No	Unset	Unreviewed	
42645	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_62()	2153	39	No	Unset	Unreviewed	
42646	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_62()	2153	39	No	Unset	Unreviewed	
431	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_62()	2154	116	No	Unset	Unreviewed	
18070	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_63' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2163	5	No	Unset	Unreviewed	
42649	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_63()	2168	49	No	Unset	Unreviewed	
42650	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_63()	2168	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
454	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_63()	2175	8	No	Unset	Unreviewed	
42653	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_63()	2182	33	No	Unset	Unreviewed	
42654	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_63()	2182	33	No	Unset	Unreviewed	
17934	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _63' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2187	5	No	Unset	Unreviewed	
42657	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_63()	2191	39	No	Unset	Unreviewed	
42658	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_63()	2191	39	No	Unset	Unreviewed	
463	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_63()	2192	116	No	Unset	Unreviewed	
18068	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_64' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2201	5	No	Unset	Unreviewed	
42661	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_64()	2206	49	No	Unset	Unreviewed	
42662	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_64()	2206	49	No	Unset	Unreviewed	
456	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		2213	8	No	Unset	Unreviewed	
42665	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_64()	2220	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42666	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_64()	2220	33	No	Unset	Unreviewed	
17994	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _64' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2225	5	No	Unset	Unreviewed	
42669	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_64()	2229	39	No	Unset	Unreviewed	
42670	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_64()	2229	39	No	Unset	Unreviewed	
399	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_64()	2230	116	No	Unset	Unreviewed	
17981	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_65' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2239	5	No	Unset	Unreviewed	
42673	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_65()	2244	49	No	Unset	Unreviewed	
42674	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_65()	2244	49	No	Unset	Unreviewed	
465	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		2251	8	No	Unset	Unreviewed	
42677	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_65()	2258	33	No	Unset	Unreviewed	
42678	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_65()	2258	33	No	Unset	Unreviewed	
17995	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	2263	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_65' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42679	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_65()	2267	39	No	Unset	Unreviewed	
42680	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_65()	2267	39	No	Unset	Unreviewed	
401	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_65()	2268	116	No	Unset	Unreviewed	
17892	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_66' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2277	5	No	Unset	Unreviewed	
42681	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_66()	2282	49	No	Unset	Unreviewed	
42682	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_66()	2282	49	No	Unset	Unreviewed	
460	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_66()	2289	8	No	Unset	Unreviewed	
42683	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_66()	2296	33	No	Unset	Unreviewed	
42684	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_66()	2296	33	No	Unset	Unreviewed	
18108	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _66' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2301	5	No	Unset	Unreviewed	
42685	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_66()	2305	39	No	Unset	Unreviewed	
42686	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_66()	2305	39	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
406	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_66()	2306	116	No	Unset	Unreviewed	
18075	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_67' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2315	5	No	Unset	Unreviewed	
42687	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_67()	2320	49	No	Unset	Unreviewed	
42688	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_67()	2320	49	No	Unset	Unreviewed	
471	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_67()	2327	8	No	Unset	Unreviewed	
42689	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_67()	2334	33	No	Unset	Unreviewed	
42690	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_67()	2334	33	No	Unset	Unreviewed	
18014	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _67' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2339	5	No	Unset	Unreviewed	
42691	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_67()	2343	39	No	Unset	Unreviewed	
42692	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_67()	2343	39	No	Unset	Unreviewed	
457	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_67()	2344	116	No	Unset	Unreviewed	
18044	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE	File Scope	2353	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_68' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).							
42693	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_68()	2358	49	No	Unset	Unreviewed	
42694	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_68()	2358	49	No	Unset	Unreviewed	
476	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_68()	2365	8	No	Unset	Unreviewed	
42695	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_68()	2372	33	No	Unset	Unreviewed	
42696	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_68()	2372	33	No	Unset	Unreviewed	
17839	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _68' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2377	5	No	Unset	Unreviewed	
42697	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_68()	2381	39	No	Unset	Unreviewed	
42698	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_68()	2381	39	No	Unset	Unreviewed	
379	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_68()	2382	116	No	Unset	Unreviewed	
18099	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_69' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2391	5	No	Unset	Unreviewed	
42699	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_69()	2396	49	No	Unset	Unreviewed	
42700	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_69()	2396	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
464	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_69()	2403	8	No	Unset	Unreviewed	
42701	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_69()	2410	33	No	Unset	Unreviewed	
42702	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_69()	2410	33	No	Unset	Unreviewed	
18096	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _69' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2415	5	No	Unset	Unreviewed	
42703	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_69()	2419	39	No	Unset	Unreviewed	
42704	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_69()	2419	39	No	Unset	Unreviewed	
475	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_69()	2420	116	No	Unset	Unreviewed	
18011	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_70' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2429	5	No	Unset	Unreviewed	
42705	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_70()	2434	49	No	Unset	Unreviewed	
42706	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_70()	2434	49	No	Unset	Unreviewed	
417	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_70()	2441	8	No	Unset	Unreviewed	
42707	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_70()	2448	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42708	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_70()	2448	33	No	Unset	Unreviewed	
17877	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _70' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2453	5	No	Unset	Unreviewed	
42709	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_70()	2457	39	No	Unset	Unreviewed	
42710	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_70()	2457	39	No	Unset	Unreviewed	
397	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_70()	2458	116	No	Unset	Unreviewed	
17983	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_71' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2467	5	No	Unset	Unreviewed	
42711	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_71()	2472	49	No	Unset	Unreviewed	
42712	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_71()	2472	49	No	Unset	Unreviewed	
441	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_71()	2479	8	No	Unset	Unreviewed	
42713	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_71()	2486	33	No	Unset	Unreviewed	
42714	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_71()	2486	33	No	Unset	Unreviewed	
17982	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA	File Scope	2491	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_71' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).							
42715	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_71()	2495	39	No	Unset	Unreviewed	
42716	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_71()	2495	39	No	Unset	Unreviewed	
424	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_71()	2496	116	No	Unset	Unreviewed	
17857	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_72' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2505	5	No	Unset	Unreviewed	
42717	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_72()	2510	49	No	Unset	Unreviewed	
42718	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_72()	2510	49	No	Unset	Unreviewed	
404	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_72()	2517	8	No	Unset	Unreviewed	
42719	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_72()	2524	33	No	Unset	Unreviewed	
42720	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_72()	2524	33	No	Unset	Unreviewed	
18015	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _72' conflicts with the external identifier 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2529	5	No	Unset	Unreviewed	
42721	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_72()	2533	39	No	Unset	Unreviewed	
42722	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_72()	2533	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
480	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_72()	2534	116	No	Unset	Unreviewed	
17911	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Gpt_GPT_EXCLUSIVE_ARE A_73' conflicts with the external identifier 'SchM_Enter_G pt_GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 491).	File Scope	2543	5	No	Unset	Unreviewed	
42723	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_73()	2548	49	No	Unset	Unreviewed	
42724	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_73()	2548	49	No	Unset	Unreviewed	
481	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_73()	2555	8	No	Unset	Unreviewed	
42725	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_73()	2562	33	No	Unset	Unreviewed	
42726	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Gpt_GPT_EXC LUSIVE_AREA_73()	2562	33	No	Unset	Unreviewed	
18033	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Gpt_GPT_EXCLUSIVE_AREA _73' conflicts with the external identifier 'SchM_Exit_Gpt_ GPT_EXCLUSIVE_AREA_00' (SchM_Gpt.c line 515).	File Scope	2567	5	No	Unset	Unreviewed	
42727	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_73()	2571	39	No	Unset	Unreviewed	
42728	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_73()	2571	39	No	Unset	Unreviewed	
453	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Gpt_GPT_EXCL USIVE_AREA_73()	2572	116	No	Unset	Unreviewed	

Table 4.48. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_I2c.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
252	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volatile-qualified type	File Scope	95	16	No	Unset	Unreviewed	
256	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type category Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
253	D4.3	Assembly language shall be encapsulated and isolated.	I2c_schm_read_msr()	312	8	No	Unset	Unreviewed	
254	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	I2c_schm_read_msr()	315	19	No	Unset	Unreviewed	
257	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_I2c_I2C_EXCL USIVE_AREA_00()	397	8	No	Unset	Unreviewed	
255	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_I2c_I2C_EXCLU SIVE_AREA_00()	414	116	No	Unset	Unreviewed	
17838	5.1	External identifiers shall be distinct. External function 'SchM_Enter_I2c_I2C_EXCLUSIVE_AREA_01' conflicts with the external identifier 'SchM_Enter_I2c_I2C_EXCLUSIVE_AREA_00' (SchM_I2c.c line 385).	File Scope	423	5	No	Unset	Unreviewed	
258	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		435	8	No	Unset	Unreviewed	
17836	5.1	External identifiers shall be distinct. External function 'SchM_Exit_I2c_I2C_EXCLUSIVE_AREA_0 1' conflicts with the external identifier 'SchM_Exit_I2c_I2C _EXCLUSIVE_AREA_00' (SchM_I2c.c line 409).	File Scope	447	5	No	Unset	Unreviewed	
259	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_I2c_I2C_EXCLU SIVE_AREA_01()	452	116	No	Unset	Unreviewed	

Table 4.49. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcl.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
273	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
279	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volatile-qualified type	File Scope	95	16	No	Unset	Unreviewed	
270	D4.3	Assembly language shall be encapsulated and isolated.	Mcl_schm_read_msr()	402	8	No	Unset	Unreviewed	
348	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Mcl_schm_read_msr()	405	19	No	Unset	Unreviewed	
278	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_00()	487	8	No	Unset	Unreviewed	
364	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_00()	504	116	No	Unset	Unreviewed	
17894	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_01' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	513	5	No	Unset	Unreviewed	
285	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_01()	525	8	No	Unset	Unreviewed	
18087	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _01' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	537	5	No	Unset	Unreviewed	
284	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_01()	542	116	No	Unset	Unreviewed	
17963	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	551	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_02' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
283	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		563	8	No	Unset	Unreviewed	
18037	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _02' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	575	5	No	Unset	Unreviewed	
288	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_02()	580	116	No	Unset	Unreviewed	
18110	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_03' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	589	5	No	Unset	Unreviewed	
41892	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	594	49	No	Unset	Unreviewed	
41893	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	594	49	No	Unset	Unreviewed	
281	14.4		SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	601	8	No	Unset	Unreviewed	
41904	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	608	33	No	Unset	Unreviewed	
41905	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	608	33	No	Unset	Unreviewed	
17926	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _03' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	613	5	No	Unset	Unreviewed	
41912	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_03()	617	39	No	Unset	Unreviewed	
41913	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_03()	617	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
275	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_03()	618	116	No	Unset	Unreviewed	
18102	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_04' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	627	5	No	Unset	Unreviewed	
294	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		639	8	No	Unset	Unreviewed	
17986	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _04' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	651	5	No	Unset	Unreviewed	
326	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_04()	656	116	No	Unset	Unreviewed	
18100	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_05' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	665	5	No	Unset	Unreviewed	
347	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		677	8	No	Unset	Unreviewed	
17992	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _05' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	689	5	No	Unset	Unreviewed	
319	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_05()	694	116	No	Unset	Unreviewed	
17898	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_06' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	703	5	No	Unset	Unreviewed	
292	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_06()	715	8	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
17989	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _06' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	727	5	No	Unset	Unreviewed	
297	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_06()	732	116	No	Unset	Unreviewed	
17965	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_07' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	741	5	No	Unset	Unreviewed	
318	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_07()	753	8	No	Unset	Unreviewed	
17974	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _07' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	765	5	No	Unset	Unreviewed	
290	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_07()	770	116	No	Unset	Unreviewed	
17897	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_08' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	779	5	No	Unset	Unreviewed	
301	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_08()	791	8	No	Unset	Unreviewed	
17920	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _08' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	803	5	No	Unset	Unreviewed	
354	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_08()	808	116	No	Unset	Unreviewed	
17895	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_09' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	817	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
289	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_09()	829	8	No	Unset	Unreviewed	
17919	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _09' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	841	5	No	Unset	Unreviewed	
338	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_09()	846	116	No	Unset	Unreviewed	
17936	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_10' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	855	5	No	Unset	Unreviewed	
42085	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	860	49	No	Unset	Unreviewed	
42086	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	860	49	No	Unset	Unreviewed	
276	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	867	8	No	Unset	Unreviewed	
42093	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	874	33	No	Unset	Unreviewed	
42094	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	874	33	No	Unset	Unreviewed	
17841	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _10' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	879	5	No	Unset	Unreviewed	
42101	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_10()	883	39	No	Unset	Unreviewed	
42102	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_10()	883	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
299	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_10()	884	116	No	Unset	Unreviewed	
17987	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_11' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	893	5	No	Unset	Unreviewed	
277	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_11()	905	8	No	Unset	Unreviewed	
17917	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _11' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	917	5	No	Unset	Unreviewed	
351	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_11()	922	116	No	Unset	Unreviewed	
17970	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_12' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	931	5	No	Unset	Unreviewed	
286	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		943	8	No	Unset	Unreviewed	
17842	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _12' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	955	5	No	Unset	Unreviewed	
309	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_12()	960	116	No	Unset	Unreviewed	
18098	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_13' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	969	5	No	Unset	Unreviewed	
42135	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	974	49	No	Unset	Unreviewed	
42136	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	974	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
342	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	981	8	No	Unset	Unreviewed	
42141	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	988	33	No	Unset	Unreviewed	
42142	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	988	33	No	Unset	Unreviewed	
17973	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _13' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	993	5	No	Unset	Unreviewed	
42157	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_13()	997	39	No	Unset	Unreviewed	
42158	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_13()	997	39	No	Unset	Unreviewed	
293	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_13()	998	116	No	Unset	Unreviewed	
17890	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_14' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1007	5	No	Unset	Unreviewed	
42163	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1012	49	No	Unset	Unreviewed	
42164	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1012	49	No	Unset	Unreviewed	
323	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1019	8	No	Unset	Unreviewed	
42169	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1026	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42170	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1026	33	No	Unset	Unreviewed	
17909	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _14' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1031	5	No	Unset	Unreviewed	
42175	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_14()	1035	39	No	Unset	Unreviewed	
42176	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_14()	1035	39	No	Unset	Unreviewed	
315	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_14()	1036	116	No	Unset	Unreviewed	
17859	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_15' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1045	5	No	Unset	Unreviewed	
42183	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1050	49	No	Unset	Unreviewed	
42184	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1050	49	No	Unset	Unreviewed	
300	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1057	8	No	Unset	Unreviewed	
42189	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1064	33	No	Unset	Unreviewed	
42190	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1064	33	No	Unset	Unreviewed	
17893	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	1069	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_15' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42197	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_15()	1073	39	No	Unset	Unreviewed	
42198	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_15()	1073	39	No	Unset	Unreviewed	
302	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_15()	1074	116	No	Unset	Unreviewed	
17848	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_16' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1083	5	No	Unset	Unreviewed	
42205	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1088	49	No	Unset	Unreviewed	
42206	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1088	49	No	Unset	Unreviewed	
303	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1095	8	No	Unset	Unreviewed	
42211	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1102	33	No	Unset	Unreviewed	
42212	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1102	33	No	Unset	Unreviewed	
17906	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _16' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1107	5	No	Unset	Unreviewed	
42217	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_16()	1111	39	No	Unset	Unreviewed	
42218	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_16()	1111	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
357	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_16()	1112	116	No	Unset	Unreviewed	
18041	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_17' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1121	5	No	Unset	Unreviewed	
42223	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1126	49	No	Unset	Unreviewed	
42224	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1126	49	No	Unset	Unreviewed	
340	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1133	8	No	Unset	Unreviewed	
42229	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1140	33	No	Unset	Unreviewed	
42230	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1140	33	No	Unset	Unreviewed	
18009	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _17' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1145	5	No	Unset	Unreviewed	
42235	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_17()	1149	39	No	Unset	Unreviewed	
42236	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_17()	1149	39	No	Unset	Unreviewed	
313	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_17()	1150	116	No	Unset	Unreviewed	
17988	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	1159	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_18' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
42241	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1164	49	No	Unset	Unreviewed	
42242	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1164	49	No	Unset	Unreviewed	
305	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1171	8	No	Unset	Unreviewed	
42247	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1178	33	No	Unset	Unreviewed	
42248	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1178	33	No	Unset	Unreviewed	
17930	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _18' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1183	5	No	Unset	Unreviewed	
42253	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_18()	1187	39	No	Unset	Unreviewed	
42254	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_18()	1187	39	No	Unset	Unreviewed	
291	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_18()	1188	116	No	Unset	Unreviewed	
18107	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_19' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1197	5	No	Unset	Unreviewed	
42259	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1202	49	No	Unset	Unreviewed	
42260	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1202	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
287	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1209	8	No	Unset	Unreviewed	
42265	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1216	33	No	Unset	Unreviewed	
42266	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1216	33	No	Unset	Unreviewed	
18001	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _19' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1221	5	No	Unset	Unreviewed	
42271	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_19()	1225	39	No	Unset	Unreviewed	
42272	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_19()	1225	39	No	Unset	Unreviewed	
366	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_19()	1226	116	No	Unset	Unreviewed	
18086	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_20' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1235	5	No	Unset	Unreviewed	
42277	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1240	49	No	Unset	Unreviewed	
42278	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1240	49	No	Unset	Unreviewed	
349	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1247	8	No	Unset	Unreviewed	
42283	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1254	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42284	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1254	33	No	Unset	Unreviewed	
17913	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _20' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1259	5	No	Unset	Unreviewed	
42289	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_20()	1263	39	No	Unset	Unreviewed	
42290	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_20()	1263	39	No	Unset	Unreviewed	
307	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_20()	1264	116	No	Unset	Unreviewed	
17902	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_21' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1273	5	No	Unset	Unreviewed	
42295	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1278	49	No	Unset	Unreviewed	
42296	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1278	49	No	Unset	Unreviewed	
282	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1285	8	No	Unset	Unreviewed	
42301	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1292	33	No	Unset	Unreviewed	
42302	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1292	33	No	Unset	Unreviewed	
17959	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	1297	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_21' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42307	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_21()	1301	39	No	Unset	Unreviewed	
42308	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_21()	1301	39	No	Unset	Unreviewed	
321	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_21()	1302	116	No	Unset	Unreviewed	
18027	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_22' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1311	5	No	Unset	Unreviewed	
42313	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1316	49	No	Unset	Unreviewed	
42314	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1316	49	No	Unset	Unreviewed	
310	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1323	8	No	Unset	Unreviewed	
42319	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1330	33	No	Unset	Unreviewed	
42320	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1330	33	No	Unset	Unreviewed	
18093	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _22' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1335	5	No	Unset	Unreviewed	
42325	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_22()	1339	39	No	Unset	Unreviewed	
42326	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_22()	1339	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
312	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_22()	1340	116	No	Unset	Unreviewed	
17997	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_23' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1349	5	No	Unset	Unreviewed	
42331	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1354	49	No	Unset	Unreviewed	
42332	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1354	49	No	Unset	Unreviewed	
272	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1361	8	No	Unset	Unreviewed	
42337	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1368	33	No	Unset	Unreviewed	
42338	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1368	33	No	Unset	Unreviewed	
18079	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _23' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1373	5	No	Unset	Unreviewed	
42343	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_23()	1377	39	No	Unset	Unreviewed	
42344	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_23()	1377	39	No	Unset	Unreviewed	
317	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_23()	1378	116	No	Unset	Unreviewed	
18112	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	1387	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_24' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
42349	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1392	49	No	Unset	Unreviewed	
42350	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1392	49	No	Unset	Unreviewed	
316	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1399	8	No	Unset	Unreviewed	
42355	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1406	33	No	Unset	Unreviewed	
42356	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1406	33	No	Unset	Unreviewed	
17882	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _24' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1411	5	No	Unset	Unreviewed	
42365	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_24()	1415	39	No	Unset	Unreviewed	
42366	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_24()	1415	39	No	Unset	Unreviewed	
359	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_24()	1416	116	No	Unset	Unreviewed	
17870	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_25' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1425	5	No	Unset	Unreviewed	
42371	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1430	49	No	Unset	Unreviewed	
42372	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1430	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
325	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1437	8	No	Unset	Unreviewed	
42381	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1444	33	No	Unset	Unreviewed	
42382	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1444	33	No	Unset	Unreviewed	
17928	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _25' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1449	5	No	Unset	Unreviewed	
42387	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_25()	1453	39	No	Unset	Unreviewed	
42388	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_25()	1453	39	No	Unset	Unreviewed	
280	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_25()	1454	116	No	Unset	Unreviewed	
18062	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_26' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1463	5	No	Unset	Unreviewed	
42397	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1468	49	No	Unset	Unreviewed	
42398	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1468	49	No	Unset	Unreviewed	
361	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1475	8	No	Unset	Unreviewed	
42407	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1482	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42408	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1482	33	No	Unset	Unreviewed	
17914	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _26' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1487	5	No	Unset	Unreviewed	
42413	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_26()	1491	39	No	Unset	Unreviewed	
42414	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_26()	1491	39	No	Unset	Unreviewed	
274	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_26()	1492	116	No	Unset	Unreviewed	
17899	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_27' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1501	5	No	Unset	Unreviewed	
42425	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1506	49	No	Unset	Unreviewed	
42426	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1506	49	No	Unset	Unreviewed	
320	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1513	8	No	Unset	Unreviewed	
42429	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1520	33	No	Unset	Unreviewed	
42430	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1520	33	No	Unset	Unreviewed	
17901	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	1525	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_27' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42437	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_27()	1529	39	No	Unset	Unreviewed	
42438	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_27()	1529	39	No	Unset	Unreviewed	
324	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_27()	1530	116	No	Unset	Unreviewed	
18106	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_28' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1539	5	No	Unset	Unreviewed	
42445	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1544	49	No	Unset	Unreviewed	
42446	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1544	49	No	Unset	Unreviewed	
311	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1551	8	No	Unset	Unreviewed	
42449	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1558	33	No	Unset	Unreviewed	
42450	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1558	33	No	Unset	Unreviewed	
18077	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _28' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1563	5	No	Unset	Unreviewed	
42457	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_28()	1567	39	No	Unset	Unreviewed	
42458	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_28()	1567	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
343	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_28()	1568	116	No	Unset	Unreviewed	
17910	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_29' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1577	5	No	Unset	Unreviewed	
42461	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1582	49	No	Unset	Unreviewed	
42462	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1582	49	No	Unset	Unreviewed	
308	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1589	8	No	Unset	Unreviewed	
42465	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1596	33	No	Unset	Unreviewed	
42466	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1596	33	No	Unset	Unreviewed	
17856	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _29' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1601	5	No	Unset	Unreviewed	
42469	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_29()	1605	39	No	Unset	Unreviewed	
42470	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_29()	1605	39	No	Unset	Unreviewed	
322	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_29()	1606	116	No	Unset	Unreviewed	
17884	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	1615	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_30' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
42473	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1620	49	No	Unset	Unreviewed	
42474	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1620	49	No	Unset	Unreviewed	
271	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1627	8	No	Unset	Unreviewed	
42477	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1634	33	No	Unset	Unreviewed	
42478	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1634	33	No	Unset	Unreviewed	
17851	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _30' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1639	5	No	Unset	Unreviewed	
42481	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_30()	1643	39	No	Unset	Unreviewed	
42482	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_30()	1643	39	No	Unset	Unreviewed	
363	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_30()	1644	116	No	Unset	Unreviewed	
17880	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_31' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1653	5	No	Unset	Unreviewed	
42485	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1658	49	No	Unset	Unreviewed	
42486	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1658	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
314	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1665	8	No	Unset	Unreviewed	
42489	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1672	33	No	Unset	Unreviewed	
42490	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1672	33	No	Unset	Unreviewed	
17916	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _31' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1677	5	No	Unset	Unreviewed	
42493	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_31()	1681	39	No	Unset	Unreviewed	
42494	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_31()	1681	39	No	Unset	Unreviewed	
345	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_31()	1682	116	No	Unset	Unreviewed	
17824	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_32' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1691	5	No	Unset	Unreviewed	
42497	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1696	49	No	Unset	Unreviewed	
42498	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1696	49	No	Unset	Unreviewed	
329	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1703	8	No	Unset	Unreviewed	
42503	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1710	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42504	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1710	33	No	Unset	Unreviewed	
18020	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _32' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1715	5	No	Unset	Unreviewed	
42507	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_32()	1719	39	No	Unset	Unreviewed	
42508	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_32()	1719	39	No	Unset	Unreviewed	
330	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_32()	1720	116	No	Unset	Unreviewed	
18007	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_33' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1729	5	No	Unset	Unreviewed	
42511	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1734	49	No	Unset	Unreviewed	
42512	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1734	49	No	Unset	Unreviewed	
298	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1741	8	No	Unset	Unreviewed	
42515	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1748	33	No	Unset	Unreviewed	
42516	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1748	33	No	Unset	Unreviewed	
17905	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	1753	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_33' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42519	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_33()	1757	39	No	Unset	Unreviewed	
42520	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_33()	1757	39	No	Unset	Unreviewed	
362	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_33()	1758	116	No	Unset	Unreviewed	
17862	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_34' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1767	5	No	Unset	Unreviewed	
42523	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1772	49	No	Unset	Unreviewed	
42524	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1772	49	No	Unset	Unreviewed	
332	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1779	8	No	Unset	Unreviewed	
42527	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1786	33	No	Unset	Unreviewed	
42528	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1786	33	No	Unset	Unreviewed	
18049	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _34' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1791	5	No	Unset	Unreviewed	
42531	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_34()	1795	39	No	Unset	Unreviewed	
42532	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_34()	1795	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
295	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_34()	1796	116	No	Unset	Unreviewed	
18124	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_35' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1805	5	No	Unset	Unreviewed	
42535	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1810	49	No	Unset	Unreviewed	
42536	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1810	49	No	Unset	Unreviewed	
335	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1817	8	No	Unset	Unreviewed	
42539	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1824	33	No	Unset	Unreviewed	
42540	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1824	33	No	Unset	Unreviewed	
17940	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _35' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1829	5	No	Unset	Unreviewed	
42543	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_35()	1833	39	No	Unset	Unreviewed	
42544	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_35()	1833	39	No	Unset	Unreviewed	
337	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_35()	1834	116	No	Unset	Unreviewed	
17863	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	1843	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_36' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
42547	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1848	49	No	Unset	Unreviewed	
42548	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1848	49	No	Unset	Unreviewed	
352	14.4		SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1855	8	No	Unset	Unreviewed	
42551	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1862	33	No	Unset	Unreviewed	
42552	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1862	33	No	Unset	Unreviewed	
17908	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _36' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1867	5	No	Unset	Unreviewed	
42555	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_36()	1871	39	No	Unset	Unreviewed	
42556	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_36()	1871	39	No	Unset	Unreviewed	
333	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_36()	1872	116	No	Unset	Unreviewed	
18064	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_37' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1881	5	No	Unset	Unreviewed	
42559	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1886	49	No	Unset	Unreviewed	
42560	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1886	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
341	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1893	8	No	Unset	Unreviewed	
42563	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1900	33	No	Unset	Unreviewed	
42564	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1900	33	No	Unset	Unreviewed	
17977	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _37' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1905	5	No	Unset	Unreviewed	
42567	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_37()	1909	39	No	Unset	Unreviewed	
42568	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_37()	1909	39	No	Unset	Unreviewed	
355	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_37()	1910	116	No	Unset	Unreviewed	
18073	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_38' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1919	5	No	Unset	Unreviewed	
42571	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1924	49	No	Unset	Unreviewed	
42572	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1924	49	No	Unset	Unreviewed	
304	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1931	8	No	Unset	Unreviewed	
42575	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1938	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42576	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1938	33	No	Unset	Unreviewed	
18104	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _38' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	1943	5	No	Unset	Unreviewed	
42579	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_38()	1947	39	No	Unset	Unreviewed	
42580	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_38()	1947	39	No	Unset	Unreviewed	
346	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_38()	1948	116	No	Unset	Unreviewed	
18047	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_39' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1957	5	No	Unset	Unreviewed	
42583	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1962	49	No	Unset	Unreviewed	
42584	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1962	49	No	Unset	Unreviewed	
306	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1969	8	No	Unset	Unreviewed	
42587	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1976	33	No	Unset	Unreviewed	
42588	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1976	33	No	Unset	Unreviewed	
18005	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	1981	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_39' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42591	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_39()	1985	39	No	Unset	Unreviewed	
42592	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_39()	1985	39	No	Unset	Unreviewed	
327	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_39()	1986	116	No	Unset	Unreviewed	
17912	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_40' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	1995	5	No	Unset	Unreviewed	
42595	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2000	49	No	Unset	Unreviewed	
42596	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2000	49	No	Unset	Unreviewed	
336	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		2007	8	No	Unset	Unreviewed	
42601	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2014	33	No	Unset	Unreviewed	
42602	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2014	33	No	Unset	Unreviewed	
17961	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _40' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2019	5	No	Unset	Unreviewed	
334	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_40()	2024	116	No	Unset	Unreviewed	
17952	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE	File Scope	2033	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_41' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).							
42607	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2038	49	No	Unset	Unreviewed	
42608	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2038	49	No	Unset	Unreviewed	
339	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2045	8	No	Unset	Unreviewed	
42611	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2052	33	No	Unset	Unreviewed	
42612	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2052	33	No	Unset	Unreviewed	
17864	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _41' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2057	5	No	Unset	Unreviewed	
42615	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_41()	2061	39	No	Unset	Unreviewed	
42616	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_41()	2061	39	No	Unset	Unreviewed	
350	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_41()	2062	116	No	Unset	Unreviewed	
17918	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_42' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	2071	5	No	Unset	Unreviewed	
42619	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2076	49	No	Unset	Unreviewed	
42620	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2076	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
353	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2083	8	No	Unset	Unreviewed	
42623	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2090	33	No	Unset	Unreviewed	
42624	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2090	33	No	Unset	Unreviewed	
18034	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _42' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2095	5	No	Unset	Unreviewed	
42627	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_42()	2099	39	No	Unset	Unreviewed	
42628	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_42()	2099	39	No	Unset	Unreviewed	
328	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_42()	2100	116	No	Unset	Unreviewed	
18076	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_43' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	2109	5	No	Unset	Unreviewed	
42631	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2114	49	No	Unset	Unreviewed	
42632	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2114	49	No	Unset	Unreviewed	
331	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2121	8	No	Unset	Unreviewed	
42635	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2128	33	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42636	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2128	33	No	Unset	Unreviewed	
17939	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _43' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2133	5	No	Unset	Unreviewed	
42639	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_43()	2137	39	No	Unset	Unreviewed	
42640	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_43()	2137	39	No	Unset	Unreviewed	
358	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_43()	2138	116	No	Unset	Unreviewed	
17846	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_44' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	2147	5	No	Unset	Unreviewed	
42643	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2152	49	No	Unset	Unreviewed	
42644	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2152	49	No	Unset	Unreviewed	
360	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2159	8	No	Unset	Unreviewed	
42647	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2166	33	No	Unset	Unreviewed	
42648	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2166	33	No	Unset	Unreviewed	
17923	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA	File Scope	2171	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		_44' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).							
42651	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_44()	2175	39	No	Unset	Unreviewed	
42652	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_44()	2175	39	No	Unset	Unreviewed	
296	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_44()	2176	116	No	Unset	Unreviewed	
17826	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_45' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	2185	5	No	Unset	Unreviewed	
42655	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2190	49	No	Unset	Unreviewed	
42656	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2190	49	No	Unset	Unreviewed	
344	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2197	8	No	Unset	Unreviewed	
42659	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2204	33	No	Unset	Unreviewed	
42660	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2204	33	No	Unset	Unreviewed	
17945	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _45' conflicts with the external identifier 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2209	5	No	Unset	Unreviewed	
42663	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_45()	2213	39	No	Unset	Unreviewed	
42664	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_45()	2213	39	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
365	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_45()	2214	116	No	Unset	Unreviewed	
17876	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcl_MCL_EXCLUSIVE_ARE A_46' conflicts with the external identifier 'SchM_Enter_M cl_MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 475).	File Scope	2223	5	No	Unset	Unreviewed	
42667	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2228	49	No	Unset	Unreviewed	
42668	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2228	49	No	Unset	Unreviewed	
367	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2235	8	No	Unset	Unreviewed	
42671	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2242	33	No	Unset	Unreviewed	
42672	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2242	33	No	Unset	Unreviewed	
17903	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcl_MCL_EXCLUSIVE_AREA _46' conflicts with the external identifier 'SchM_Exit_Mcl_ MCL_EXCLUSIVE_AREA_00' (SchM_Mcl.c line 499).	File Scope	2247	5	No	Unset	Unreviewed	
42675	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_46()	2251	39	No	Unset	Unreviewed	
42676	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_46()	2251	39	No	Unset	Unreviewed	
356	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_46()	2252	116	No	Unset	Unreviewed	

Table 4.50. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcu.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
262	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
265	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volatile-qualified type	File Scope	95	16	No	Unset	Unreviewed	
260	D4.3	Assembly language shall be encapsulated and isolated.	Mcu_schm_read_msr()	314	8	No	Unset	Unreviewed	
261	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Mcu_schm_read_msr()	317	19	No	Unset	Unreviewed	
41733	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	392	49	No	Unset	Unreviewed	
41734	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	392	49	No	Unset	Unreviewed	
264	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	399	8	No	Unset	Unreviewed	
41745	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	406	33	No	Unset	Unreviewed	
41746	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	406	33	No	Unset	Unreviewed	
268	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_00()	416	116	No	Unset	Unreviewed	
17980	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcu_MCU_EXCLUSIVE_AR EA_01' conflicts with the external identifier 'SchM_Enter_ Mcu_MCU_EXCLUSIVE_AREA_00' (SchM_Mcu.c line 387).	File Scope	425	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41761	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	430	49	No	Unset	Unreviewed	
41762	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	430	49	No	Unset	Unreviewed	
267	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	437	8	No	Unset	Unreviewed	
41773	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	444	33	No	Unset	Unreviewed	
41774	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	444	33	No	Unset	Unreviewed	
18046	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcu_MCU_EXCLUSIVE_ARE A_01' conflicts with the external identifier 'SchM_Exit_Mc u_MCU_EXCLUSIVE_AREA_00' (SchM_Mcu.c line 411).	File Scope	449	5	No	Unset	Unreviewed	
41783	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_01()	453	39	No	Unset	Unreviewed	
41784	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_01()	453	39	No	Unset	Unreviewed	
269	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_01()	454	116	No	Unset	Unreviewed	
17929	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Mcu_MCU_EXCLUSIVE_AR EA_02' conflicts with the external identifier 'SchM_Enter_ Mcu_MCU_EXCLUSIVE_AREA_00' (SchM_Mcu.c line 387).	File Scope	463	5	No	Unset	Unreviewed	
41787	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	468	49	No	Unset	Unreviewed	
41788	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	468	49	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
263	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	475	8	No	Unset	Unreviewed	
41801	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	482	33	No	Unset	Unreviewed	
41802	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	482	33	No	Unset	Unreviewed	
17933	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Mcu_MCU_EXCLUSIVE_ARE A_02' conflicts with the external identifier 'SchM_Exit_Mc u_MCU_EXCLUSIVE_AREA_00' (SchM_Mcu.c line 411).	File Scope	487	5	No	Unset	Unreviewed	
41806	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_02()	491	39	No	Unset	Unreviewed	
41807	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_02()	491	39	No	Unset	Unreviewed	
266	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Mcu_MCU_EXC LUSIVE_AREA_02()	492	116	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
487	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evalu ation because of multiple read accesses to objects of volati le-qualified type	•	95	16	No	Unset	Unreviewed	
494	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
527	D4.3	Assembly language shall be encapsulated and isolated.	Port_schm_read_msr()	364	8	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
539	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Port_schm_read_msr()	367	19	No	Unset	Unreviewed	
486	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_00()	449	8	No	Unset	Unreviewed	
503	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_00()	466	117	No	Unset	Unreviewed	
18078	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_01' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	475	5	No	Unset	Unreviewed	
41799	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_01()	480	50	No	Unset	Unreviewed	
41800	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_01()	480	50	No	Unset	Unreviewed	
484	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_01()	487	8	No	Unset	Unreviewed	
41812	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_01()	494	34	No	Unset	Unreviewed	
41813	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_01()	494	34	No	Unset	Unreviewed	
18097	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_01' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	499	5	No	Unset	Unreviewed	
41822	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_01()	503	40	No	Unset	Unreviewed	
41823	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_01()	503	40	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
529	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_01()	504	117	No	Unset	Unreviewed	
18126	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_02' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	513	5	No	Unset	Unreviewed	
41840	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_02()	518	50	No	Unset	Unreviewed	
41841	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_02()	518	50	No	Unset	Unreviewed	
507	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		525	8	No	Unset	Unreviewed	
41854	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_02()	532	34	No	Unset	Unreviewed	
41855	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_02()	532	34	No	Unset	Unreviewed	
17984	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_02' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	537	5	No	Unset	Unreviewed	
490	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_02()	542	117	No	Unset	Unreviewed	
18042	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_03' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	551	5	No	Unset	Unreviewed	
41872	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_03()	556	50	No	Unset	Unreviewed	
41873	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_03()	556	50	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
518	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_03()	563	8	No	Unset	Unreviewed	
41882	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_03()	570	34	No	Unset	Unreviewed	
41883	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_03()	570	34	No	Unset	Unreviewed	
18116	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_03' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	575	5	No	Unset	Unreviewed	
496	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_03()	580	117	No	Unset	Unreviewed	
18111	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_04' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	589	5	No	Unset	Unreviewed	
41894	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_04()	594	50	No	Unset	Unreviewed	
41895	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_04()	594	50	No	Unset	Unreviewed	
489	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_04()	601	8	No	Unset	Unreviewed	
41906	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_04()	608	34	No	Unset	Unreviewed	
41907	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_04()	608	34	No	Unset	Unreviewed	
18089	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_04' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	613	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41914	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_04()	617	40	No	Unset	Unreviewed	
41915	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_04()	617	40	No	Unset	Unreviewed	
523	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_04()	618	117	No	Unset	Unreviewed	
18050	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_05' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	627	5	No	Unset	Unreviewed	
482	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		639	8	No	Unset	Unreviewed	
18088	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_05' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	651	5	No	Unset	Unreviewed	
483	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_05()	656	117	No	Unset	Unreviewed	
18067	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_06' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	665	5	No	Unset	Unreviewed	
492	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_06()	677	8	No	Unset	Unreviewed	
18095	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_06' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	689	5	No	Unset	Unreviewed	
497	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_06()	694	117	No	Unset	Unreviewed	
18031	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR	File Scope	703	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		EA_07' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).							
501	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_07()	715	8	No	Unset	Unreviewed	
17935	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_07' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	727	5	No	Unset	Unreviewed	
513	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_07()	732	117	No	Unset	Unreviewed	
18118	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_08' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	741	5	No	Unset	Unreviewed	
42003	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_08()	746	50	No	Unset	Unreviewed	
42004	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_08()	746	50	No	Unset	Unreviewed	
488	14.4		SchM_Enter_Port_PORT_E XCLUSIVE_AREA_08()	753	8	No	Unset	Unreviewed	
42015	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_08()	760	34	No	Unset	Unreviewed	
42016	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_08()	760	34	No	Unset	Unreviewed	
18094	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_08' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	765	5	No	Unset	Unreviewed	
42021	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_08()	769	40	No	Unset	Unreviewed	
42022	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_08()	769	40	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
502	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_08()	770	117	No	Unset	Unreviewed	
18045	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_09' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	779	5	No	Unset	Unreviewed	
42033	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_09()	784	50	No	Unset	Unreviewed	
42034	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_09()	784	50	No	Unset	Unreviewed	
500	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_09()	791	8	No	Unset	Unreviewed	
42045	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_09()	798	34	No	Unset	Unreviewed	
42046	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_09()	798	34	No	Unset	Unreviewed	
17924	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_09' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	803	5	No	Unset	Unreviewed	
42051	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_09()	807	40	No	Unset	Unreviewed	
42052	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_09()	807	40	No	Unset	Unreviewed	
491	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_09()	808	117	No	Unset	Unreviewed	
18056	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR	File Scope	817	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		EA_10' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).							
42057	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_10()	822	50	No	Unset	Unreviewed	
42058	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_10()	822	50	No	Unset	Unreviewed	
515	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_10()	829	8	No	Unset	Unreviewed	
42063	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_10()	836	34	No	Unset	Unreviewed	
42064	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_10()	836	34	No	Unset	Unreviewed	
17985	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_10' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	841	5	No	Unset	Unreviewed	
42071	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_10()	845	40	No	Unset	Unreviewed	
42072	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_10()	845	40	No	Unset	Unreviewed	
530	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_10()	846	117	No	Unset	Unreviewed	
17883	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_11' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	855	5	No	Unset	Unreviewed	
42087	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_11()	860	50	No	Unset	Unreviewed	
42088	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_11()	860	50	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
505	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_11()	867	8	No	Unset	Unreviewed	
42095	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_11()	874	34	No	Unset	Unreviewed	
42096	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_11()	874	34	No	Unset	Unreviewed	
18103	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_11' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	879	5	No	Unset	Unreviewed	
42103	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_11()	883	40	No	Unset	Unreviewed	
42104	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_11()	883	40	No	Unset	Unreviewed	
493	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_11()	884	117	No	Unset	Unreviewed	
18022	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_12' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	893	5	No	Unset	Unreviewed	
42109	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_12()	898	50	No	Unset	Unreviewed	
42110	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_12()	898	50	No	Unset	Unreviewed	
525	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_12()	905	8	No	Unset	Unreviewed	
42115	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_12()	912	34	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42116	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_12()	912	34	No	Unset	Unreviewed	
17885	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_12' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	917	5	No	Unset	Unreviewed	
42119	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_12()	921	40	No	Unset	Unreviewed	
42120	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_12()	921	40	No	Unset	Unreviewed	
540	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_12()	922	117	No	Unset	Unreviewed	
18030	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_13' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	931	5	No	Unset	Unreviewed	
42123	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_13()	936	50	No	Unset	Unreviewed	
42124	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_13()	936	50	No	Unset	Unreviewed	
504	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		943	8	No	Unset	Unreviewed	
42127	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_13()	950	34	No	Unset	Unreviewed	
42128	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_13()	950	34	No	Unset	Unreviewed	
18066	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE	File Scope	955	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_13' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).							
42131	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_13()	959	40	No	Unset	Unreviewed	
42132	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_13()	959	40	No	Unset	Unreviewed	
499	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_13()	960	117	No	Unset	Unreviewed	
18038	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_14' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	969	5	No	Unset	Unreviewed	
42137	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_14()	974	50	No	Unset	Unreviewed	
42138	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_14()	974	50	No	Unset	Unreviewed	
522	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_14()	981	8	No	Unset	Unreviewed	
42143	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_14()	988	34	No	Unset	Unreviewed	
42144	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_14()	988	34	No	Unset	Unreviewed	
17858	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_14' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	993	5	No	Unset	Unreviewed	
42159	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_14()	997	40	No	Unset	Unreviewed	
42160	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_14()	997	40	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
498	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_14()	998	117	No	Unset	Unreviewed	
18051	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_15' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1007	5	No	Unset	Unreviewed	
42165	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_15()	1012	50	No	Unset	Unreviewed	
42166	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_15()	1012	50	No	Unset	Unreviewed	
512	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_15()	1019	8	No	Unset	Unreviewed	
42171	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_15()	1026	34	No	Unset	Unreviewed	
42172	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_15()	1026	34	No	Unset	Unreviewed	
18048	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_15' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1031	5	No	Unset	Unreviewed	
42177	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_15()	1035	40	No	Unset	Unreviewed	
42178	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_15()	1035	40	No	Unset	Unreviewed	
516	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_15()	1036	117	No	Unset	Unreviewed	
18035	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR	File Scope	1045	5	No	Unset	Unreviewed	

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
		EA_16' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).							
42185	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_16()	1050	50	No	Unset	Unreviewed	
42186	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_16()	1050	50	No	Unset	Unreviewed	
520	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_16()	1057	8	No	Unset	Unreviewed	
42191	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_16()	1064	34	No	Unset	Unreviewed	
42192	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_16()	1064	34	No	Unset	Unreviewed	
18092	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_16' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1069	5	No	Unset	Unreviewed	
42199	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_16()	1073	40	No	Unset	Unreviewed	
42200	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_16()	1073	40	No	Unset	Unreviewed	
509	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_16()	1074	117	No	Unset	Unreviewed	
17900	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_17' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1083	5	No	Unset	Unreviewed	
42207	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_17()	1088	50	No	Unset	Unreviewed	
42208	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_17()	1088	50	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
508	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_17()	1095	8	No	Unset	Unreviewed	
42213	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_17()	1102	34	No	Unset	Unreviewed	
42214	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_17()	1102	34	No	Unset	Unreviewed	
18074	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_17' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1107	5	No	Unset	Unreviewed	
42219	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_17()	1111	40	No	Unset	Unreviewed	
42220	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_17()	1111	40	No	Unset	Unreviewed	
526	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_17()	1112	117	No	Unset	Unreviewed	
17942	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_18' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1121	5	No	Unset	Unreviewed	
42225	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_18()	1126	50	No	Unset	Unreviewed	
42226	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_18()	1126	50	No	Unset	Unreviewed	
510	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_18()	1133	8	No	Unset	Unreviewed	
42231	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_18()	1140	34	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42232	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_18()	1140	34	No	Unset	Unreviewed	
18063	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_18' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1145	5	No	Unset	Unreviewed	
42237	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_18()	1149	40	No	Unset	Unreviewed	
42238	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_18()	1149	40	No	Unset	Unreviewed	
514	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_18()	1150	117	No	Unset	Unreviewed	
17879	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_19' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1159	5	No	Unset	Unreviewed	
42243	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_19()	1164	50	No	Unset	Unreviewed	
42244	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_19()	1164	50	No	Unset	Unreviewed	
532	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		1171	8	No	Unset	Unreviewed	
42249	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_19()	1178	34	No	Unset	Unreviewed	
42250	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_19()	1178	34	No	Unset	Unreviewed	
18052	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE	File Scope	1183	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_19' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).							
42255	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_19()	1187	40	No	Unset	Unreviewed	
42256	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_19()	1187	40	No	Unset	Unreviewed	
519	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_19()	1188	117	No	Unset	Unreviewed	
17915	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_20' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1197	5	No	Unset	Unreviewed	
42261	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_20()	1202	50	No	Unset	Unreviewed	
42262	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_20()	1202	50	No	Unset	Unreviewed	
521	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_20()	1209	8	No	Unset	Unreviewed	
42267	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_20()	1216	34	No	Unset	Unreviewed	
42268	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_20()	1216	34	No	Unset	Unreviewed	
17954	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_20' conflicts with the external identifier 'SchM_Exit_Por t_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1221	5	No	Unset	Unreviewed	
42273	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_20()	1225	40	No	Unset	Unreviewed	
42274	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_20()	1225	40	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
511	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_20()	1226	117	No	Unset	Unreviewed	
18032	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_21' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1235	5	No	Unset	Unreviewed	
42279	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_21()	1240	50	No	Unset	Unreviewed	
42280	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_21()	1240	50	No	Unset	Unreviewed	
538	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_21()	1247	8	No	Unset	Unreviewed	
42285	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_21()	1254	34	No	Unset	Unreviewed	
42286	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_21()	1254	34	No	Unset	Unreviewed	
18053	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_21' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1259	5	No	Unset	Unreviewed	
42291	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_21()	1263	40	No	Unset	Unreviewed	
42292	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_21()	1263	40	No	Unset	Unreviewed	
541	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_21()	1264	117	No	Unset	Unreviewed	
18058	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR	File Scope	1273	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		EA_22' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).							
42297	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_22()	1278	50	No	Unset	Unreviewed	
42298	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_22()	1278	50	No	Unset	Unreviewed	
528	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_22()	1285	8	No	Unset	Unreviewed	
42303	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_22()	1292	34	No	Unset	Unreviewed	
42304	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_22()	1292	34	No	Unset	Unreviewed	
18024	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_22' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1297	5	No	Unset	Unreviewed	
42309	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_22()	1301	40	No	Unset	Unreviewed	
42310	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_22()	1301	40	No	Unset	Unreviewed	
535	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_22()	1302	117	No	Unset	Unreviewed	
18115	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_23' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1311	5	No	Unset	Unreviewed	
42315	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_23()	1316	50	No	Unset	Unreviewed	
42316	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_23()	1316	50	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
485	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_23()	1323	8	No	Unset	Unreviewed	
42321	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_23()	1330	34	No	Unset	Unreviewed	
42322	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_23()	1330	34	No	Unset	Unreviewed	
18122	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_23' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1335	5	No	Unset	Unreviewed	
42327	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_23()	1339	40	No	Unset	Unreviewed	
42328	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_23()	1339	40	No	Unset	Unreviewed	
506	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_23()	1340	117	No	Unset	Unreviewed	
17834	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_24' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1349	5	No	Unset	Unreviewed	
42333	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_24()	1354	50	No	Unset	Unreviewed	
42334	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_24()	1354	50	No	Unset	Unreviewed	
531	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_24()	1361	8	No	Unset	Unreviewed	
42339	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_24()	1368	34	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42340	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_24()	1368	34	No	Unset	Unreviewed	
18084	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_24' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1373	5	No	Unset	Unreviewed	
42345	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_24()	1377	40	No	Unset	Unreviewed	
42346	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_24()	1377	40	No	Unset	Unreviewed	
517	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_24()	1378	117	No	Unset	Unreviewed	
18029	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_25' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1387	5	No	Unset	Unreviewed	
42351	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_25()	1392	50	No	Unset	Unreviewed	
42352	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_25()	1392	50	No	Unset	Unreviewed	
495	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_25()	1399	8	No	Unset	Unreviewed	
42357	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_25()	1406	34	No	Unset	Unreviewed	
42358	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_25()	1406	34	No	Unset	Unreviewed	
17979	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE	File Scope	1411	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_25' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).							
42367	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_25()	1415	40	No	Unset	Unreviewed	
42368	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_25()	1415	40	No	Unset	Unreviewed	
533	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_25()	1416	117	No	Unset	Unreviewed	
17830	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_26' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1425	5	No	Unset	Unreviewed	
42373	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_26()	1430	50	No	Unset	Unreviewed	
42374	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_26()	1430	50	No	Unset	Unreviewed	
534	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_26()	1437	8	No	Unset	Unreviewed	
42383	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_26()	1444	34	No	Unset	Unreviewed	
42384	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_26()	1444	34	No	Unset	Unreviewed	
17896	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_26' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1449	5	No	Unset	Unreviewed	
42389	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_26()	1453	40	No	Unset	Unreviewed	
42390	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_26()	1453	40	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
524	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_26()	1454	117	No	Unset	Unreviewed	
17849	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Port_PORT_EXCLUSIVE_AR EA_27' conflicts with the external identifier 'SchM_Enter_ Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 437).	File Scope	1463	5	No	Unset	Unreviewed	
42399	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_27()	1468	50	No	Unset	Unreviewed	
42400	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_27()	1468	50	No	Unset	Unreviewed	
536	14.4		SchM_Enter_Port_PORT_E XCLUSIVE_AREA_27()	1475	8	No	Unset	Unreviewed	
42409	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_27()	1482	34	No	Unset	Unreviewed	
42410	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Port_PORT_E XCLUSIVE_AREA_27()	1482	34	No	Unset	Unreviewed	
18119	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Port_PORT_EXCLUSIVE_ARE A_27' conflicts with the external identifier 'SchM_Exit_Port_PORT_EXCLUSIVE_AREA_00' (SchM_Port.c line 461).	File Scope	1487	5	No	Unset	Unreviewed	
42415	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_27()	1491	40	No	Unset	Unreviewed	
42416	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_27()	1491	40	No	Unset	Unreviewed	
537	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_27()	1492	117	No	Unset	Unreviewed	

Table 4.52. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Rm.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
544	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
548	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volati le-qualified type	File Scope	95	16	No	Unset	Unreviewed	
547	D4.3	Assembly language shall be encapsulated and isolated.	Rm_schm_read_msr()	334	8	No	Unset	Unreviewed	
555	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Rm_schm_read_msr()	337	19	No	Unset	Unreviewed	
41755	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	412	48	No	Unset	Unreviewed	
41756	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	412	48	No	Unset	Unreviewed	
553	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	419	8	No	Unset	Unreviewed	
41757	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	426	32	No	Unset	Unreviewed	
41758	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	426	32	No	Unset	Unreviewed	
41767	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_00()	435	38	No	Unset	Unreviewed	
41768	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_00()	435	38	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
564	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_00()	436	115	No	Unset	Unreviewed	
17946	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA_01' conflicts with the external identifier 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	445	5	No	Unset	Unreviewed	
41779	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	450	48	No	Unset	Unreviewed	
41780	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	450	48	No	Unset	Unreviewed	
542	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	457	8	No	Unset	Unreviewed	
41785	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	464	32	No	Unset	Unreviewed	
41786	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	464	32	No	Unset	Unreviewed	
17832	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 01' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	469	5	No	Unset	Unreviewed	
41797	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_01()	473	38	No	Unset	Unreviewed	
41798	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_01()	473	38	No	Unset	Unreviewed	
546	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_01()	474	115	No	Unset	Unreviewed	
17844	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _02' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	483	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41804	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	488	48	No	Unset	Unreviewed	
41805	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	488	48	No	Unset	Unreviewed	
559	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	495	8	No	Unset	Unreviewed	
41820	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	502	32	No	Unset	Unreviewed	
41821	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	502	32	No	Unset	Unreviewed	
17991	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 02' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	507	5	No	Unset	Unreviewed	
41826	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_02()	511	38	No	Unset	Unreviewed	
41827	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_02()	511	38	No	Unset	Unreviewed	
545	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_02()	512	115	No	Unset	Unreviewed	
17978	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _03' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	521	5	No	Unset	Unreviewed	
41846	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	526	48	No	Unset	Unreviewed	
41847	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	526	48	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
543	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	533	8	No	Unset	Unreviewed	
41860	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	540	32	No	Unset	Unreviewed	
41861	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	540	32	No	Unset	Unreviewed	
18002	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 03' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	545	5	No	Unset	Unreviewed	
41870	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_03()	549	38	No	Unset	Unreviewed	
41871	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_03()	549	38	No	Unset	Unreviewed	
554	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_03()	550	115	No	Unset	Unreviewed	
17835	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _04' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	559	5	No	Unset	Unreviewed	
41876	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	564	48	No	Unset	Unreviewed	
41877	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	564	48	No	Unset	Unreviewed	
558	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	571	8	No	Unset	Unreviewed	
41886	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	578	32	No	Unset	Unreviewed	
41887	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	578	32	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
17831	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 04' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	583	5	No	Unset	Unreviewed	
41890	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_04()	587	38	No	Unset	Unreviewed	
41891	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_04()	587	38	No	Unset	Unreviewed	
568	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_04()	588	115	No	Unset	Unreviewed	
17949	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _05' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	597	5	No	Unset	Unreviewed	
41902	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	602	48	No	Unset	Unreviewed	
41903	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	602	48	No	Unset	Unreviewed	
551	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	609	8	No	Unset	Unreviewed	
41910	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	616	32	No	Unset	Unreviewed	
41911	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	616	32	No	Unset	Unreviewed	
17850	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 05' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	621	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41918	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_05()	625	38	No	Unset	Unreviewed	
41919	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_05()	625	38	No	Unset	Unreviewed	
556	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_05()	626	115	No	Unset	Unreviewed	
17852	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _06' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	635	5	No	Unset	Unreviewed	
41924	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	640	48	No	Unset	Unreviewed	
41925	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	640	48	No	Unset	Unreviewed	
549	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	647	8	No	Unset	Unreviewed	
41935	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	654	32	No	Unset	Unreviewed	
41936	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	654	32	No	Unset	Unreviewed	
17888	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 06' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	659	5	No	Unset	Unreviewed	
41943	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_06()	663	38	No	Unset	Unreviewed	
41944	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_06()	663	38	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
563	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_06()	664	115	No	Unset	Unreviewed	
17847	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _07' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	673	5	No	Unset	Unreviewed	
41953	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	678	48	No	Unset	Unreviewed	
41954	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	678	48	No	Unset	Unreviewed	
570	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	685	8	No	Unset	Unreviewed	
41965	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	692	32	No	Unset	Unreviewed	
41966	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	692	32	No	Unset	Unreviewed	
17922	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 07' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	697	5	No	Unset	Unreviewed	
41979	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_07()	701	38	No	Unset	Unreviewed	
41980	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_07()	701	38	No	Unset	Unreviewed	
569	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_07()	702	115	No	Unset	Unreviewed	
18101	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _08' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	711	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41987	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	716	48	No	Unset	Unreviewed	
41988	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	716	48	No	Unset	Unreviewed	
562	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	723	8	No	Unset	Unreviewed	
41993	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	730	32	No	Unset	Unreviewed	
41994	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	730	32	No	Unset	Unreviewed	
17845	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 08' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	735	5	No	Unset	Unreviewed	
41999	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_08()	739	38	No	Unset	Unreviewed	
42000	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_08()	739	38	No	Unset	Unreviewed	
565	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_08()	740	115	No	Unset	Unreviewed	
17829	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _09' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	749	5	No	Unset	Unreviewed	
42009	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	754	48	No	Unset	Unreviewed	
42010	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	754	48	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
561	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	761	8	No	Unset	Unreviewed	
42019	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	768	32	No	Unset	Unreviewed	
42020	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	768	32	No	Unset	Unreviewed	
18025	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 09' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	773	5	No	Unset	Unreviewed	
42025	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_09()	777	38	No	Unset	Unreviewed	
42026	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_09()	777	38	No	Unset	Unreviewed	
566	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_09()	778	115	No	Unset	Unreviewed	
17853	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _10' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	787	5	No	Unset	Unreviewed	
42039	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	792	48	No	Unset	Unreviewed	
42040	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	792	48	No	Unset	Unreviewed	
567	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	799	8	No	Unset	Unreviewed	
42049	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	806	32	No	Unset	Unreviewed	
42050	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	806	32	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		r operand. Warning: array index may be outside bounds : [03]							
17855	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 10' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	811	5	No	Unset	Unreviewed	
42055	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_10()	815	38	No	Unset	Unreviewed	
42056	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_10()	815	38	No	Unset	Unreviewed	
557	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_10()	816	115	No	Unset	Unreviewed	
17891	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _11' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	825	5	No	Unset	Unreviewed	
42061	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	830	48	No	Unset	Unreviewed	
42062	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	830	48	No	Unset	Unreviewed	
550	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	837	8	No	Unset	Unreviewed	
42069	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	844	32	No	Unset	Unreviewed	
42070	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	844	32	No	Unset	Unreviewed	
17865	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 11' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	849	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
42081	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_11()	853	38	No	Unset	Unreviewed	
42082	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_11()	853	38	No	Unset	Unreviewed	
552	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_11()	854	115	No	Unset	Unreviewed	
17861	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Rm_RM_EXCLUSIVE_AREA _12' conflicts with the external identifier 'SchM_Enter_Rm _RM_EXCLUSIVE_AREA_00' (SchM_Rm.c line 407).	File Scope	863	5	No	Unset	Unreviewed	
42091	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	868	48	No	Unset	Unreviewed	
42092	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	868	48	No	Unset	Unreviewed	
560	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	875	8	No	Unset	Unreviewed	
42099	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	882	32	No	Unset	Unreviewed	
42100	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	882	32	No	Unset	Unreviewed	
17825	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Rm_RM_EXCLUSIVE_AREA_ 12' conflicts with the external identifier 'SchM_Exit_Rm_R M_EXCLUSIVE_AREA_00' (SchM_Rm.c line 431).	File Scope	887	5	No	Unset	Unreviewed	
42107	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_12()	891	38	No	Unset	Unreviewed	
42108	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_12()	891	38	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
571	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_12()	892	115	No	Unset	Unreviewed	

Table 4.53. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Uart.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
577	10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type. The value of the composite expression of essential type ca tegory Boolean shall not be cast to the different essential type category unsigned.	File Scope	95	16	No	Unset	Unreviewed	
586	13.2	The value of an expression and its persistent side effects s hall be the same under all permitted evaluation orders. The value of the expression depends on the order of evaluation because of multiple read accesses to objects of volati le-qualified type	File Scope	95	16	No	Unset	Unreviewed	
572	D4.3	Assembly language shall be encapsulated and isolated.	Uart_schm_read_msr()	326	8	No	Unset	Unreviewed	
587	9.1	The value of an object with automatic storage duration sh all not be read before it has been set. Variable "reg_tmp" is used before its value is set	Uart_schm_read_msr()	329	19	No	Unset	Unreviewed	
580	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_00()	411	8	No	Unset	Unreviewed	
593	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_00()	428	117	No	Unset	Unreviewed	
17823	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_01' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	437	5	No	Unset	Unreviewed	
582	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_01()	449	8	No	Unset	Unreviewed	
17872	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_01' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	461	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
585	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_01()	466	117	No	Unset	Unreviewed	
18071	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_02' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	475	5	No	Unset	Unreviewed	
583	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_02()	487	8	No	Unset	Unreviewed	
17833	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_02' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	499	5	No	Unset	Unreviewed	
584	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_02()	504	117	No	Unset	Unreviewed	
17873	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_03' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	513	5	No	Unset	Unreviewed	
573	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_03()	525	8	No	Unset	Unreviewed	
17868	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_03' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	537	5	No	Unset	Unreviewed	
581	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_03()	542	117	No	Unset	Unreviewed	
17875	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_04' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	551	5	No	Unset	Unreviewed	
588	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_04()	563	8	No	Unset	Unreviewed	
17866	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE	File Scope	575	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		A_04' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).							
576	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_04()	580	117	No	Unset	Unreviewed	
18117	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_05' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	589	5	No	Unset	Unreviewed	
575	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.		601	8	No	Unset	Unreviewed	
17998	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_05' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	613	5	No	Unset	Unreviewed	
590	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_05()	618	117	No	Unset	Unreviewed	
18081	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_06' conflicts with the external identifier 'SchM_Enter_Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	627	5	No	Unset	Unreviewed	
41920	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_06()	632	50	No	Unset	Unreviewed	
41921	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_06()	632	50	No	Unset	Unreviewed	
591	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_06()	639	8	No	Unset	Unreviewed	
41931	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_06()	646	34	No	Unset	Unreviewed	
41932	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_06()	646	34	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
17837	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_06' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	651	5	No	Unset	Unreviewed	
41937	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_06()	655	40	No	Unset	Unreviewed	
41938	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_06()	655	40	No	Unset	Unreviewed	
574	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_06()	656	117	No	Unset	Unreviewed	
17874	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_07' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	665	5	No	Unset	Unreviewed	
41949	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_07()	670	50	No	Unset	Unreviewed	
41950	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_07()	670	50	No	Unset	Unreviewed	
592	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_07()	677	8	No	Unset	Unreviewed	
41960	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_07()	684	34	No	Unset	Unreviewed	
41961	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_07()	684	34	No	Unset	Unreviewed	
17878	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_07' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	689	5	No	Unset	Unreviewed	
41969	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_07()	693	40	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41970	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_07()	693	40	No	Unset	Unreviewed	
578	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_07()	694	117	No	Unset	Unreviewed	
17867	5.1	External identifiers shall be distinct. External function 'SchM_Enter_Uart_UART_EXCLUSIVE_A REA_08' conflicts with the external identifier 'SchM_Enter _Uart_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 399).	File Scope	703	5	No	Unset	Unreviewed	
41981	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_08()	708	50	No	Unset	Unreviewed	
41982	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_08()	708	50	No	Unset	Unreviewed	
589	14.4	The controlling expression of an if statement and the cont rolling expression of an iteration-statement shall have ess entially Boolean type.	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_08()	715	8	No	Unset	Unreviewed	
41989	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_08()	722	34	No	Unset	Unreviewed	
41990	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Enter_Uart_UART_E XCLUSIVE_AREA_08()	722	34	No	Unset	Unreviewed	
18113	5.1	External identifiers shall be distinct. External function 'SchM_Exit_Uart_UART_EXCLUSIVE_ARE A_08' conflicts with the external identifier 'SchM_Exit_Uar t_UART_EXCLUSIVE_AREA_00' (SchM_Uart.c line 423).	File Scope	727	5	No	Unset	Unreviewed	
41995	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_08()	731	40	No	Unset	Unreviewed	
41996	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [03]	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_08()	731	40	No	Unset	Unreviewed	
579	10.1	Operands shall not be of an inappropriate essential type. The left operand of the && operator is of an inappropriat e essential type category unsigned.	SchM_Exit_Uart_UART_EX CLUSIVE_AREA_08()	732	117	No	Unset	Unreviewed	

Table 4.54. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
595	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5495. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
604	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_PinInit()	308	19	No	Unset	Unreviewed	
599	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_PinInit()	314	19	No	Unset	Unreviewed	
600	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_PinInit()	322	19	No	Unset	Unreviewed	
611	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_PinInit()	360	19	No	Unset	Unreviewed	
607	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_GetMSCRConfiguration()	520	23	No	Unset	Unreviewed	
610	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category.	Siul2_Port_Ip_GetMSCRConfiguration()	522	24	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsign ed on 16 bits)							
613	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_GetMSCRConfiguration()	í 570	15	No	Unset	Unreviewed	
603	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_GetMSCRConfiguration()	f 576	15	No	Unset	Unreviewed	
598	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_GetMSCRConfiguration()	f 580	15	No	Unset	Unreviewed	
597	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_GetMSCRConfiguration()	f 582	15	No	Unset	Unreviewed	
41967	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetPullSel()	693	22	No	Unset	Unreviewed	
41968	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPullSel()	693	22	No	Unset	Unreviewed	
41971	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetPullSel()	699	31	No	Unset	Unreviewed	
41972	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPullSel()	699	31	No	Unset	Unreviewed	
41983	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetPullSel()	709	31	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
41984	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPullSel()	709	31	No	Unset	Unreviewed	
42005	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetOutputBu ffer()	747	14	No	Unset	Unreviewed	
42006	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetOutputBu ffer()	747	14	No	Unset	Unreviewed	
616	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetOutputBu ffer()	747	20	No	Unset	Unreviewed	
618	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetOutputBu ffer()	749	20	No	Unset	Unreviewed	
31538	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Siul2_Port_Ip_SetOutputBu ffer()	749	69	No	Unset	Unreviewed	
42035	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetInputBuff er()	785	18	No	Unset	Unreviewed	
42036	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetInputBuff er()	785	18	No	Unset	Unreviewed	
605	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetInputBuff er()	785	24	No	Unset	Unreviewed	
606	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as		786	24	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		signed to an object with a narrower essential type (unsign ed on 16 bits)							
31539	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Siul2_Port_Ip_SetInputBuff er()	786	73	No	Unset	Unreviewed	
42067	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetPinDirect ion()	842	18	No	Unset	Unreviewed	
42068	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	842	18	No	Unset	Unreviewed	
601	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetPinDirect ion()	842	24	No	Unset	Unreviewed	
602	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetPinDirect ion()	843	24	No	Unset	Unreviewed	
31540	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Siul2_Port_Ip_SetPinDirect ion()	843	58	No	Unset	Unreviewed	
42073	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [015]	Siul2_Port_Ip_SetPinDirect ion()	847	18	No	Unset	Unreviewed	
42074	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	847	18	No	Unset	Unreviewed	
608	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)		847	24	No	Unset	Unreviewed	
609	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as		848	24	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		signed to an object with a narrower essential type (unsign ed on 16 bits)							
31541	D4.1	Run-time failures shall be minimized. Error: scalar shift amount is outside its bounds[015]	Siul2_Port_Ip_SetPinDirect ion()	848	58	No	Unset	Unreviewed	
42077	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	852	18	No	Unset	Unreviewed	
42078	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	852	18	No	Unset	Unreviewed	
614	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetPinDirect ion()	852	24	No	Unset	Unreviewed	
42083	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	856	18	No	Unset	Unreviewed	
42084	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [015]	Siul2_Port_Ip_SetPinDirect ion()	856	18	No	Unset	Unreviewed	
612	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_SetPinDirect ion()	856	24	No	Unset	Unreviewed	
617	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	Siul2_Port_Ip_RevertPinCo nfiguration()	878	23	No	Unset	Unreviewed	
615	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)		958	15	No	Unset	Unreviewed	

Table 4.55.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Tspc_Port_Ip.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
620	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5632. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
41554	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Tspc_Port_Ip_ConfigureOb eGroup()	192	35	No	Unset	Unreviewed	
41555	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Tspc_Port_Ip_ConfigureOb eGroup()	192	35	No	Unset	Unreviewed	
41556	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Tspc_Port_Ip_ConfigureOb eGroup()	192	100	No	Unset	Unreviewed	
41559	D4.1	Run-time failures shall be minimized. Warning: array index may be outside bounds : [01]	Tspc_Port_Ip_ConfigureOb eGroup()	196	35	No	Unset	Unreviewed	
41560	18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as that pointe r operand. Warning: array index may be outside bounds: [01]	Tspc_Port_Ip_ConfigureOb eGroup()	196	35	No	Unset	Unreviewed	
41569	D4.1	Run-time failures shall be minimized. Warning: scalar shift amount may be outside its bounds[015]	Tspc_Port_Ip_ConfigureOb eGroup()	196	100	No	Unset	Unreviewed	

Table 4.56. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Uart_Ipw.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
628	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Uart_Ipw_apChnConfig' variable has no previous declaration.	File Scope	164	29	No	Unset	Unreviewed	

Table 4.57. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\board\Siul2_Port_Ip_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
594	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5164. The limit is 4095.	-	1	1	No	Unset	Unreviewed	

Table 4.58. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_I2c_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
5	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	168	5	No	Unset	Unreviewed	
6	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	168	5	No	Unset	Unreviewed	
4	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'I2c_PBCfgVariantPredefined' variable has no previous declaration.	File Scope	177	29	No	Unset	Unreviewed	

Table 4.59. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Mcl_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
2	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 6001. The limit is 4095.	-	1	1	No	Unset	Unreviewed	

Table 4.60.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Mcl_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
13	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 6005. The limit is 4095.	-	1	1	No	Unset	Unreviewed	

Table 4.61.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Rm_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
17	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Rm_Config' variable has no previous declaration.	File Scope	100	20	No	Unset	Unreviewed	

Table 4.62. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Uart_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
15	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Uart_ChannelConfig_0' variable has no previous declaration.	File Scope	165	29	No	Unset	Unreviewed	
16	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Uart_pxPBcfgVariantPredefined' variable has no previous declaration.	File Scope	193	29	No	Unset	Unreviewed	

Table 4.63.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Clock_Ip_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
83		The value of an expression shall not be assigned to an object with a narrower essential type or of a different essenti		208	8	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		al type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsign ed on 16 bits)							
62	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	921	8	No	Unset	Unreviewed	
63	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	938	8	No	Unset	Unreviewed	
75	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	955	8	No	Unset	Unreviewed	
66	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	972	8	No	Unset	Unreviewed	
80	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	991	8	No	Unset	Unreviewed	
81	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	997	8	No	Unset	Unreviewed	
61	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essenti	File Scope	1003	8	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		al type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsign ed on 16 bits)							
68	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	1009	8	No	Unset	Unreviewed	
67	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type unsigned on 32 bits) is as signed to an object with a narrower essential type (unsigned on 16 bits)	File Scope	1015	8	No	Unset	Unreviewed	
72	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1045	10	No	Unset	Unreviewed	
85	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1045	10	No	Unset	Unreviewed	
64	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1046	10	No	Unset	Unreviewed	
69	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1046	10	No	Unset	Unreviewed	
70	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1047	10	No	Unset	Unreviewed	
74	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1047	10	No	Unset	Unreviewed	
77	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1048	10	No	Unset	Unreviewed	
78	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1048	10	No	Unset	Unreviewed	
71	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1049	10	No	Unset	Unreviewed	
79	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1049	10	No	Unset	Unreviewed	
76	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1050	10	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
87	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1050	10	No	Unset	Unreviewed	
73	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1053	10	No	Unset	Unreviewed	
82	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1053	10	No	Unset	Unreviewed	
84	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1055	10	No	Unset	Unreviewed	
86	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1055	10	No	Unset	Unreviewed	
65	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	1056	10	No	Unset	Unreviewed	
88	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	1056	10	No	Unset	Unreviewed	

Table 4.64.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Ip_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
111	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4175. The limit is 4095.	-	1	1	No	Unset	Unreviewed	
122	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'GlobalResetConfig' variable has no previous declaration.	_	140	30	No	Unset	Unreviewed	
123	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'TransferResetConfig' variable has no previous declaration.	File Scope	163	32	No	Unset	Unreviewed	

Table 4.65.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Ip_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
138	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4176. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
140	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'LogicInstance0ConfigPB_BOARD_INIT PERIPHERALS' variable has no previous declaration.	File Scope	117	37	No	Unset	Unreviewed	
145	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Dma_Ip_paxLogicInstanceConfigArrayPB_BOARD_INITPERIPHERALS' variable has no previous declaration.	File Scope	132	45	No	Unset	Unreviewed	
141	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'LogicChannel0GlobalConfigPB_BOAR D_INITPERIPHERALS' variable has no previous declaration.	File Scope	149	30	No	Unset	Unreviewed	
146	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'LogicChannel1GlobalConfigPB_BOAR D_INITPERIPHERALS' variable has no previous declaration.	File Scope	171	30	No	Unset	Unreviewed	
142	8.5	An external object or function shall be declared once in o ne and only one file. Function 'Lpi2c_Ip_DmaCompleteNotification0' that has e xternal linkage should be declared in a header file.	File Scope	219	12	No	Unset	Unreviewed	
147	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'LogicChannelOConfigPB_BOARD_INIT PERIPHERALS' variable has no previous declaration.	File Scope	221	36	No	Unset	Unreviewed	
150	8.5	An external object or function shall be declared once in o ne and only one file. Function 'Lpi2c_Ip_DmaCompleteNotification0' that has e xternal linkage should be declared in a header file.	File Scope	236	12	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
17819	8.5	An external object or function shall be declared once in o ne and only one file. Function 'Lpi2c_Ip_DmaCompleteNotification0' has extern al declarations in multiple files	File Scope	236	12	No	Unset	Unreviewed	
143	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'LogicChannel1ConfigPB_BOARD_INIT PERIPHERALS' variable has no previous declaration.	File Scope	238	36	No	Unset	Unreviewed	
148	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Dma_Ip_paxLogicChannelConfigArrayPB_BOARD_INITPERIPHERALS' variable has no previous declaration.	File Scope	253	44	No	Unset	Unreviewed	
144	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'HwChannel0StatePB_BOARD_INITPE RIPHERALS' variable has no previous declaration.	File Scope	268	26	No	Unset	Unreviewed	
149	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'HwChannel1StatePB_BOARD_INITPE RIPHERALS' variable has no previous declaration.	File Scope	271	26	No	Unset	Unreviewed	
151	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Dma_Ip_paxHwChannelStateArrayPB_BOARD_INITPERIPHERALS' variable has no previous declaration.	File Scope	282	28	No	Unset	Unreviewed	

Table 4.66.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Mux_Ip_PBcfg .c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
139	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Dma_Mux_Config' variable has no previous declaration.	1	114	28	No	Unset	Unreviewed	

Table 4.67.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Flexio_I2c_Ip_PBcfg.

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
167	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4524. The limit is 4095.	1	1	1	No	Unset	Unreviewed	

Table 4.68.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Flexio_Uart_Ip_PBcf g.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
178	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'Flexio_Uart_Ip_apStateStructure' that has extern al linkage should be declared in a header file.	File Scope	114	41	No	Unset	Unreviewed	

Table 4.69. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Gpt_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
179	8.5	An external object or function shall be declared once in o ne and only one file. Function 'Gpt_PitNotification' that has external linkage sh ould be declared in a header file.	File Scope	116	12	No	Unset	Unreviewed	
180	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	166	5	No	Unset	Unreviewed	
181	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	166	5	No	Unset	Unreviewed	
182	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	170	5	No	Unset	Unreviewed	
183	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	170	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
184	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	176	5	No	Unset	Unreviewed	
185	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	176	5	No	Unset	Unreviewed	

Table 4.70. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Igf_Port_Ip_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
186	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5402. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

Table 4.71. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Igf_Port_Ip_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
188	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5402. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

Table 4.72. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\IntCtrl_Ip_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
189	D2.1	All source files shall compile without any compilation err ors. Identifier "DMATCD0_IRQn" is undefined	File Scope	73	5	No	Unset	Unreviewed	
191	D2.1	All source files shall compile without any compilation err ors. Identifier "DMATCD1_IRQn" is undefined	File Scope	74	5	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
192	D2.1	All source files shall compile without any compilation err ors. Identifier "LPUART6_IRQn" is undefined	File Scope	75	5	No	Unset	Unreviewed	
193	D2.1	All source files shall compile without any compilation err ors. Identifier "PITO_IRQn" is undefined	File Scope	76	5	No	Unset	Unreviewed	
190	D2.1	All source files shall compile without any compilation err ors. Expression must have a constant value	File Scope	80	4	No	Unset	Unreviewed	

Table 4.73. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Lpi2c_Ip_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
202	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 4756. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

Table 4.74.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Lpuart_Uart_Ip_PBc fg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
216	8.5	An external object or function shall be declared once in o ne and only one file. Variable 'Lpuart_Uart_Ip_apStateStructure' that has exter nal linkage should be declared in a header file.	File Scope	130	41	No	Unset	Unreviewed	
217	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Lpuart_Uart_Ip_xHwConfigPB_0' variable has no previous declaration.	File Scope	145	36	No	Unset	Unreviewed	
218	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assig	File Scope	148	4	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ned to an object with a different essential type category (unsigned)							

Table 4.75. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Mcu_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
220	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5871. The limit is 4095.		1	1	No	Unset	Unreviewed	

Table 4.76. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Mcu_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
222	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5871. The limit is 4095.	•	1	1	No	Unset	Unreviewed	
223	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	157	5	No	Unset	Unreviewed	
224	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	157	5	No	Unset	Unreviewed	
225	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	161	5	No	Unset	Unreviewed	
226	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	161	5	No	Unset	Unreviewed	

Table 4.77. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Port_Cfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
237	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme	File Scope	1	1	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
		ntation's translation limits. Too many macro definitions have been found in this translation unit: 7670. The limit is 4095.							

Table 4.78. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Port_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
239	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 7670. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
241	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'au8Port_PartitionList' variable has no previous declaration.	File Scope	93	12	No	Unset	Unreviewed	
242	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	-	294	129	No	Unset	Unreviewed	

Table 4.79. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Power_Ip_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
246	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	291	9	No	Unset	Unreviewed	
247	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	291	9	No	Unset	Unreviewed	
248	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	296	9	No	Unset	Unreviewed	
249	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	296	9	No	Unset	Unreviewed	
250	11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	File Scope	321	9	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
251	11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	File Scope	321	9	No	Unset	Unreviewed	

Table 4.80.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Siul2_Port_Ip_PBcfg .c

ID	Guidelin	Message	Function	Line	Col	Jus	Severity	Status	Comment
596	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5403. The limit is 4095.	-	1	1	No	Unset	Unreviewed	
619	9.3	Arrays shall not be partially initialized.	File Scope	128	33	No	Unset	Unreviewed	

Table 4.81.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Tspc_Port_Ip_PBcfg.
c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
621	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 5617. The limit is 4095.	File Scope	1	1	No	Unset	Unreviewed	
622	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)		108	26	No	Unset	Unreviewed	
623	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)		112	26	No	Unset	Unreviewed	

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
624	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	•	116	26	No	Unset	Unreviewed	
625	10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a different essential type category. The expression (of essential type category signed) is assigned to an object with a different essential type category (unsigned)	<u>-</u>	120	26	No	Unset	Unreviewed	

Table 4.82.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Uart_Ipw_PBcfg.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
626	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Uart_Ipw_IpChnConfigPB_0' variable has no previous declaration.	File Scope	166	28	No	Unset	Unreviewed	
627	8.4	A compatible declaration shall be visible when an object or function with external linkage is defined. Global definition of 'Uart_Ipw_xHwConfigPB_0' variable has no previous declaration.	File Scope	178	28	No	Unset	Unreviewed	

Table 4.83. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\src\main.c

ID	Guidelin e	Message	Function	Line	Col	Jus	Severity	Status	Comment
669	1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the impleme ntation's translation limits. Too many macro definitions have been found in this trans lation unit: 20113. The limit is 4095.	_	1	1	No	Unset	Unreviewed	

Chapter 5. Polyspace Run-Time Checks Results

Proven Run-Time Errors

Table 5.1.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Dma_Ip_Hwv3_Acces sInline.h

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24941	Invalid shift operations	hwv3AccInlineDmaCh_SetC ontrol_EnMasterIdReplicati on()		84	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
25323	Invalid shift operations	hwv3AccInlineDmaCh_SetP riority_EnPreemption()	328	87	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
25251	Invalid shift operations	hwv3AccInlineDmaCh_SetPriority_DisPreempt()	336	87	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
25043	Invalid shift operations	hwv3AccInlineDmaCh_Set MinorLoop_enSrcOffset()	403	101	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
25099	Invalid shift operations	hwv3AccInlineDmaCh_Set MinorLoop_enDstOffset()	409	101	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.2. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21063	Non-terminating call	Clock_Ip_InitClock()	747	4	The called function Clock_Ip_Specific.Cl ock_Ip_Command (in the current conte xt) either contains an error or does not terminate.	No	Unset	Unreviewed	

Table 5.3. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Divider.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21868	Illegally dereferenced pointer	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	180	63	Error: pointer is outside its bounds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21893	Illegally dereferenced pointer	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	205	69	Error: pointer is outside its bounds	No	Unset	Unreviewed	
21813	Invalid shift operations	Clock_Ip_SetPllPll0divDeDi vOutput()	265	70	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
21777	Invalid shift operations	Clock_Ip_SetPllPlldvOdiv2O utput()	293	60	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.4.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_DividerTrigger.
c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21932	Illegally dereferenced pointer	Clock_Ip_ConfigureCgmXDivTrigCtrlTctlHhenUpdStat()		52	Error: pointer is outside its bounds	No	Unset	Unreviewed	
21956	Illegally dereferenced pointer	Clock_Ip_ConfigureCgmXDivTrigCtrlTctlHhenUpdStat()	159	52	Error: pointer is outside its bounds	No	Unset	Unreviewed	
21986	Illegally dereferenced pointer	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUpd Stat()		52	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.5.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ExtOsc.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22026	Invalid shift operations	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	203	64	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
22144	Invalid shift operations	Clock_Ip_SetSxoscOsconEoc v()	345	64	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.6.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Gate.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22272	Illegally dereferenced pointer	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()		60	Error: pointer is outside its bounds	No	Unset	Unreviewed	
39642	Illegally dereferenced pointer	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()		60	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.7. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Monitor.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22434	Non-terminating loop	Clock_Ip_CMU_ClockFailInt(491	4	The loop is infinite or contains a run-ti	No	Unset	Unreviewed	
)			me error.				

Table 5.8. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Pll.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22732		Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSprea dctlStepnoStepsize()	223	67	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.9. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22854	Division by zero	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	234	87	Error: scalar division by zero occurs	No	Unset	Unreviewed	

Table 5.10. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Selector.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23342	Illegally dereferenced pointer	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	48	Error: pointer is outside its bounds	No	Unset	Unreviewed	
39831	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssClk swSwip()	215	71	Error: pointer is outside its bounds	No	Unset	Unreviewed	
23288	Illegally dereferenced pointer	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()		48	Error: pointer is outside its bounds	No	Unset	Unreviewed	
39934	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip ()		71	Error: pointer is outside its bounds	No	Unset	Unreviewed	
23240	Illegally dereferenced pointer	Clock_Ip_ResetCgmXCscCss CsGrip()	453	48	Error: pointer is outside its bounds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22956	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssCs Grip()	518	48	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.11.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23536	Invalid shift operations	Clock_Ip_PllPowerClockIp()	227	90	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.12. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24206	Illegally dereferenced pointer	Dma_Ip_ConvertLogicChTo HwCh()	298	34	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24124	Illegally dereferenced pointer	Dma_Ip_SetLogicInstanceC ommand()	689	74	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24214	Illegally dereferenced pointer	Dma_Ip_GetLogicInstanceSt atus()	743	74	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24223	Illegally dereferenced pointer	Dma_Ip_SetLogicChannelCo mmand()	1006	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24371	Illegally dereferenced pointer	Dma_Ip_GetLogicChannelSt atus()	1103	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24412	Illegally dereferenced pointer	Dma_Ip_SetLogicChannelGl obalList()	1173	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24487	Illegally dereferenced pointer	Dma_Ip_SetLogicChannelTr ansferList()	1285	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24133	Illegally dereferenced pointer	Dma_Ip_SetLogicChannelSc atterGatherList()	1425	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24466	Illegally dereferenced pointer	Dma_Ip_GetLogicChannelP aram()	1559	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24284	Illegally dereferenced pointer	Dma_Ip_SetLogicChannelSc atterGatherConfig()	1618	73	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.13.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24608	Illegally dereferenced pointer	Dma_Ip_SetHwChannel_Re setEvent()	144	62	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24630	Illegally dereferenced pointer	Dma_Ip_SetHwChannel_Re adyEvent()	157	62	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24615	Illegally dereferenced pointer	Dma_Ip_SetHwChannel_Tra nsferEvent()	170	62	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24637	Illegally dereferenced pointer	Dma_Ip_SetHwChannel_Sca tterGatherEvent()	183	62	Error: pointer is outside its bounds	No	Unset	Unreviewed	
24644	Illegally dereferenced pointer	Dma_Ip_SetHwChannel_Err orEvent()	196	62	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.14. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Irq.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25402	Non-terminating call	Dma0_Ch0_IRQHandler()	389	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.		Unset	Unreviewed	
25407	Non-terminating call	Dma0_Ch4_IRQHandler()	409	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.	No	Unset	Unreviewed	
25405	Non-terminating call	Dma0_Ch8_IRQHandler()	429	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.	No	Unset	Unreviewed	
25404	Non-terminating call	Dma0_Ch12_IRQHandler()	449	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.	No	Unset	Unreviewed	
25409	Non-terminating call	Dma0_Ch16_IRQHandler()	469	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.		Unset	Unreviewed	
25403	Non-terminating call	Dma0_Ch20_IRQHandler()	489	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.	No	Unset	Unreviewed	
25406	Non-terminating call	Dma0_Ch24_IRQHandler()	509	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25348	Non-terminating call	Dma0_Ch28_IRQHandler()	529	4	The called function Dma_Ip_Irq.IrqHan dler (in the current context) either cont ains an error or does not terminate.		Unset	Unreviewed	

Table 5.15. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Emios_Mcl_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25459	Invalid shift operations	Emios_Mcl_Ip_Init()	204	198	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.16. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip_HwAccess.

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25931	Invalid shift operations	Flexio_Mcl_Ip_SetDebugEn able()	179	62	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.17. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpi2c_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26631	Illegally dereferenced pointer	Lpi2c_Ip_MasterCompleteD MATransfer()	971	14	Error: pointer is outside its bounds	No	Unset	Unreviewed	
40811	Illegally dereferenced pointer	Lpi2c_Ip_MasterSetBaudRa te()	1717	13	Error: pointer is outside its bounds	No	Unset	Unreviewed	
40861	Illegally dereferenced pointer	Lpi2c_Ip_MasterGetBaudRa te()	2020	38	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.18. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26879	Illegally dereferenced pointer	Mcu_InitClock()	867	47	Error: pointer is outside its bounds	No	Unset	Unreviewed	
26870	Illegally dereferenced pointer	Mcu_SetMode()	917	46	Error: pointer is outside its bounds	No	Unset	Unreviewed	

Table 5.19. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Pit_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26988	Invalid shift operations	Pit_Ip_GetLifetimeTimer()	1595	37	Error: scalar shift amount is outside its bounds[031]	No	Unset	Unreviewed	

Table 5.20. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_DCM_GPR.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27225	Invalid shift operations	Power_Ip_DCM_GPR_Global PadkeepingConfig()	246	51	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Table 5.21. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_RGM.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27265	Non-terminating loop	Power_Ip_MC_RGM_GetRes etRawValue()	640	8	The loop is infinite or contains a run-ti me error.	No	Unset	Unreviewed	
27286	Non-terminating loop	Power_Ip_MC_RGM_GetRes etRawValue()	679	8	The loop is infinite or contains a run-ti me error.	No	Unset	Unreviewed	

Table 5.22. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31101	Invalid shift operations	Siul2_Port_Ip_SetOutputBuf fer()	749	69	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
31012	Invalid shift operations	Siul2_Port_Ip_SetInputBuff er()	786	73	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
31055	Invalid shift operations	Siul2_Port_Ip_SetPinDirecti on()	843	58	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	
31071	Invalid shift operations	Siul2_Port_Ip_SetPinDirecti on()	848	58	Error: scalar shift amount is outside its bounds[015]	No	Unset	Unreviewed	

Proven Unreachable Code Branches

Table 5.23. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Dma_Ip_Hwv3_Acces sInline.h

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25119	Unreachable code	hwv3AccInlineDmaCh_Set MinorLoop_Size()	438	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 437 (column 7). Block ends at line 440 (column 4)	No	Unset	Unreviewed	

Table 5.24. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Pit_Ip.h

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31344	Unreachable code	Pit_Ip_GetInterruptStatusFl ag()	234	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 233 (column 9). Block ends at line 236 (column 4) The block is only partially unreachabl e.		Unset	Unreviewed	

Table 5.25. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_I2c.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20186	Unreachable code	I2c_CheckInit()	276	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 275 (column 9). Block ends at line 279 (column 4)	No	Unset	Unreviewed	
20166	Unreachable code	I2c_CheckInit()	311	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 305 (column 13). Block ends at line 328 (column 8)	No	Unset	Unreviewed	
20203	Unreachable code	I2c_CheckMasterTransmit()	361	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					t line 354 (column 9). Block ends at line 395 (column 4)				
20230	Unreachable code	I2c_CheckSlaveApi()	429	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 422 (column 9). Block ends at line 463 (column 4)	No	Unset	Unreviewed	
20411	Unreachable code	I2c_Init()	500	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 499 (column 7). Block ends at line 520 (column 4)		Unset	Unreviewed	
20106	Unreachable code	I2c_DeInit()	546	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 540 (column 9). Block ends at line 566 (column 4)	No	Unset	Unreviewed	
20261	Unreachable code	I2c_PrepareSlaveBuffer()	589	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 588 (column 7). Block ends at line 608 (column 4)		Unset	Unreviewed	
20347	Unreachable code	I2c_SyncTransmit()	632	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 631 (column 7). Block ends at line 674 (column 4)		Unset	Unreviewed	
20043	Unreachable code	I2c_AsyncTransmit()	696	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 695 (column 7). Block ends at line 737 (column 4)		Unset	Unreviewed	
20478	Unreachable code	I2c_StartListening()	761	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 760 (column 7). Block ends at line 781 (column 4)		Unset	Unreviewed	
20309	Unreachable code	I2c_GetStatus()	810	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 803 (column 9). Block ends at line 834 (column 4)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20199	Unreachable code	I2c_GetVersionInfo()	852	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 851 (column 9). Block ends at line 854 (column 4)	No	Unset	Unreviewed	

Table 5.26. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Mcl.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20633	Unreachable code	Mcl_ValidatePtrInit()	259	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 253 (column 9).	No	Unset	Unreviewed	
20531	Unreachable code	Mcl_Init()	349	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 348 (column 7). Block ends at line 427 (column 4)	No	Unset	Unreviewed	
20663	Unreachable code	Mcl_DeInit()	444	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 443 (column 8). Block ends at line 483 (column 4)	No	Unset	Unreviewed	
20608	Unreachable code	Mcl_SetDmaInstanceComm and()	507	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 506 (column 11). Block ends at line 509 (column 8)	No	Unset	Unreviewed	
20629	Unreachable code	Mcl_GetDmaInstanceStatus 0	533	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 532 (column 13). Block ends at line 535 (column 8)	No	Unset	Unreviewed	
20598	Unreachable code	Mcl_SetDmaChannelComm and()	562	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 561 (column 11). Block ends at line 564 (column 8)	No	Unset	Unreviewed	
20588	Unreachable code	Mcl_GetDmaChannelStatus(588	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a		Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					t line 587 (column 13). Block ends at line 590 (column 8)				
20558	Unreachable code	Mcl_SetDmaChannelGlobal List()	626	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 625 (column 11). Block ends at line 630 (column 8)	No	Unset	Unreviewed	
20643	Unreachable code	Mcl_SetDmaChannelTransf erList()	662	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 661 (column 11). Block ends at line 666 (column 8)	No	Unset	Unreviewed	
20700	Unreachable code	Mcl_SetDmaChannelScatter GatherList()	698	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 697 (column 11). Block ends at line 702 (column 8)	No	Unset	Unreviewed	
20689	Unreachable code	Mcl_GetDmaChannelParam ()	725	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 724 (column 11). Block ends at line 727 (column 8)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20719	Unreachable code	Rm_ValidateGlobalCall()	191	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 190 (column 9). Block ends at line 200 (column 4)	No	Unset	Unreviewed	
20721	Unreachable code	Rm_ValidateGlobalCall()	208	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 203 (column 13). Block ends at line 210 (column 8)	No	Unset	Unreviewed	
20726	Unreachable code	Rm_ValidatePtrInit()	249	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 243 (column 13).	No	Unset	Unreviewed	
20730	Unreachable code	Rm_Init()	336	4	The section of code is unreachable or the condition is redundant.	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					If-condition always evaluates to true a t line 336 (column 8).				
20734	Unreachable code	Rm_Init()	340	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 339 (column 12). Block ends at line 381 (column 8)		Unset	Unreviewed	

$Table 5.28. \ D: \ \ D: \ \ S32DS_S32K312EVBQ172 \land S_I2C_Master_DMA_FunctionTest_RTD3.0.0 \land S32K312_I2C_Master_MCAL \land RTD \land S_I2C_Master_DMA_FunctionTest_RTD3.0.0 \land S32K312_I2C_Master_MCAL \land S_I2C_Master_DMA_FunctionTest_RTD3.0.0 \land S32K312_I2C_Master_MCAL \land S_I2C_Master_MCAL \land S_I2C_Master_DMA_FunctionTest_RTD3.0.0 \land S_I2C_Master_MCAL \land S_I2C_Master_MCAL \land S_I2C_Master_DMA_FunctionTest_RTD3.0.0 \land S_I2C_Master_MCAL \land S_I2C_$

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20956	Unreachable code	Uart_Init()	550	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 549 (column 8). Block ends at line 558 (column 4)	No	Unset	Unreviewed	
20903	Unreachable code	Uart_Init()	616	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 592 (column 13). Block ends at line 640 (column 8)	No	Unset	Unreviewed	
20837	Unreachable code	Uart_Deinit()	668	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 657 (column 8). Block ends at line 726 (column 4)	No	Unset	Unreviewed	
20759	Unreachable code	Uart_SetBaudrate()	751	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 740 (column 8). Block ends at line 811 (column 4)	No	Unset	Unreviewed	
20746	Unreachable code	Uart_GetBaudrate()	835	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 825 (column 8). Block ends at line 894 (column 4)	No	Unset	Unreviewed	
20822	Unreachable code	Uart_Abort()	918	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 908 (column 8). Block ends at line 973 (column 4)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
20960	Unreachable code	Uart_SetBuffer()	995	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 985 (column 8). Block ends at line 1033 (column 4)	No	Unset	Unreviewed	
20978	Unreachable code	Uart_SyncSend()	1057	4	The section of code is unreachable or the condition is redundant. If-condition always evaluates to true at line 1047 (column 8). Block ends at line 1095 (column 4)	No	Unset	Unreviewed	
20998	Unreachable code	Uart_SyncReceive()	1121	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1111 (column 8). Block ends at line 1159 (column 4)	No	Unset	Unreviewed	
20784	Unreachable code	Uart_AsyncReceive()	1183	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1173 (column 8). Block ends at line 1221 (column 4)	No	Unset	Unreviewed	
20803	Unreachable code	Uart_AsyncSend()	1245	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1235 (column 8). Block ends at line 1283 (column 4)	No	Unset	Unreviewed	
21018	Unreachable code	Uart_GetStatus()	1307	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1297 (column 8). Block ends at line 1374 (column 4)	No	Unset	Unreviewed	
21046	Unreachable code	Uart_GetVersionInfo()	1383	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1382 (column 9). Block ends at line 1391 (column 4)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21637	Unreachable code	Clock_Ip_GetPllStatus()	935	4	The section of code is unreachable or t he condition is redundant. For-condition always evaluates to fals	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					e at line 934 (column 21). Block ends at line 950 (column 4)				
21657	Unreachable code	Clock_Ip_GetPllStatus()	953	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 952 (column 8). Block ends at line 964 (column 4)	No	Unset	Unreviewed	
21512	Unreachable code	Clock_Ip_DistributePll()	986	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 986 (column 9).	No	Unset	Unreviewed	
21548	Unreachable code	Clock_Ip_DistributePll()	1002	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1001 (column 13). Block ends at line 1020 (column 8)	No	Unset	Unreviewed	

Table 5.30. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Divider.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
39585	Unreachable code	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	230	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 169 (column 9). Block ends at line 242 (column 4)	No	Unset	Unreviewed	
21785	Unreachable code	Clock_Ip_SetPllPll0divDeDi vOutput()	270	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 253 (column 9). Block ends at line 274 (column 4)	No	Unset	Unreviewed	
21757	Unreachable code	Clock_Ip_SetPllPlldvOdiv2O utput()	297	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 286 (column 9). Block ends at line 301 (column 4)	No	Unset	Unreviewed	

Table 5.31.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_DividerTrigger.
c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21934	Unreachable code	Clock_Ip_ConfigureCgmXDi vTrigCtrlTctlHhenUpdStat()		4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 147 (column 9). Block ends at line 167 (column 4)	No	Unset	Unreviewed	
39609	Unreachable code	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUpd Stat()		4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 181 (column 9). Block ends at line 217 (column 4)	No	Unset	Unreviewed	

Table 5.32. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ExtOsc.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22090	Unreachable code	Clock_Ip_ResetFxoscOsconB ypEocvGmSel()	184	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 176 (column 9). Block ends at line 186 (column 4)	No	Unset	Unreviewed	
22010	Unreachable code	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	225	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 194 (column 9). Block ends at line 227 (column 4)	No	Unset	Unreviewed	
22177	Unreachable code	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	261	20	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 261 (column 24).	No	Unset	Unreviewed	
22147	Unreachable code	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	277	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 238 (column 9). Block ends at line 284 (column 4)	No	Unset	Unreviewed	
22041	Unreachable code	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	308	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					t line 297 (column 9). Block ends at line 310 (column 4)				
22114	Unreachable code	Clock_Ip_ResetSxoscOsconE ocv()	327	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 319 (column 9). Block ends at line 329 (column 4)	No	Unset	Unreviewed	
22128	Unreachable code	Clock_Ip_SetSxoscOsconEoc v()	355	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 337 (column 9). Block ends at line 357 (column 4)	No	Unset	Unreviewed	
22085	Unreachable code	Clock_Ip_CompleteSxoscOs conEocv()	385	16	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 385 (column 20).	No	Unset	Unreviewed	
22058	Unreachable code	Clock_Ip_CompleteSxoscOs conEocv()	400	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 368 (column 9). Block ends at line 407 (column 4)	No	Unset	Unreviewed	

Table 5.33. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Gate.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22248	Unreachable code	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()		4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 165 (column 9). Block ends at line 243 (column 4)	No	Unset	Unreviewed	

Table 5.34. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_IntOsc.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
2236	Unreachable code	Clock_Ip_SetFircStdby()	162	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 162 (column 9).	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22311	Unreachable code	Clock_Ip_SetSircStdby()	192	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 192 (column 9).	No	Unset	Unreviewed	
22334	Unreachable code	Clock_Ip_SetFircDivSelHSE b()	257	12	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 256 (column 17). Block ends at line 280 (column 12)	No	Unset	Unreviewed	
39671	Unreachable code	Clock_Ip_SetFircDivSelHSE b()	289	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 234 (column 9). Block ends at line 297 (column 4)	No	Unset	Unreviewed	

Table 5.35.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Monitor.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22600	Unreachable code	Clock_Ip_ResetCmuFcFceRe fCntLfrefHfref()	325	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 325 (column 9).	No	Unset	Unreviewed	
22442	Unreachable code	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	410	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 349 (column 9). Block ends at line 425 (column 4)	No	Unset	Unreviewed	
22383	Unreachable code	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	450	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 434 (column 9). Block ends at line 454 (column 4)	No	Unset	Unreviewed	
22410	Unreachable code	Clock_Ip_CMU_ClockFailInt()	517	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 497 (column 12). Block ends at line 520 (column 8)	No	Unset	Unreviewed	

Table 5.36. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Pll.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
39706	Unreachable code	Clock_Ip_ResetPllRdivMfiM fnOdiv2SdmenSsscgbypSpr eadctlStepnoStepsize()	196	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 183 (column 9). Block ends at line 199 (column 4)	No	Unset	Unreviewed	
39715	Unreachable code	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSprea dctlStepnoStepsize()	241	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 206 (column 9). Block ends at line 244 (column 4)	No	Unset	Unreviewed	
22645	Unreachable code	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize()	277	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 258 (column 8). Block ends at line 279 (column 4)	No	Unset	Unreviewed	
22620	Unreachable code	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	299	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 287 (column 9). Block ends at line 301 (column 4)	No	Unset	Unreviewed	

Table 5.37.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
39727	Unreachable code	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	300	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 206 (column 9). Block ends at line 318 (column 4)	No	Unset	Unreviewed	

Table 5.38. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Selector.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23344	Unreachable code	Clock_Ip_ResetCgmXCscCss ClkswSwip()	181	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					t line 171 (column 9). Block ends at line 185 (column 4)				
39832	Unreachable code	Clock_Ip_SetCgmXCscCssClk swSwip()	278	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 205 (column 9). Block ends at line 289 (column 4)	No	Unset	Unreviewed	
23290	Unreachable code	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()	313	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 302 (column 9). Block ends at line 317 (column 4)	No	Unset	Unreviewed	
39928	Unreachable code	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip ()	413	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 337 (column 9). Block ends at line 424 (column 4)	No	Unset	Unreviewed	
39904	Unreachable code	Clock_Ip_ResetCgmXCscCss CsGrip()	480	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 444 (column 9). Block ends at line 491 (column 4)	No	Unset	Unreviewed	
39795	Unreachable code	Clock_Ip_SetCgmXCscCssCs Grip()	558	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 509 (column 9). Block ends at line 569 (column 4)	No	Unset	Unreviewed	
23063	Unreachable code	Clock_Ip_SetRtcRtccClksel()	576	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 576 (column 9).	No	Unset	Unreviewed	

Table 5.39. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23540	Unreachable code	Clock_Ip_PllPowerClockIp()	224	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 224 (column 8).	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23585	Unreachable code	Clock_Ip_CodeInRamSetFla shWaitStates()	405	13	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 401 (column 12).	No	Unset	Unreviewed	
23587	Unreachable code	Clock_Ip_CodeInRamSetFla shWaitStates()	419	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 399 (column 8). Block ends at line 436 (column 4)	No	Unset	Unreviewed	

Table 5.40. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24356	Unreachable code	Dma_Ip_IntIrqHandler()	1818	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1817 (column 8). Block ends at line 1823 (column 4)	No	Unset	Unreviewed	

Table 5.41. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

1	D	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
2	24617	Unreachable code	Dma_Ip_GetHwChannelStat ePointer()	254	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 254 (column 8).	No	Unset	Unreviewed	

Table 5.42. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Irq.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25378	Unreachable code	IrqHandler()	368	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 367 (column 8). Block ends at line 375 (column 4)		Unset	Unreviewed	

Table 5.43. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Emios_Mcl_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25736	Unreachable code	Emios_Mcl_Ip_Init()	197	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 196 (column 12). Block ends at line 199 (column 8)	No	Unset	Unreviewed	
25788	Unreachable code	Emios_Mcl_Ip_Deinit()	351	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 346 (column 12). Block ends at line 376 (column 8)	No	Unset	Unreviewed	
25746	Unreachable code	Emios_Mcl_Ip_ValidateCha nnel()	412	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 411 (column 8). Block ends at line 414 (column 4)	No	Unset	Unreviewed	
25758	Unreachable code	Emios_Mcl_Ip_SetCounterB usPeriod()	434	10	The section of code is unreachable or t he condition is redundant.	No	Unset	Unreviewed	
25772	Unreachable code	Emios_Mcl_Ip_SetCounterB usPeriod()	435	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 432 (column 8). Block ends at line 437 (column 4)	No	Unset	Unreviewed	

Table 5.44.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip_HwAccess. c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25999	Unreachable code	Flexio_Mcl_Ip_SetSoftware Reset()	164	56	The section of code is unreachable or t he condition is redundant.	No	Unset	Unreviewed	
26032	Unreachable code	Flexio_Mcl_Ip_SetEnable()	193	56	The section of code is unreachable or the condition is redundant.	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26109	Unreachable code	Gpt_ValidateChannelCall()	375	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 364 (column 9). Block ends at line 405 (column 4)	No	Unset	Unreviewed	
26180	Unreachable code	Gpt_ValidateGlobalCall()	454	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 435 (column 9). Block ends at line 469 (column 4)	No	Unset	Unreviewed	
26129	Unreachable code	Gpt_ValidateParamPtrInit()	841	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 830 (column 9). Block ends at line 843 (column 4)	No	Unset	Unreviewed	
26067	Unreachable code	Gpt_Init()	1269	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1269 (column 7).	No	Unset	Unreviewed	
26070	Unreachable code	Gpt_Init()	1275	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1274 (column 11). Block ends at line 1324 (column 8)		Unset	Unreviewed	
26190	Unreachable code	Gpt_DeInit()	1362	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1361 (column 7). Block ends at line 1416 (column 4)		Unset	Unreviewed	
26350	Unreachable code	Gpt_StartTimer()	1671	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1670 (column 7). Block ends at line 1711 (column 4)		Unset	Unreviewed	
26293	Unreachable code	Gpt_StopTimer()	1749	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1748 (column 7). Block ends at line 1783 (column 4)		Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26410	Unreachable code	Gpt_EnableNotification()	1815	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1814 (column 7). Block ends at line 1837 (column 4)	No	Unset	Unreviewed	
26134	Unreachable code	Gpt_DisableNotification()	1872	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1871 (column 7). Block ends at line 1900 (column 4)	No	Unset	Unreviewed	
26250	Unreachable code	Gpt_ProcessCommonInterr upt()	2207	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 2206 (column 8). Block ends at line 2233 (column 4)	No	Unset	Unreviewed	

Table 5.46. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpi2c_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26562	Unreachable code	Lpi2c_Ip_ModuleIRQHandl er()	3122	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 3121 (column 8). Block ends at line 3159 (column 4)		Unset	Unreviewed	

Table 5.47. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpuart_Uart_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26778	Unreachable code	Lpuart_Uart_Ip_IrqHandler ()	1201	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 1192 (column 9). Block ends at line 1245 (column 4)	No	Unset	Unreviewed	

Table 5.48. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26874	Unreachable code	Mcu_ClkSrcFailureNotificat ion()	1439	44	The section of code is unreachable or t he condition is redundant.	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
2687	3 Unreachable code	Mcu_ClkSrcFailureNotificat ion()	1440	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 1439 (column 9).	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26898	Unreachable code	Mcu_Ipw_GetPllStatus()	449	8	The section of code is unreachable or t he condition is redundant. Switch-expression never evaluates to 0 at line 447 (column 11).	No	Unset	Unreviewed	
26899	Unreachable code	Mcu_Ipw_GetPllStatus()	454	8	The section of code is unreachable or t he condition is redundant. Switch-expression never evaluates to 1 at line 447 (column 11).	No	Unset	Unreviewed	

Table 5.50. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\OsIf_Timer.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26921	Unreachable code	OsIf_Init()	235	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 235 (column 8).	No	Unset	Unreviewed	
26909	Unreachable code	OsIf_GetCounter()	273	8	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable. s witch-expression at line 259 (column 1 2).	No	Unset	Unreviewed	
26925	Unreachable code	OsIf_GetElapsed()	297	8	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable. s witch-expression at line 283 (column 1 2).	No	Unset	Unreviewed	
26913	Unreachable code	OsIf_SetTimerFrequency()	320	8	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable. s witch-expression at line 306 (column 1 2).	No	Unset	Unreviewed	
26918	Unreachable code	OsIf_MicrosToTicks()	342	8	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable. s	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					witch-expression at line 328 (column 1 2).				

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26967	Unreachable code	Pit_Ip_EnableTimer()	476	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 460 (column 12). Block ends at line 489 (column 8)	No	Unset	Unreviewed	
27010	Unreachable code	Pit_Ip_SetChainMode()	537	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 532 (column 12). Block ends at line 539 (column 8)	No	Unset	Unreviewed	
27000	Unreachable code	Pit_Ip_SetEnableInterruptFl ag()	710	8	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 709 (column 12). Block ends at line 723 (column 8)	No	Unset	Unreviewed	
27032	Unreachable code	Pit_Ip_ProcessCommonInte rrupt()	954	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 954 (column 8).	No	Unset	Unreviewed	
27066	Unreachable code	Pit_Ip_ProcessCommonInte rrupt()	983	12	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 982 (column 15). Block ends at line 985 (column 12)	No	Unset	Unreviewed	
27070	Unreachable code	Pit_Ip_ProcessCommonInte rrupt()	988	33	The section of code is unreachable or the condition is redundant.	No	Unset	Unreviewed	
27072	Unreachable code	Pit_Ip_ProcessCommonInte rrupt()	989	12	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 988 (column 16). Block ends at line 991 (column 12)	No	Unset	Unreviewed	

Table 5.52. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Port.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27126	Unreachable code	Port_Init()	185	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 178 (column 9). Block ends at line 203 (column 4)	No	Unset	Unreviewed	
27170	Unreachable code	Port_SetPinDirection()	236	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 230 (column 9). Block ends at line 250 (column 4)	No	Unset	Unreviewed	
27149	Unreachable code	Port_SetPinMode()	340	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 334 (column 9). Block ends at line 354 (column 4)	No	Unset	Unreviewed	
27138	Unreachable code	Port_RefreshPortDirection()	379	9	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 375 (column 9).	No	Unset	Unreviewed	
27168	Unreachable code	Port_GetVersionInfo()	412	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 411 (column 9). Block ends at line 414 (column 4)	No	Unset	Unreviewed	

Table 5.53.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_RGM.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
36408	Unreachable code	Power_Ip_MC_RGM_ClearF esResetFlags()	251	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 251 (column 8).	No	Unset	Unreviewed	
36418	Unreachable code	Power_Ip_MC_RGM_CheckR esetReason()	459	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 445 (column 8). Block ends at line 480 (column 4)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
33798	Unreachable code	Power_Ip_MC_RGM_GetRes etReason()	555	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to false a t line 554 (column 8). Block ends at line 574 (column 4)	No	Unset	Unreviewed	
27289	Unreachable code	Power_Ip_MC_RGM_GetRes etRawValue()	698	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 698 (column 8).	No	Unset	Unreviewed	

Table 5.54.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_PMC.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
274	Unreachable code	Power_Ip_PMC_PowerInit()	266	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 266 (column 10).	No	Unset	Unreviewed	

Table 5.55.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31154	Unreachable code	Siul2_Port_Ip_SetPullSel()	718	8	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable. s witch-expression at line 688 (column 1 2).	No	Unset	Unreviewed	
30999	Unreachable code	Siul2_Port_Ip_SetInputBuff er()	782	4	The section of code is unreachable or t he condition is redundant. If-condition always evaluates to true a t line 782 (column 8).	No	Unset	Unreviewed	
31088	Unreachable code	Siul2_Port_Ip_SetPinDirecti on()	858	4	The section of code is unreachable or t he condition is redundant. Default switch clause is unreachable, s witch-expression at line 838 (column 1 2).	No	Unset	Unreviewed	
31163	Unreachable code	Siul2_Port_Ip_RevertPinCon figuration()	907	4	The section of code is unreachable or t he condition is redundant. For-condition always evaluates to fals	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
					e at line 906 (column 25). Block ends at line 916 (column 4)				
30940	Unreachable code	Siul2_Port_Ip_GetPinConfig uration()	963	4	The section of code is unreachable or t he condition is redundant. For-condition always evaluates to fals e at line 962 (column 25). Block ends at line 981 (column 4)	No	Unset	Unreviewed	

Unreachable Function Checks

Option to check for unreachable functions was not enabled. To enable the checks, use the option -uncalled-function-checks.

Unproven Run-Time Errors

Table 5.56.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\Project_Settings\Startup_Code\st artup.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31432	Illegally dereferenced pointer	init_data_bss()	122	15	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31428	Illegally dereferenced pointer	init_data_bss()	122	24	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31429	Non-initialized variable	init_data_bss()	122	24	Warning: variable may be non-initializ ed (type: const unsigned int 16)	No	Unset	Unreviewed	
31448	Illegally dereferenced pointer	init_data_bss()	129	16	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31444	Illegally dereferenced pointer	init_data_bss()	129	26	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31445	Non-initialized variable	init_data_bss()	129	26	Warning: variable may be non-initializ ed (type: const unsigned int 8)	No	Unset	Unreviewed	
31474	Illegally dereferenced pointer	init_data_bss()	144	15	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31482	Illegally dereferenced pointer	init_data_bss()	150	15	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31381	Illegally dereferenced pointer	init_data_bss_core2()	183	15	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31377	Illegally dereferenced pointer	init_data_bss_core2()	183	24	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31378	Non-initialized variable	init_data_bss_core2()	183	24	Warning: variable may be non-initializ ed (type: const unsigned int 16)	No	Unset	Unreviewed	
31397	Illegally dereferenced pointer	init_data_bss_core2()	189	16	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31393	Illegally dereferenced pointer	init_data_bss_core2()	189	26	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
31394	Non-initialized variable	init_data_bss_core2()	189	26	Warning: variable may be non-initializ ed (type: const unsigned int 8)	No	Unset	Unreviewed	

Table 5.57.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Dma_Ip_Hwv3_Acces sInline.h

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25280	Non-initialized variable	hwv3AccInlineDmaInst_CmdCancelTransfer()	170	25	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25216	Non-initialized variable	hwv3AccInlineDmaInst_Cm dCancelTransferWithError()		25	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
24983	Non-initialized variable	hwv3AccInlineDmaInst_Cm dHalt()	184	25	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
24968	Non-initialized variable	hwv3AccInlineDmaInst_Cm dResume()	191	25	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25291	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Cmd SetRequest()	227	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25037	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Cmd ClearRequest()	234	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25084	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Cmd SwReqStart()	241	22	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25208	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Cmd ClearDone()	248	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25193	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Cmd ClearError()	255	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24937	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetC ontrol_EnMasterIdReplicati on()	286	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
24951	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetC ontrol_EnBufferedWrites()	294	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25052	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetR equest_EnHwRequest()	302	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25332	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetInterrupt_EnError()	308	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25021	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetP riority_Group()	314	17	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25022	Non-initialized variable	hwv3AccInlineDmaCh_SetP riority_Group()	314	17	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25029	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetPriority_Level()	320	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25325	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetP riority_EnPreemption()	327	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25247	Illegally dereferenced pointer	hwv3AccInlineDmaCh_SetP riority_DisPreempt()	335	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25344	Illegally dereferenced pointer	hwv3AccInlineDmaCh_GetS ourceAddress()	536	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25286	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Get DestinationAddress()	541	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25263	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Get BeginIterCount()	546	22	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25138	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Get CurrentIterCount()	559	23	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25242	Illegally dereferenced pointer	hwv3AccInlineDmaCh_GetS toreDstAddress()	573	30	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
24945	Illegally dereferenced pointer	hwv3AccInlineDmaCh_Get MasterId()	580	30	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
24931	Illegally dereferenced pointer	hwv3AccInlineDmaCh_GetIntMajor()	586	31	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25314	Illegally dereferenced pointer	hwv3AccInlineDmaCh_GetIntHalfMajor()	591	31	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

Table 5.58. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21520	Out of bounds array index	Clock_Ip_DistributePll()	990	58	Warning: array index may be outside b ounds : [017]	No	Unset	Unreviewed	
21518	Illegally dereferenced pointer	Clock_Ip_DistributePll()	990	60	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
21517	Out of bounds array index	Clock_Ip_DistributePll()	990	92	Warning: array index may be outside b ounds : [array size undefined]	No	Unset	Unreviewed	
21529	Out of bounds array index	Clock_Ip_DistributePll()	993	91	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21527	Illegally dereferenced pointer	Clock_Ip_DistributePll()	993	93	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
21615	Out of bounds array index	Clock_Ip_DisableClockMonitor()	1036	74	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21626	Out of bounds array index	Clock_Ip_DisableModuleClock()	1066	75	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21603	Out of bounds array index	Clock_Ip_EnableModuleClock()	1082	75	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	

Table 5.59.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Divider.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21827	Out of bounds array index	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	171	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21863	Out of bounds array index	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	180	38	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
21890	Out of bounds array index	Clock_Ip_SetCgmXDeDivSta tWithoutPhase()	205	44	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
21789	Out of bounds array index	Clock_Ip_SetPllPll0divDeDivOutput()	255	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21806	Illegally dereferenced pointer	Clock_Ip_SetPllPll0divDeDivOutput()	262	60	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
21805	Out of bounds array index	Clock_Ip_SetPllPll0divDeDivOutput()	262	69	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
21761	Out of bounds array index	Clock_Ip_SetPllPlldvOdiv2O utput()	288	49	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21773	Illegally dereferenced pointer	Clock_Ip_SetPllPlldvOdiv2O utput()	291	56	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

Table 5.60.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_DividerTrigger.
c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
21938	Out of bounds array index	Clock_Ip_ConfigureCgmXDi vTrigCtrlTctlHhenUpdStat()		49	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	
21961	Out of bounds array index	Clock_Ip_ConfigureCgmXDi vTrigCtrlTctlHhenUpdStat()		27	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
21952	Out of bounds array index	Clock_Ip_ConfigureCgmXDi vTrigCtrlTctlHhenUpdStat()	159	27	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
21969	Out of bounds array index	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUpd Stat()	183	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
21983	Out of bounds array index	Clock_Ip_TriggerUpdateCg mXDivTrigCtrlTctlHhenUpd Stat()	189	27	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	

Table 5.61. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ExtOsc.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22094	Out of bounds array index	Clock_Ip_ResetFxoscOsconB ypEocvGmSel()	178	44	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	
22100	Illegally dereferenced pointer	Clock_Ip_ResetFxoscOsconB ypEocvGmSel()	181	34	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22101	Non-initialized variable	Clock_Ip_ResetFxoscOsconB ypEocvGmSel()	181	36	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22014	Out of bounds array index	Clock_Ip_SetFxoscOsconBy pEocvGmSel()	196	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22151	Out of bounds array index	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	240	44	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	
22169	Illegally dereferenced pointer	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	256	66	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22170	Non-initialized variable	Clock_Ip_CompleteFxoscOs conBypEocvGmSel()	256	68	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22104	Out of bounds array index	Clock_Ip_DisableFxoscOsco nBypEocvGmSel()	288	47	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22110	Illegally dereferenced pointer	Clock_Ip_DisableFxoscOsco nBypEocvGmSel()	291	30	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22111	Non-initialized variable	Clock_Ip_DisableFxoscOsco nBypEocvGmSel()	291	32	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22045	Out of bounds array index	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	299	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22054	Illegally dereferenced pointer	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	304	38	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22055	Non-initialized variable	Clock_Ip_EnableFxoscOsco nBypEocvGmSel()	304	40	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22118	Out of bounds array index	Clock_Ip_ResetSxoscOsconE ocv()	321	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22124	Illegally dereferenced pointer	Clock_Ip_ResetSxoscOsconE ocv()	324	34	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22125	Non-initialized variable	Clock_Ip_ResetSxoscOsconE ocv()	324	36	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22132	Out of bounds array index	Clock_Ip_SetSxoscOsconEoc v()	339	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22062	Out of bounds array index	Clock_Ip_CompleteSxoscOs conEocv()	370	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22077	Illegally dereferenced pointer	Clock_Ip_CompleteSxoscOs conEocv()	380	62	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22078	Non-initialized variable	Clock_Ip_CompleteSxoscOs conEocv()	380	64	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	

Table 5.62. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Gate.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22193	Out of bounds array index	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()		72	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22258	Illegally dereferenced pointer	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()	178	63	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22259	Non-initialized variable	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()	178	80	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22267	Non-initialized variable	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()		73	Warning: variable may be non-initializ ed (type: volatile unsigned int 16)	No	Unset	Unreviewed	
39630	Illegally dereferenced pointer	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()	208	63	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
39631	Non-initialized variable	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()	208	80	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
39638	Non-initialized variable	Clock_Ip_ClockSetGateMcM ePartitionCollectionClockRe quest()	212	73	Warning: variable may be non-initializ ed (type: volatile unsigned int 16)	No	Unset	Unreviewed	

Table 5.63. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Monitor.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22572	Out of bounds array index	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	245	95	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22579	Illegally dereferenced pointer	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	289	24	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22580	Non-initialized variable	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	289	26	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22583	Non-initialized variable	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	295	43	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22595	Non-initialized variable	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	313	11	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22598	Non-initialized variable	Clock_Ip_DisableCmuFcFce RefCntLfrefHfref()	316	11	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22446	Out of bounds array index	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	351	70	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22453	Out of bounds array index	Clock_Ip_SetCmuFcFceRefCntLfrefHfref()	355	19	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22490	Illegally dereferenced pointer	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	28	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22489	Out of bounds array index	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	359	70	Warning: array index may be outside b ounds : [array size undefined]	No	Unset	Unreviewed	
22503	Illegally dereferenced pointer	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	28	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22502	Out of bounds array index	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	360	70	Warning: array index may be outside b ounds : [array size undefined]	No	Unset	Unreviewed	
22548	Division by zero	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	390	47	Warning: scalar division by zero may o ccur	No	Unset	Unreviewed	
22559	Illegally dereferenced pointer	Clock_Ip_SetCmuFcFceRefC ntLfrefHfref()	396	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22570	Illegally dereferenced pointer	Clock_Ip_SetCmuFcFceRefCntLfrefHfref()	407	13	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22387	Out of bounds array index	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	436	71	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22401	Illegally dereferenced pointer	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	442	17	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22402	Non-initialized variable	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	442	19	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22397	Illegally dereferenced pointer	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	446	17	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22398	Non-initialized variable	Clock_Ip_EnableCmuFcFceR efCntLfrefHfref()	446	19	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22436	Out of bounds array index	Clock_Ip_CMU_GetInterrupt Status()	471	33	Warning: array index may be outside b ounds : [06]	No	Unset	Unreviewed	
22438	Illegally dereferenced pointer	Clock_Ip_CMU_GetInterrupt Status()	471	43	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22439	Non-initialized variable	Clock_Ip_CMU_GetInterrupt Status()	471	45	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22407	Illegally dereferenced pointer	Clock_Ip_CMU_ClockFailInt(494	47	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22408	Non-initialized variable	Clock_Ip_CMU_ClockFailInt(494	49	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
22420	Non-initialized variable	Clock_Ip_CMU_ClockFailInt(500	53	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	

Table 5.64. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Pll.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22664	Out of bounds array index	Clock_Ip_ResetPllRdivMfiM fnOdiv2SdmenSsscgbypSpr eadctlStepnoStepsize()	185	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
39713	Illegally dereferenced pointer	Clock_Ip_ResetPllRdivMfiM fnOdiv2SdmenSsscgbypSpr eadctlStepnoStepsize()	193	45	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22692	Out of bounds array index	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSprea dctlStepnoStepsize()	208	44	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	
22701	Illegally dereferenced pointer	Clock_Ip_SetPllRdivMfiMfn Odiv2SdmenSsscgbypSprea dctlStepnoStepsize()	213	57	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22637	Out of bounds array index	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize()	256	47	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22643	Illegally dereferenced pointer	Clock_Ip_CompletePllRdiv MfiMfnOdiv2SdmenSsscgb ypSpreadctlStepnoStepsize()	258	52	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22624	Out of bounds array index	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	289	44	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22633	Illegally dereferenced pointer	Clock_Ip_EnablePllRdivMfi MfnOdiv2SdmenSsscgbypS preadctlStepnoStepsize()	295	49	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

Table 5.65.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22788	Out of bounds array index	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	208	42	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
22785	Out of bounds array index	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	208	68	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
22793	Out of bounds array index	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	209	48	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22797	Out of bounds array index	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	215	19	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
22842	Illegally dereferenced pointer	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	221	21	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22841	Out of bounds array index	Clock_Ip_CgmXPcfsSdurDiv cDiveDivs()	221	63	Warning: array index may be outside b ounds: [array size undefined]	No	Unset	Unreviewed	

Table 5.66.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Selector.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23348	Out of bounds array index	Clock_Ip_ResetCgmXCscCss ClkswSwip()	173	49	Warning: array index may be outside b ounds: [086]	No	Unset	Unreviewed	
23367	Out of bounds array index	Clock_Ip_ResetCgmXCscCss ClkswSwip()	177	23	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
23071	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swSwip()	207	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
23069	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssClk swSwip()	207	56	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
23083	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swSwip()	209	63	Warning: array index may be outside b ounds : [017]	No	Unset	Unreviewed	
39835	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swSwip()	215	46	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
23294	Out of bounds array index	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()	304	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
23313	Out of bounds array index	Clock_Ip_ResetCgmXCscCss ClkswRampupRampdownS wip()	309	23	Warning: array index may be outside b ounds: [00]	No	Unset	Unreviewed	
23386	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip ()	339	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
23384	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip 0	339	56	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23398	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip ()	341	63	Warning: array index may be outside b ounds : [017]	No	Unset	Unreviewed	
39931	Out of bounds array index	Clock_Ip_SetCgmXCscCssClk swRampupRampdownSwip 0	347	46	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
23204	Out of bounds array index	Clock_Ip_ResetCgmXCscCss CsGrip()	446	54	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
23236	Out of bounds array index	Clock_Ip_ResetCgmXCscCss CsGrip()	453	23	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
22962	Out of bounds array index	Clock_Ip_SetCgmXCscCssCs Grip()	511	49	Warning: array index may be outside b ounds : [086]	No	Unset	Unreviewed	
22960	Illegally dereferenced pointer	Clock_Ip_SetCgmXCscCssCs Grip()	511	56	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
22974	Out of bounds array index	Clock_Ip_SetCgmXCscCssCs Grip()	513	63	Warning: array index may be outside b ounds : [017]	No	Unset	Unreviewed	
22994	Out of bounds array index	Clock_Ip_SetCgmXCscCssCs Grip()	518	23	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
23330	Out of bounds array index	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	69	Warning: array index may be outside b ounds : [017]	No	Unset	Unreviewed	
23328	Illegally dereferenced pointer	Clock_Ip_SetRtcRtccClksel_ TrustedCall()	597	76	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

Table 5.67.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23571	Illegally dereferenced pointer	Clock_Ip_CodeInRamSetFla shWaitStates()	397	27	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
23570	Out of bounds array index	Clock_Ip_CodeInRamSetFla shWaitStates()	397	69	Warning: array index may be outside b ounds : [array size undefined]	No	Unset	Unreviewed	
23520	Illegally dereferenced pointer	DisableFircInStandbyMode ()	519	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
23706	Illegally dereferenced pointer	EnableFircInStandbyMode(527	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
23533	Illegally dereferenced pointer	DisableSircInStandbyMode(533	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23529	Illegally dereferenced pointer		541	29	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

Table 5.68. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dem.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23716	Out of bounds array index	Dem_ReportErrorStatusPre ExtData()	246	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23723	Out of bounds array index	Dem_ReportErrorStatusPre ExtData()	255	30	Warning: array index may be outside b ounds: [0126]	No	Unset	Unreviewed	

Table 5.69. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dem_stub.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23845	Out of bounds array index	Dem_TestLastReportErrorS tatus()	114	41	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
23850	Out of bounds array index	Dem_TestLastReportErrorS tatus()	117	43	Warning: array index may be outside b ounds : [0126]	No	Unset	Unreviewed	
23930	Out of bounds array index	Dem_TestNoError()	151	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23921	Out of bounds array index	Dem_TestNoError()	153	68	Warning: array index may be outside b ounds : [0126]	No	Unset	Unreviewed	
23770	Out of bounds array index	Dem_TestLastReportErrorS tatusPreExtData()	184	41	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23775	Out of bounds array index	Dem_TestLastReportErrorS tatusPreExtData()	187	50	Warning: array index may be outside b ounds : [0126]	No	Unset	Unreviewed	
23795	Out of bounds array index	Dem_TestLastReportErrorS tatusPreExtData()	196	80	Warning: array index may be outside b ounds : [099]	No	Unset	Unreviewed	
23892	Out of bounds array index	Dem_ClearEvents()	234	22	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23895	Out of bounds array index	Dem_GetEvent()	253	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23901	Out of bounds array index	Dem_GetEvent()	255	41	Warning: array index may be outside b ounds : [0126]	No	Unset	Unreviewed	
23915	Out of bounds array index	Dem_GetEventCount()	277	29	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
23841	Out of bounds array index	Dem_BufferOverflow()	298	28	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

Table 5.70. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Det.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
23963	Out of bounds array index	Det_ReportError()	214	16	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23950	Out of bounds array index	Det_ReportTransientFault()	268	25	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

Table 5.71. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Det_stub.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24094	Out of bounds array index	Det_TestLastReportError()	111	26	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
24046	Out of bounds array index	Det_TestLastReportRuntim eError()	144	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
24020	Out of bounds array index	Det_TestLastReportTransie ntFault()	176	35	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
24072	Out of bounds array index	Det_TestNoError()	198	25	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23998	Out of bounds array index	Det_TestNoRuntimeError()	219	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
23976	Out of bounds array index	Det_TestNoTransientFault()	240	34	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

Table 5.72. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24605	Out of bounds array index	Dma_Ip_SetHwChannel_Re setEvent()	144	42	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24606	Out of bounds array index	Dma_Ip_SetHwChannel_Re setEvent()	144	53	Warning: array index may be outside b ounds: [031]	No	Unset	Unreviewed	
24627	Out of bounds array index	Dma_Ip_SetHwChannel_Re adyEvent()	157	42	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24628	Out of bounds array index	Dma_Ip_SetHwChannel_Re adyEvent()	157	53	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24612	Out of bounds array index	Dma_Ip_SetHwChannel_Tra nsferEvent()	170	42	Warning: array index may be outside b ounds: [00]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24613	Out of bounds array index	Dma_Ip_SetHwChannel_Tra nsferEvent()	170	53	Warning: array index may be outside b ounds: [031]	No	Unset	Unreviewed	
24634	Out of bounds array index	Dma_Ip_SetHwChannel_Sca tterGatherEvent()	183	42	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24635	Out of bounds array index	Dma_Ip_SetHwChannel_Sca tterGatherEvent()	183	53	Warning: array index may be outside b ounds: [031]	No	Unset	Unreviewed	
24641	Out of bounds array index	Dma_Ip_SetHwChannel_Err orEvent()	196	42	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24642	Out of bounds array index	Dma_Ip_SetHwChannel_Err orEvent()	196	53	Warning: array index may be outside b ounds: [031]	No	Unset	Unreviewed	

Table 5.73.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Hw_Access.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24708	Out of bounds array index	hwAccDmaInst_CmdCancel Transfer()	366	73	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24899	Out of bounds array index	hwAccDmaInst_CmdCancel TransferWithError()	387	82	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24796	Out of bounds array index	hwAccDmaInst_CmdHalt()	408	63	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24689	Out of bounds array index	hwAccDmaInst_CmdResum e()	429	65	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24666	Out of bounds array index	HwAccDmaCh_CmdSetHwR equest()	493	65	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24667	Out of bounds array index	HwAccDmaCh_CmdSetHwR equest()	493	76	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24675	Out of bounds array index	HwAccDmaCh_CmdClearH wRequest()	511	67	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24676	Out of bounds array index	HwAccDmaCh_CmdClearH wRequest()	511	78	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24853	Out of bounds array index	HwAccDmaCh_CmdSetSwR equest()	529	65	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24854	Out of bounds array index	HwAccDmaCh_CmdSetSwR equest()	529	76	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24816	Out of bounds array index	HwAccDmaCh_CmdClearDo ne()	547	64	Warning: array index may be outside b ounds: [00]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24817	Out of bounds array index	HwAccDmaCh_CmdClearDo ne()	547	75	Warning: array index may be outside b ounds: [031]	No	Unset	Unreviewed	
24810	Out of bounds array index	HwAccDmaCh_CmdClearEr ror()	565	65	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24811	Out of bounds array index	HwAccDmaCh_CmdClearEr ror()	565	76	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24920	Out of bounds array index	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	84	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24921	Out of bounds array index	HwAccDmaCh_SetControl_ EnMasterIdReplication()	644	95	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24784	Out of bounds array index	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	79	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24785	Out of bounds array index	HwAccDmaCh_SetControl_ EnBufferedWrites()	664	90	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24713	Out of bounds array index	HwAccDmaCh_SetRequest_ EnRequest()	683	74	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24714	Out of bounds array index	HwAccDmaCh_SetRequest_ EnRequest()	683	85	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24761	Out of bounds array index	HwAccDmaCh_SetInterrupt _EnError()	700	72	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24762	Out of bounds array index	HwAccDmaCh_SetInterrupt _EnError()	700	83	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24777	Out of bounds array index	HwAccDmaCh_SetPriority_ Group()	739	91	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24836	Out of bounds array index	HwAccDmaCh_SetPriority_ Level()	758	69	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24837	Out of bounds array index	HwAccDmaCh_SetPriority_ Level()	758	80	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24754	Out of bounds array index	HwAccDmaCh_SetPriority_ EnPreemption()	777	76	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24755	Out of bounds array index	HwAccDmaCh_SetPriority_ EnPreemption()	777	87	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24870	Out of bounds array index	HwAccDmaCh_SetPriority_ DisPreempt()	797	74	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24871	Out of bounds array index	HwAccDmaCh_SetPriority_ DisPreempt()	797	85	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
24822	Out of bounds array index	HwAccDmaCh_GetSourceA ddress()	1408	68	Warning: array index may be outside b ounds: [00]	No	Unset	Unreviewed	
24823	Out of bounds array index	HwAccDmaCh_GetSourceA ddress()	1408	79	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24657	Out of bounds array index	HwAccDmaCh_GetDestinati onAddress()	1433	73	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24658	Out of bounds array index	HwAccDmaCh_GetDestinati onAddress()	1433	84	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24904	Out of bounds array index	HwAccDmaCh_GetBeginIte rCount()	1456	69	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24905	Out of bounds array index	HwAccDmaCh_GetBeginIte rCount()	1456	80	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24744	Out of bounds array index	HwAccDmaCh_GetCurrentI terCount()	1474	71	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24745	Out of bounds array index	HwAccDmaCh_GetCurrentI terCount()	1474	82	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24723	Out of bounds array index	HwAccDmaCh_GetStoreDst Address()	1496	70	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24724	Out of bounds array index	HwAccDmaCh_GetStoreDst Address()	1496	81	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24727	Illegally dereferenced pointer	HwAccDmaCh_GetStoreDst Address()	1500	24	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
24728	Non-initialized variable	HwAccDmaCh_GetStoreDst Address()	1500	24	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
24887	Out of bounds array index	HwAccDmaCh_GetMasterId	1521	63	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24888	Out of bounds array index	HwAccDmaCh_GetMasterId	1521	74	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24880	Out of bounds array index	HwAccDmaCh_GetIntMajor	1540	63	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24881	Out of bounds array index	HwAccDmaCh_GetIntMajor	1540	74	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	
24737	Out of bounds array index	HwAccDmaCh_GetIntHalfM ajor()	1558	67	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
24738	Out of bounds array index	HwAccDmaCh_GetIntHalfM ajor()	1558	78	Warning: array index may be outside b ounds : [031]	No	Unset	Unreviewed	

Table 5.74. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Irq.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25354	Illegally dereferenced pointer	IrqHandler()	362	73	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
25355	Non-initialized variable	IrqHandler()	362	83	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25362	Non-initialized variable	IrqHandler()	363	83	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25369	Non-initialized variable	IrqHandler()	364	84	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25376	Non-initialized variable	IrqHandler()	365	83	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	

Table 5.75.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Emios_Mcl_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25461	Out of bounds array index	Emios_Mcl_Ip_Init()	187	55	Warning: array index may be outside b ounds: [01]	No	Unset	Unreviewed	
25468	Non-initialized variable	Emios_Mcl_Ip_Init()	203	18	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25861	Out of bounds array index	Emios_Mcl_Ip_EnableChan nel()	288	39	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25865	Non-initialized variable	Emios_Mcl_Ip_EnableChan nel()	289	10	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25867	Invalid shift operations	Emios_Mcl_Ip_EnableChan nel()	289	39	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
25452	Out of bounds array index	Emios_Mcl_Ip_DisableChan nel()	300	39	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25456	Non-initialized variable	Emios_Mcl_Ip_DisableChan nel()	301	10	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25458	Invalid shift operations	Emios_Mcl_Ip_DisableChan nel()	301	38	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
25869	Out of bounds array index	Emios_Mcl_Ip_Comparator TransferEnable()	312	39	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25873	Non-initialized variable	Emios_Mcl_Ip_Comparator TransferEnable()	314	10	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25854	Out of bounds array index	Emios_Mcl_Ip_Comparator TransferDisable()	325	39	Warning: array index may be outside b ounds: [01]	No	Unset	Unreviewed	
25858	Non-initialized variable	Emios_Mcl_Ip_Comparator TransferDisable()	327	10	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25783	Out of bounds array index	Emios_Mcl_Ip_Deinit()	339	55	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25775	Out of bounds array index	Emios_Mcl_Ip_SetReloadInt erval()	398	20	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25780	Out of bounds array index	Emios_Mcl_Ip_SetReloadInt erval()	398	39	Warning: array index may be outside b ounds : [023]	No	Unset	Unreviewed	
25743	Out of bounds array index	Emios_Mcl_Ip_ValidateCha nnel()	411	31	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25744	Out of bounds array index	Emios_Mcl_Ip_ValidateCha nnel()	411	43	Warning: array index may be outside b ounds : [023]	No	Unset	Unreviewed	
25750	Out of bounds array index	Emios_Mcl_Ip_SetCounterB usPeriod()	432	29	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25751	Out of bounds array index	Emios_Mcl_Ip_SetCounterB usPeriod()	432	41	Warning: array index may be outside b ounds : [023]	No	Unset	Unreviewed	
25877	Out of bounds array index	Emios_Mcl_Ip_GetCounterB usPeriod()	468	56	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
25878	Out of bounds array index	Emios_Mcl_Ip_GetCounterB usPeriod()	468	66	Warning: array index may be outside b ounds : [023]	No	Unset	Unreviewed	
25844	Out of bounds array index	Emios_Mcl_Ip_ConfigureGl obalTimebase()	497	39	Warning: array index may be outside b ounds: [01]	No	Unset	Unreviewed	
25852	Non-initialized variable	Emios_Mcl_Ip_ConfigureGl obalTimebase()	501	14	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25849	Non-initialized variable	Emios_Mcl_Ip_ConfigureGl obalTimebase()	505	14	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	

Table 5.76.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25891	Out of bounds array index	Flexio_Mcl_Ip_InitDevice()	148	32	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
25904	Out of bounds array index	Flexio_Mcl_Ip_DeinitDevice ()	176	32	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	

Table 5.77.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip_HwAccess.

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
25934	Non-initialized variable	Flexio_Mcl_Ip_SetDebugEn able()	176	40	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
26029	Non-initialized variable	Flexio_Mcl_Ip_SetEnable()	190	40	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
25993	Invalid shift operations	Flexio_Mcl_Ip_GetShifterSt atus()	209	35	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
25953	Invalid shift operations	Flexio_Mcl_Ip_ClearShifter Status()	234	30	Warning: scalar shift amount may be o utside its bounds[031]	No	Unset	Unreviewed	
26009	Invalid shift operations	Flexio_Mcl_Ip_GetShifterEr rorStatus()	248	34	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
26015	Invalid shift operations	Flexio_Mcl_Ip_ClearShifter ErrorStatus()	273	29	Warning: scalar shift amount may be o utside its bounds[031]	No	Unset	Unreviewed	
25945	Invalid shift operations	Flexio_Mcl_Ip_GetTimerStatus()	289	33	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
25930	Invalid shift operations	Flexio_Mcl_Ip_GetTimerInt erruptEnable()	295	32	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
26011	Invalid shift operations	Flexio_Mcl_Ip_ClearTimerS tatus()	320	28	Warning: scalar shift amount may be o utside its bounds[031]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26461	Out of bounds array index	I2c_Ipw_SlaveCallback()	154	31	Warning: array index may be outside b ounds : [05]	No	Unset	Unreviewed	
26463	Out of bounds array index	I2c_Ipw_SlaveCallback()	159	31	Warning: array index may be outside b ounds : [05]	No	Unset	Unreviewed	

Table 5.79. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpi2c_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26472	Out of bounds array index	Lpi2c_Ip_MasterSetBaudRa te()	1710	30	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
26593	Out of bounds array index	Lpi2c_Ip_MasterGetBaudRa	2008	30	Warning: array index may be outside b	No	Unset	Unreviewed	
		te()			ounds : [01]				

Table 5.80. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
2688	Out of bounds array index	Mcu_InitClock()	849	54	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	
2686	Out of bounds array index	Mcu_SetMode()	902	48	Warning: array index may be outside b ounds : [00]	No	Unset	Unreviewed	

Table 5.81. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Pit_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27111	Out of bounds array index	Pit_Ip_SetCounterValue()	658	15	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
27091	Out of bounds array index	Pit_Ip_GetUpperLifetimerV alue()	784	24	Warning: array index may be outside b ounds: [01]	No	Unset	Unreviewed	

Table 5.82. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_RGM.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
36419	Invalid shift operations	Power_Ip_MC_RGM_CheckR esetReason()	484	37	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
27254	Invalid shift operations	Power_Ip_MC_RGM_GetRes etRawValue()	642	41	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
27275	Invalid shift operations	Power_Ip_MC_RGM_GetRes etRawValue()	681	41	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
27281	Invalid shift operations	Power_Ip_MC_RGM_GetRes etRawValue()	686	47	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	

Table 5.83. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Ram_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27485	Division by zero	Ram_Ip_InitRamSection()	284	90	Warning: scalar division by zero may o ccur	No	Unset	Unreviewed	
27496	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	294	18	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27504	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	299	18	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27512	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	304	18	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27520	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	309	18	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27536	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	329	69	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27537	Non-initialized variable	Ram_Ip_InitRamSection()	329	69	Warning: variable may be non-initializ ed (type: unsigned int 8)	No	Unset	Unreviewed	
27545	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	337	69	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27546	Non-initialized variable	Ram_Ip_InitRamSection()	337	69	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
27554	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	345	69	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27555	Non-initialized variable	Ram_Ip_InitRamSection()	345	69	Warning: variable may be non-initializ ed (type: unsigned int 16)	No	Unset	Unreviewed	
27563	Illegally dereferenced pointer	Ram_Ip_InitRamSection()	353	69	Warning: pointer may be outside its bo unds	No	Unset	Unreviewed	
27564	Non-initialized variable	Ram_Ip_InitRamSection()	353	69	Warning: variable may be non-initializ ed (type: unsigned int 32)	No	Unset	Unreviewed	

Table 5.84. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Gpt.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28683	Non-initialized local varia ble	Gpt_schm_read_msr()	421	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
28606	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_00()	496	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28611	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_00()	510	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28537	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_00()	519	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28571	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_01()	534	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28576	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_01()	548	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
28832	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_01()	557	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28467	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_02()	572	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28472	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_02()	586	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28526	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_02()	595	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28419	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_03()	610	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28424	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_03()	624	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27820	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_03()	633	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28339	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_04()	648	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28344	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_04()	662	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28548	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_04()	671	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28269	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_05()	686	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28274	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_05()	700	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27831	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_05()	709	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28222	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_06()	724	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28227	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_06()	738	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28630	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_06()	747	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28198	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_07()	762	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28203	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_07()	776	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
27925	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_07()	785	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27901	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_10()	800	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27906	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_10()	814	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28164	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_10()	823	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27853	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_11()	838	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27858	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_11()	852	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27787	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_11()	861	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27763	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_12()	876	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27768	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_12()	890	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28175	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_12()	899	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28820	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_17()	914	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28825	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_17()	928	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28809	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_17()	937	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28559	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_21()	952	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28564	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_21()	966	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
28641	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_21()	975	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28479	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_22()	990	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28484	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_22()	1004	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
27936	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_22()	1013	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28878	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_23()	1028	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28883	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_23()	1042	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28652	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_23()	1051	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28351	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_24()	1066	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28356	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_24()	1080	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27947	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_24()	1089	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28013	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_25()	1104	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28018	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_25()	1118	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28775	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_25()	1127	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28234	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_26()	1142	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28239	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_26()	1156	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27958	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_26()	1165	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28186	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_27()	1180	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28191	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_27()	1194	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28663	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_27()	1203	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28152	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_28()	1218	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28157	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_28()	1232	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
27969	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_28()	1241	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28104	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_29()	1256	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28109	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_29()	1270	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28674	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_29()	1279	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27913	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_30()	1294	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27918	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_30()	1308	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28697	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_30()	1317	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27865	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_31()	1332	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27870	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_31()	1346	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28305	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_31()	1355	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28902	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_35()	1370	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28907	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_35()	1384	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28316	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_35()	1393	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28855	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_36()	1408	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28860	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_36()	1422	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28926	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_36()	1431	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28797	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_38()	1446	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28802	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_38()	1460	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
28937	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_38()	1469	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28890	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_39()	1484	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28895	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_39()	1498	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28503	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_39()	1507	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28618	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_40()	1522	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28623	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_40()	1536	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28786	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_40()	1545	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27727	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_41()	1560	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27732	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_41()	1574	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27980	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_41()	1583	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28491	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_42()	1598	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28496	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_42()	1612	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27842	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_42()	1621	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28092	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_43()	1636	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28097	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_43()	1650	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27991	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_43()	1659	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28363	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_44()	1674	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28368	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_44()	1688	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28408	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_44()	1697	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28281	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_45()	1712	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28286	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_45()	1726	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28002	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_45()	1735	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28246	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_46()	1750	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28251	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_46()	1764	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28686	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_46()	1773	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28443	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_50()	1788	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28448	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_50()	1802	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28386	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_50()	1811	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27877	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_51()	1826	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27882	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_51()	1840	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27694	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_51()	1849	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28763	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_52()	1864	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28768	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_52()	1878	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28375	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_52()	1887	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27739	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_53()	1902	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
27744	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_53()	1916	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27705	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_53()	1925	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27682	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_54()	1940	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27687	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_54()	1954	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28583	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_54()	1963	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28914	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_55()	1978	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28919	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_55()	1992	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27716	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_55()	2001	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28843	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_56()	2016	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28848	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_56()	2030	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28397	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_56()	2039	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28594	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_60()	2054	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28599	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_60()	2068	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28037	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_60()	2077	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28128	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_61()	2092	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
28133	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_61()	2106	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28719	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_61()	2115	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28455	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_62()	2130	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28460	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_62()	2144	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28048	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_62()	2153	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28431	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_63()	2168	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28436	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_63()	2182	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28708	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_63()	2191	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28327	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_64()	2206	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28332	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_64()	2220	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28059	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_64()	2229	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28293	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_65()	2244	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28298	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_65()	2258	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28730	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_65()	2267	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28514	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_66()	2282	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28519	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_66()	2296	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28070	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_66()	2305	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28210	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_67()	2320	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28215	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_67()	2334	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28741	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_67()	2343	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28140	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_68()	2358	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
28145	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_68()	2372	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28258	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_68()	2381	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28116	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_69()	2396	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28121	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_69()	2410	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28752	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_69()	2419	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28025	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_70()	2434	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28030	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_70()	2448	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27798	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_70()	2457	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27889	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_71()	2472	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27894	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_71()	2486	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28867	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_71()	2495	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27775	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_72()	2510	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27780	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_72()	2524	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27809	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_72()	2533	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27751	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_73()	2548	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
27756	Out of bounds array index	SchM_Enter_Gpt_GPT_EXCL USIVE_AREA_73()	2562	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
28081	Out of bounds array index	SchM_Exit_Gpt_GPT_EXCLU SIVE_AREA_73()	2571	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

Table 5.85. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcl.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29611	Non-initialized local varia ble	Mcl_schm_read_msr()	405	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
29601	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	594	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29606	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_03()	608	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29118	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_03()	617	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29381	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	860	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29386	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_10()	874	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29348	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_10()	883	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29221	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	974	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29226	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_13()	988	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29787	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_13()	997	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29197	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1012	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29202	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_14()	1026	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29731	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_14()	1035	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29129	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1050	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29134	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_15()	1064	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29798	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_15()	1073	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29106	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1088	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29111	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_16()	1102	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29440	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_16()	1111	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29404	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1126	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29409	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_17()	1140	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29776	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_17()	1149	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29082	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1164	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29087	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_18()	1178	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29337	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_18()	1187	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29695	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1202	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29700	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_19()	1216	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29765	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_19()	1225	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29719	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1240	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29724	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_20()	1254	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29174	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_20()	1263	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29683	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1278	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29688	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_21()	1292	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29393	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_21()	1301	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29626	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1316	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29631	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_22()	1330	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
29163	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_22()	1339	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29589	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1354	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29594	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_23()	1368	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29531	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_23()	1377	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29565	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1392	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29570	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_24()	1406	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29152	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_24()	1415	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29497	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1430	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29502	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_25()	1444	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29520	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_25()	1453	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29058	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1468	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29063	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_26()	1482	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29141	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_26()	1491	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29462	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1506	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
29467	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_27()	1520	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29542	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_27()	1529	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29302	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1544	49	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29307	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_28()	1558	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29451	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_28()	1567	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29416	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1582	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29421	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_29()	1596	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29509	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_29()	1605	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29290	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1620	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29295	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_30()	1634	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29853	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_30()	1643	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29278	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1658	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29283	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_31()	1672	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29474	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_31()	1681	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29233	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1696	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29238	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_32()	1710	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29831	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_32()	1719	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29209	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1734	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29214	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_33()	1748	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29742	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_33()	1757	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29185	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1772	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29190	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_34()	1786	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29820	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_34()	1795	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29428	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1810	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29433	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_35()	1824	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29267	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_35()	1833	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29094	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1848	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29099	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_36()	1862	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29809	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_36()	1871	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29753	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1886	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29758	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_37()	1900	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29359	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_37()	1909	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29070	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1924	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29075	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_38()	1938	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29842	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_38()	1947	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29046	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1962	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29051	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_39()	1976	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29370	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_39()	1985	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29707	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2000	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29712	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_40()	2014	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
29671	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2038	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29676	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_41()	2052	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29256	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_41()	2061	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29614	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2076	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29619	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_42()	2090	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29660	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_42()	2099	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29577	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2114	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29582	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_43()	2128	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29245	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_43()	2137	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29553	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2152	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29558	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_44()	2166	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29649	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_44()	2175	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29485	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2190	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29490	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_45()	2204	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29314	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_45()	2213	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29325	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2228	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29330	Out of bounds array index	SchM_Enter_Mcl_MCL_EXC LUSIVE_AREA_46()	2242	33	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29638	Out of bounds array index	SchM_Exit_Mcl_MCL_EXCL USIVE_AREA_46()	2251	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

Table 5.86. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcu.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
29868	Non-initialized local varia ble	Mcu_schm_read_msr()	317	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
29906	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	392	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29911	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_00()	406	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29894	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	430	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29899	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_01()	444	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29918	Out of bounds array index	SchM_Exit_Mcu_MCU_EXCL USIVE_AREA_01()	453	39	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29882	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	468	49	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29887	Out of bounds array index	SchM_Enter_Mcu_MCU_EX CLUSIVE_AREA_02()	482	33	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29871	Out of bounds array index	SchM_Exit_Mcu_MCU_EXCL USIVE_AREA_02()	491	39	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

Table 5.87. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Port.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30444	Non-initialized local varia ble	Port_schm_read_msr()	367	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
30342	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_01()	480	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30347	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_01()	494	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29997	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_01()	503	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30180	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_02()	518	50	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30185	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_02()	532	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30307	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_03()	556	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30312	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_03()	570	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30400	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_04()	594	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30405	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_04()	608	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30239	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_04()	617	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30157	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_08()	746	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30162	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_08()	760	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30354	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_08()	769	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30504	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_09()	784	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30509	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_09()	798	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30424	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_09()	807	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30065	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_10()	822	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30070	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_10()	836	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30447	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_10()	845	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30145	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_11()	860	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30150	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_11()	874	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30481	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_11()	883	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30273	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_12()	898	50	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30278	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_12()	912	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30008	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_12()	921	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30215	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_13()	936	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30220	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_13()	950	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30296	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_13()	959	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30458	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_14()	974	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30463	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_14()	988	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30099	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_14()	997	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29985	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_15()	1012	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
29990	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_15()	1026	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30019	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_15()	1035	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30053	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_16()	1050	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30058	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_16()	1064	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30192	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_16()	1073	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30133	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_17()	1088	50	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30138	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_17()	1102	34	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30285	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_17()	1111	40	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30250	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_18()	1126	50	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30255	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_18()	1140	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30331	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_18()	1149	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30377	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_19()	1164	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30382	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_19()	1178	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30389	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_19()	1187	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30365	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_20()	1202	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30370	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_20()	1216	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30435	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_20()	1225	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30227	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_21()	1240	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30232	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_21()	1254	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30470	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_21()	1263	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30319	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_22()	1278	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30324	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_22()	1292	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30077	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_22()	1301	40	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30412	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_23()	1316	50	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30417	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_23()	1330	34	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	
30042	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_23()	1339	40	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30492	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_24()	1354	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30497	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_24()	1368	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30088	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_24()	1377	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30030	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_25()	1392	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30035	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_25()	1406	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30122	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_25()	1415	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30110	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_26()	1430	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30115	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_26()	1444	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30169	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_26()	1453	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30203	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_27()	1468	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30208	Out of bounds array index	SchM_Enter_Port_PORT_EX CLUSIVE_AREA_27()	1482	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30262	Out of bounds array index	SchM_Exit_Port_PORT_EXC LUSIVE_AREA_27()	1491	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

Table 5.88. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Rm.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30699	Non-initialized local varia ble	Rm_schm_read_msr()	337	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
30807	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	412	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30812	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_00()	426	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30666	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_00()	435	38	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30714	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	450	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30719	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_01()	464	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30553	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_01()	473	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30677	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	488	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30682	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_02()	502	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30819	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_02()	511	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30575	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	526	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30580	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_03()	540	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30749	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_03()	549	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30830	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	564	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30835	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_04()	578	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30644	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_04()	587	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30737	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	602	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30742	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_05()	616	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30655	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_05()	625	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30702	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	640	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30707	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_06()	654	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30599	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_06()	663	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30621	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	678	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30626	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_07()	692	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30542	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_07()	701	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30784	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	716	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30789	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_08()	730	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30796	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_08()	739	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30772	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	754	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30777	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_09()	768	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30726	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_09()	777	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30760	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	792	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30765	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_10()	806	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30610	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_10()	815	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30689	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	830	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30694	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_11()	844	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30633	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_11()	853	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30587	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	868	48	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30592	Out of bounds array index	SchM_Enter_Rm_RM_EXCL USIVE_AREA_12()	882	32	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30564	Out of bounds array index	SchM_Exit_Rm_RM_EXCLU SIVE_AREA_12()	891	38	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	

Table 5.89. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Uart.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
30903	Non-initialized local varia ble	Uart_schm_read_msr()	329	19	Warning: local variable may be non-ini tialized (type: unsigned int 16)	No	Unset	Unreviewed	
30906	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_06()	632	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30911	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_06()	646	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30871	Out of bounds array index	SchM_Exit_Uart_UART_EXC LUSIVE_AREA_06()	655	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30882	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_07()	670	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30887	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_07()	684	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30860	Out of bounds array index	SchM_Exit_Uart_UART_EXC LUSIVE_AREA_07()	693	40	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30918	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_08()	708	50	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30923	Out of bounds array index	SchM_Enter_Uart_UART_EX CLUSIVE_AREA_08()	722	34	Warning: array index may be outside b ounds : [03]	No	Unset	Unreviewed	
30894	Out of bounds array index	SchM_Exit_Uart_UART_EXC LUSIVE_AREA_08()	731	40	Warning: array index may be outside b ounds: [03]	No	Unset	Unreviewed	

Table 5.90.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31121	Out of bounds array index	Siul2_Port_Ip_SetPullSel()	693	22	Warning: array index may be outside b ounds : [015]	No	Unset	Unreviewed	
31128	Out of bounds array index	Siul2_Port_Ip_SetPullSel()	699	31	Warning: array index may be outside b ounds : [015]	No	Unset	Unreviewed	
31142	Out of bounds array index	Siul2_Port_Ip_SetPullSel()	709	31	Warning: array index may be outside b ounds : [015]	No	Unset	Unreviewed	
31091	Out of bounds array index	Siul2_Port_Ip_SetOutputBuf fer()	747	14	Warning: array index may be outside b ounds : [015]	No	Unset	Unreviewed	
31002	Out of bounds array index	Siul2_Port_Ip_SetInputBuff er()	785	18	Warning: array index may be outside b ounds : [015]	No	Unset	Unreviewed	

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31046	Out of bounds array index	Siul2_Port_Ip_SetPinDirecti on()	842	18	Warning: array index may be outside b ounds: [015]	No	Unset	Unreviewed	
31062	Out of bounds array index	Siul2_Port_Ip_SetPinDirecti on()	847	18	Warning: array index may be outside b ounds: [015]	No	Unset	Unreviewed	
31078	Out of bounds array index	Siul2_Port_Ip_SetPinDirecti on()	852	18	Warning: array index may be outside b ounds: [015]	No	Unset	Unreviewed	
31084	Out of bounds array index	Siul2_Port_Ip_SetPinDirecti on()	856	18	Warning: array index may be outside b ounds: [015]	No	Unset	Unreviewed	

Table 5.91. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Tspc_Port_Ip.c

ID	Check	Function	Line	Col	Detail	Jus	Severity	Status	Comment
31236	Out of bounds array index	Tspc_Port_Ip_ConfigureObe Group()	192	35	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
31243	Invalid shift operations	Tspc_Port_Ip_ConfigureObe Group()	192	100	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	
31222	Out of bounds array index	Tspc_Port_Ip_ConfigureObe Group()	196	35	Warning: array index may be outside b ounds : [01]	No	Unset	Unreviewed	
31229	Invalid shift operations	Tspc_Port_Ip_ConfigureObe Group()	196	100	Warning: scalar shift amount may be o utside its bounds[015]	No	Unset	Unreviewed	

Chapter 6. Global Variables

Variable Checks for: I2C_MCAL_FunctionTest_20250103 - CP_Result

Unused variable

Table 6.1.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Dma_Ip_Hw_Access. h

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41452	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.g_ptHwChT		144	52	Unset	Unreview	
	cdPtrArray'.					ed	

Table 6.2. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_I2c.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38696	There exists no reachable operation on variable 'CDD_I2c.I2c_as8PartitionHardw areMap'.		242	6	Unset	Unreview ed	

Table 6.3. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Uart.c

\mathbf{II}	D	Detail	Protection	Line	Col	Severity	Status	Comment
3	8992	There exists no reachable operation on variable 'CDD_Uart.Uart_au32ClockFrequency'.		124	14	Unset	Unreview ed	
3	8991	There exists no reachable operation on variable 'CDD_Uart.Uart_apConfig'.		158	31	Unset	Unreview ed	

Table 6.4. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39148	There exists no reachable operation on variable 'Clock_Ip.FunctionWasCalled'.		133	15		Unreview ed	

Table 6.5.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Data.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39551	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8DividerCallbackIndex'.		892	12	Unset	Unreview ed	
39545	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8DividerTriggerCallbackIndex'.		903	12	Unset	Unreview ed	
39575	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8Xos cCallbackIndex'.		914	12	Unset	Unreview ed	
39558	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8Fra ctionalDividerCallbackIndex'.		955	12	Unset	Unreview ed	
39573	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8PllC allbackIndex'.		966	12	Unset	Unreview ed	
39568	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_au8Pcfs CallbackIndex'.		992	12	Unset	Unreview ed	
39557	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_apxMc MeGetPartitions'.		3554	52	Unset	Unreview ed	
39572	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_aeHwPl lName'.		3654	24	Unset	Unreview ed	
39570	There exists no reachable operation on variable 'Clock_Ip_Data.Clock_Ip_aeHwD fsName'.		3662	24	Unset	Unreview ed	

Table 6.6. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Divider.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39583	There exists no reachable operation on variable 'Clock_Ip_Divider.Clock_Ip_axDi viderCallbacks'.		326	35	Unset	Unreview ed	

Table 6.7. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_DividerTrigger. c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39606	There exists no reachable operation on variable 'Clock_Ip_DividerTrigger.Clock_I p_axDividerTriggerCallbacks'.		237	42	Unset	Unreview ed	

Table 6.8. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ExtOsc.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39614	There exists no reachable operation on variable 'Clock_Ip_ExtOsc.Clock_Ip_axExt		441	34	Unset	Unreview	
	OscCallbacks'.					ed	

Table 6.9. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_FracDiv.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39618	There exists no reachable operation on variable 'Clock_Ip_FracDiv.Clock_Ip_axFr acDivCallbacks'.		162	35	Unset	Unreview ed	

Table 6.10. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Pll.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39701	There exists no reachable operation on variable 'Clock_Ip_Pll.Clock_Ip_axPllCallb acks'.		439	31		Unreview ed	

Table 6.11. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39726	There exists no reachable operation on variable 'Clock_Ip_ProgFreqSwitch.AMax '.		113	20	Unset	Unreview ed	
39724	There exists no reachable operation on variable 'Clock_Ip_ProgFreqSwitch.PcfsR ate'.		114	20	Unset	Unreview ed	
39722	There exists no reachable operation on variable 'Clock_Ip_ProgFreqSwitch.Clock_Ip_axPcfsCallbacks'.		340	32	Unset	Unreview ed	

Table 6.12. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40002	There exists no reachable operation on variable 'Clock_Ip_Specific.Clock_Ip_SetFl ashWaitStatesCallback'.		163	38	Unset	Unreview ed	

Table 6.13. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40290	There exists no reachable operation on variable 'Dma_Ip_Driver_State.fpa_Dma_		205	25	Unset	Unreview	
	Ip_HwChannelState'.					ed	

Table 6.14. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Hw_Access.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40297	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.fpHwAcc_D maInst_Command'.		445	25	Unset	Unreview ed	
40294	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.fpHwAcc_D maCh_Command'.		581	25	Unset	Unreview ed	
40295	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.fpHwAcc_D maCh_SetGlobalParam'.		814	25	Unset	Unreview ed	
40296	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.fpHwAcc_D maCh_SetTransferParam'.		1352	25	Unset	Unreview ed	
40293	There exists no reachable operation on variable 'Dma_Ip_Hw_Access.fpHwAcc_D maCh_GetParam'.		1635	25	Unset	Unreview ed	

Table 6.15. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Mux_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40341	There exists no reachable operation on variable 'Dma_Mux_Ip.Dma_Mux_Ip_Bas ePointer'.		91	21	Unset	Unreview ed	

Table 6.16. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Uart_Ip.c

1	D	Detail	Protection	Line	Col	Severity	Status	Comment
4	10591	There exists no reachable operation on variable 'Flexio_Uart_Ip.Flexio_Uart_Ip_a pStateStructure'.		171	34	Unset	Unreview ed	
4	10590	There exists no reachable operation on variable 'Flexio_Uart_Ip.Flexio_Uart_Ip_a pUserConfig'.		195	38	Unset	Unreview ed	

Table 6.17. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Gpt.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40592	There exists no reachable operation on variable 'Gpt.Gpt_eMode'.		189	29	Unset	Unreview ed	
40595	There exists no reachable operation on variable 'Gpt.Gpt_aChannelInfo'.		207	27	Unset	Unreview ed	
40593	There exists no reachable operation on variable 'Gpt.Gpt_aStopTime'.		213	30	Unset	Unreview ed	

Table 6.18. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Port_Ipw.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41007	There exists no reachable operation on variable 'Port_Ipw.Port_Ipw_au16GPIODi rChangeability'.		184	14	Unset	Unreview ed	

Table 6.19. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_I2c.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41176	There exists no reachable operation on variable 'SchM_I2c.msr_I2C_EXCLUSIVE_AREA_00'.		119	23	Unset	Unreview ed	
41174	There exists no reachable operation on variable 'SchM_I2c.reentry_guard_I2C_E XCLUSIVE_AREA_00'.		120	23	Unset	Unreview ed	
41175	There exists no reachable operation on variable 'SchM_I2c.msr_I2C_EXCLUSIVE_AREA_01'.		121	23	Unset	Unreview ed	
41177	There exists no reachable operation on variable 'SchM_I2c.reentry_guard_I2C_E XCLUSIVE_AREA_01'.		122	23	Unset	Unreview ed	

Table 6.20. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcl.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41208	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_00'.		119	23	Unset	Unreview ed	
41183	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_00'.		120	23	Unset	Unreview ed	
41229	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_01'.		121	23	Unset	Unreview ed	
41222	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_01'.		122	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41204	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_02'.		123	23	Unset	Unreview ed	
41179	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_02'.		124	23	Unset	Unreview ed	
41211	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_04'.		127	23	Unset	Unreview ed	
41182	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_04'.		128	23	Unset	Unreview ed	
41259	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_05'.		129	23	Unset	Unreview ed	
41243	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_05'.		130	23	Unset	Unreview ed	
41215	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_06'.		131	23	Unset	Unreview ed	
41178	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_06'.		132	23	Unset	Unreview ed	
41262	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_07'.		133	23	Unset	Unreview ed	
41223	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_07'.		134	23	Unset	Unreview ed	
41227	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_08'.		135	23	Unset	Unreview ed	
41181	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_08'.		136	23	Unset	Unreview ed	
41264	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_09'.		137	23	Unset	Unreview ed	
41226	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_09'.		138	23	Unset	Unreview ed	
41198	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_11'.		141	23	Unset	Unreview ed	
41206	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_11'.		142	23	Unset	Unreview ed	
41241	There exists no reachable operation on variable 'SchM_Mcl.msr_MCL_EXCLUSIV E_AREA_12'.		143	23	Unset	Unreview ed	
41251	There exists no reachable operation on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_12'.		144	23	Unset	Unreview ed	

Table 6.21. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Port.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41287	There exists no reachable operation on variable 'SchM_Port.msr_PORT_EXCLUSI VE_AREA_00'.		119	23	Unset	Unreview ed	
41291	There exists no reachable operation on variable 'SchM_Port.reentry_guard_PORT _EXCLUSIVE_AREA_00'.		120	23	Unset	Unreview ed	
41316	There exists no reachable operation on variable 'SchM_Port.msr_PORT_EXCLUSI VE_AREA_05'.		129	23	Unset	Unreview ed	
41333	There exists no reachable operation on variable 'SchM_Port.reentry_guard_PORT _EXCLUSIVE_AREA_05'.		130	23	Unset	Unreview ed	
41309	There exists no reachable operation on variable 'SchM_Port.msr_PORT_EXCLUSI VE_AREA_06'.		131	23	Unset	Unreview ed	
41285	There exists no reachable operation on variable 'SchM_Port.reentry_guard_PORT _EXCLUSIVE_AREA_06'.		132	23	Unset	Unreview ed	
41305	There exists no reachable operation on variable 'SchM_Port.msr_PORT_EXCLUSI VE_AREA_07'.		133	23	Unset	Unreview ed	
41326	There exists no reachable operation on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_07'.		134	23	Unset	Unreview ed	

Table 6.22. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Uart.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41362	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_00'.		119	23	Unset	Unreview ed	
41374	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_00'.		120	23	Unset	Unreview ed	
41360	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_01'.		121	23	Unset	Unreview ed	
41373	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_01'.		122	23	Unset	Unreview ed	
41361	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_02'.		123	23	Unset	Unreview ed	
41371	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_02'.		124	23	Unset	Unreview ed	
41376	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_03'.		125	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41370	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_03'.		126	23	Unset	Unreview ed	
41375	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_04'.		127	23	Unset	Unreview ed	
41369	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_04'.		128	23	Unset	Unreview ed	
41372	There exists no reachable operation on variable 'SchM_Uart.msr_UART_EXCLUSI VE_AREA_05'.		129	23	Unset	Unreview ed	
41367	There exists no reachable operation on variable 'SchM_Uart.reentry_guard_UAR T_EXCLUSIVE_AREA_05'.		130	23	Unset	Unreview ed	

Table 6.23. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41379	There exists no reachable operation on variable 'Siul2_Port_Ip.Port_au32Siul2Ba seAddr'.		167	13	Unset	Unreview ed	

Table 6.24. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_I2c_Cfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38967	There exists no reachable operation on variable 'CDD_I2c_Cfg.I2c_DemConfig'.		113	24	Unset	Unreview ed	

Table 6.25. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_I2c_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38972	There exists no reachable operation on variable 'CDD_I2c_PBcfg.I2c_ChannelConf igPB'.		127	34	Unset	Unreview ed	
38969	There exists no reachable operation on variable 'CDD_I2c_PBcfg,I2c_PBCfgVarian tPredefined_spurious_core'.		157	28	Unset	Unreview ed	
38968	There exists no reachable operation on variable 'CDD_I2c_PBcfg.I2c_PBCfgVarian tPredefined'.		177	29	Unset	Unreview ed	

Table 6.26. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Mcl_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38985	There exists no reachable operation on variable 'CDD_Mcl_PBcfg.Mcl_Config'.		105	21	Unset	Unreview ed	

Table 6.27.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Rm_Ipw_PBcfg .c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38989	There exists no reachable operation on variable 'CDD_Rm_Ipw_PBcfg.Rm_Ipw_C onfig'.		100	24	Unset	Unreview ed	

Table 6.28.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Rm_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38990	There exists no reachable operation on variable 'CDD_Rm_PBcfg.Rm_Config'.		100	20	Unset	Unreview ed	

Table 6.29. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\CDD_Uart_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39140	There exists no reachable operation on variable 'CDD_Uart_PBcfg.Uart_ChannelC onfig_0'.		165	29	Unset	Unreview ed	
39138	There exists no reachable operation on variable 'CDD_Uart_PBcfg.Uart_xConfig'.		179	29	Unset	Unreview ed	
39139	There exists no reachable operation on variable 'CDD_Uart_PBcfg.Uart_pxPBcfgV ariantPredefined'.		193	29	Unset	Unreview ed	

Table 6.30. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Ip_Cfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40284	There exists no reachable operation on variable 'Dma_Ip_Cfg.Dma_Ip_xLogicInst anceResetConfig'.		118	37	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40283	There exists no reachable operation on variable 'Dma_Ip_Cfg.GlobalResetConfig'.		140	30	Unset	Unreview ed	
40263	There exists no reachable operation on variable 'Dma_Ip_Cfg.TransferResetConfig'.		163	32	Unset	Unreview ed	
40260	There exists no reachable operation on variable 'Dma_Ip_Cfg.Dma_Ip_xLogicCha nnelResetConfig'.		205	36	Unset	Unreview ed	

Table 6.31.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Ip_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40321	There exists no reachable operation on variable 'Dma_Ip_PBcfg.Dma_Ip_paxLogi cInstanceConfigArrayPB_BOARD_INITPERIPHERALS'.		132	45	Unset	Unreview ed	
40322	There exists no reachable operation on variable 'Dma_Ip_PBcfg.LogicChannel0Gl obalConfigPB_BOARD_INITPERIPHERALS'.		149	30	Unset	Unreview ed	
40340	There exists no reachable operation on variable 'Dma_Ip_PBcfg.LogicChannel1Gl obalConfigPB_BOARD_INITPERIPHERALS'.		171	30	Unset	Unreview ed	
40329	There exists no reachable operation on variable 'Dma_Ip_PBcfg.LogicChannel0C onfigPB_BOARD_INITPERIPHERALS'.		221	36	Unset	Unreview ed	
40323	There exists no reachable operation on variable 'Dma_Ip_PBcfg.LogicChannel1C onfigPB_BOARD_INITPERIPHERALS'.		238	36	Unset	Unreview ed	
40331	There exists no reachable operation on variable 'Dma_Ip_PBcfg.Dma_Ip_paxLogi cChannelConfigArrayPB_BOARD_INITPERIPHERALS'.		253	44	Unset	Unreview ed	

Table 6.32.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Mux_Ip_PBcfg .c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40344	There exists no reachable operation on variable 'Dma_Mux_Ip_PBcfg.Dma_Mux_Channel_Config'.		101	42	Unset	Unreview ed	
40342	There exists no reachable operation on variable 'Dma_Mux_Ip_PBcfg.Dma_Mux_Config'.		114	28	Unset	Unreview ed	

Table 6.33. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Gpt_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40800	There exists no reachable operation on variable 'Gpt_PBcfg.u8GptChannelIdToIn dexMap'.		134	19	Unset	Unreview ed	
40801	There exists no reachable operation on variable 'Gpt_PBcfg.Gpt_InitChannelPB'.		147	35	Unset	Unreview ed	
40795	There exists no reachable operation on variable 'Gpt_PBcfg.Gpt_Config'.		161	21	Unset	Unreview ed	

Table 6.34. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\I2c_Ipw_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40806	There exists no reachable operation on variable 'I2c_Ipw_PBcfg.I2c_Ipw_Channel Config0'.		126	36	Unset	Unreview ed	

Table 6.35. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Lpi2c_Ip_PBcfg.c

Π	D	Detail	Protection	Line	Col	Severity	Status	Comment
4	0934	There exists no reachable operation on variable 'Lpi2c_Ip_PBcfg.baudrateParam s0'.		122	35	Unset	Unreview ed	
4	0935	There exists no reachable operation on variable 'Lpi2c_Ip_PBcfg.I2c_Lpi2cMaster Channel0'.		135	32	Unset	Unreview ed	

Table 6.36. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Lpuart_Uart_Ip_PBc fg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40969	There exists no reachable operation on variable 'Lpuart_Uart_Ip_PBcfg.Lpuart_Uart_Ip_xHwConfigPB_0'.		145	36	Unset	Unreview ed	

Table 6.37.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Pit_Ip_PBcfg.c

Ι	D	Detail	Protection	Line	Col	Severity	Status	Comment
4	0989	There exists no reachable operation on variable 'Pit_Ip_PBcfg.PIT_0_InitConfig_P B'.		111	32	Unset	Unreview ed	
4	0985	There exists no reachable operation on variable 'Pit_Ip_PBcfg.PIT_0_ChannelConf ig_PB'.		124	31	Unset	Unreview ed	

Table 6.38. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Port_Cfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41006	There exists no reachable operation on variable 'Port_Cfg.Port_SIUL2_0_au16In MuxSettingsIndex'.		103	20	Unset	Unreview ed	
41000	There exists no reachable operation on variable 'Port_Cfg.Port_SIUL2_0_au16Pin ModeAvailability'.		426	47	Unset	Unreview ed	
41003	There exists no reachable operation on variable 'Port_Cfg.Port_SIUL2_0_aInMux Settings'.		5208	35	Unset	Unreview ed	
40997	There exists no reachable operation on variable 'Port_Cfg.Port_SIUL2_0_aInoutM uxSettings'.		6614	35	Unset	Unreview ed	
41005	There exists no reachable operation on variable 'Port_Cfg.Port_au16NumInoutM uxSettings'.		7635	13	Unset	Unreview ed	
41002	There exists no reachable operation on variable 'Port_Cfg.Port_apSiul2InstancePinModeAvailability'.		7649	48	Unset	Unreview ed	
41004	There exists no reachable operation on variable 'Port_Cfg.Port_apInMuxSettings' .		7657	36	Unset	Unreview ed	
40999	There exists no reachable operation on variable 'Port_Cfg.Port_apInMuxSettingsIndex'.		7671	20	Unset	Unreview ed	
41001	There exists no reachable operation on variable 'Port_Cfg.Port_apInoutMuxSettings'.		7685	36	Unset	Unreview ed	

Table 6.39. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Port_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41010	There exists no reachable operation on variable 'Port_PBcfg.au8Port_PartitionLis t'.		93	12	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41014	There exists no reachable operation on variable 'Port_PBcfg.au32Port_PinToParti tionMap'.		106	20	Unset	Unreview ed	
41012	There exists no reachable operation on variable 'Port_PBcfg.Port_aUnusedPads'.		125	38	Unset	Unreview ed	
41008	There exists no reachable operation on variable 'Port_PBcfg.Port_UnusedPinConfig'.		278	44	Unset	Unreview ed	
41009	There exists no reachable operation on variable 'Port_PBcfg.Port_aUsedPinConfigs'.		289	38	Unset	Unreview ed	
41011	There exists no reachable operation on variable 'Port_PBcfg.Port_aSIUL2_0_Imcr InitConfig'.		305	39	Unset	Unreview ed	
41013	There exists no reachable operation on variable 'Port_PBcfg.Port_Config'.		702	22	Unset	Unreview ed	

Table 6.40.

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Power_Ip_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41059	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_MC_ME_aPartition0CoreConfigPB_0'.		143	43	Unset	Unreview ed	
41031	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_MC_ME_aPartition0CofbConfigPB_0'.		167	43	Unset	Unreview ed	
41048	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_MC_ME_aPartition1CofbConfigPB_0'.		189	43	Unset	Unreview ed	
41036	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_MC_ME_aPartitionConfigPB_0'.		267	48	Unset	Unreview ed	
41052	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_MC_ME_ModeConfigPB_0'.		337	43	Unset	Unreview ed	
41026	There exists no reachable operation on variable 'Power_Ip_PBcfg.Power_Ip_DCM _GPR_ConfigPB_0'.		358	41	Unset	Unreview ed	

Table 6.41.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Siul2_Port_Ip_PBcfg .c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41447	There exists no reachable operation on variable 'Siul2_Port_Ip_PBcfg.g_pin_mux _InitConfigArr'.		106	38	Unset	Unreview ed	

Table 6.42.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Tspc_Port_Ip_PBcfg.
c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41448	There exists no reachable operation on variable 'Tspc_Port_Ip_PBcfg.aTspc_InitC onfigArr'.		104	34	Unset	Unreview ed	

Table 6.43. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Uart_Ipw_PBcfg.c

I	D	Detail	Protection	Line	Col	Severity	Status	Comment
4	1451	There exists no reachable operation on variable 'Uart_Ipw_PBcfg.Uart_Ipw_IpCh nConfigPB_0'.		166	28	Unset	Unreview ed	
4	1450	There exists no reachable operation on variable 'Uart_Ipw_PBcfg.Uart_Ipw_xHw ConfigPB_0'.		178	28	Unset	Unreview ed	

Used non-shared variable

Table 6.44. ?extern

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38690	Operations on variable '?externINIT_TABLE' do not interfere with each other.		0	0	Unset	Unreview ed	
38691	Operations on variable '?externZERO_TABLE' do not interfere with each other.		0	0	Unset	Unreview ed	
38692	Operations on variable '?externINDEX_COPY_CORE2' do not interfere with each other.		0	0	Unset	Unreview ed	

Table 6.45. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\include\Dma_Ip_Hw_Access. h

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41453	Operations on variable 'Dma_Ip_Hw_Access.g_ptHwChTcdPtrArray_2' do not inte rfere with each other.		144	52	Unset	Unreview ed	
41454	Operations on variable 'Dma_Ip_Hw_Access.g_ptHwChTcdPtrArray_1' do not inte rfere with each other.		144	52	Unset	Unreview ed	

Table 6.46. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_I2c.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38697	Operations on variable 'CDD_I2c.I2c_au8DriverStatus' do not interfere with each other.		159	13	Unset	Unreview ed	
38694	Operations on variable 'CDD_I2c.I2c_aeChannelStatus' do not interfere with each other.		198	24	Unset	Unreview ed	
38693	Operations on variable 'CDD_I2c.I2c_apConfig' do not interfere with each other.		213	23	Unset	Unreview ed	
38698	Operations on variable 'CDD_I2c.I2c_apDemCfg' do not interfere with each other.		220	26	Unset	Unreview ed	
38695	Operations on variable 'CDD_I2c.I2c_as8ChannelHardwareMap' do not interfere with each other.		236	6	Unset	Unreview ed	

Table 6.47. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Mcl.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38973	Operations on variable 'CDD_Mcl.Mcl_pxConfig' do not interfere with each other.		152	30	Unset	Unreview ed	

Table 6.48. D:\jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Rm.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38986	Operations on variable 'CDD_Rm.Rm_pConfig' do not interfere with each other.		145	29	Unset	Unreview ed	

Table 6.49. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\CDD_Uart.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
38993	Operations on variable 'CDD_Uart.Uart_au8DriverStatus' do not interfere with each other.		112	26	Unset	Unreview ed	

Table 6.50. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39143	Operations on variable 'Clock_Ip.DriverContext' do not interfere with each other.		143	34	Unset	Unreview ed	
39149	Operations on variable 'Clock_Ip.Clock_Ip_pxConfig' do not interfere with each ot her.		160	32	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39150	Operations on variable 'Clock_Ip.Clock_Ip_FreqIds' do not interfere with each ot her.		180	6	Unset	Unreview ed	
39151	Operations on variable 'Clock_Ip.Clock_Ip_pfkNotificationsCallback' do not interfere with each other.		219	42	Unset	Unreview ed	

Table 6.51. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Data.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39578	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8IrcoscCallbackIndex' do not i nterfere with each other.		929	12	Unset	Unreview ed	
39549	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8GateCallbackIndex' do not in terfere with each other.		944	12	Unset	Unreview ed	
39574	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8SelectorCallbackIndex' do no t interfere with each other.		981	12	Unset	Unreview ed	
39553	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8CmuCallbackIndex' do not in terfere with each other.		1003	12	Unset	Unreview ed	
39576	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8ClockFeatures' do not interfe re with each other.		1019	12	Unset	Unreview ed	
39565	Operations on variable 'Clock_Ip_Data.Clock_Ip_au8SoftwareMuxResetValue' do not interfere with each other.		1746	12	Unset	Unreview ed	
39577	Operations on variable 'Clock_Ip_Data.Clock_Ip_au16SelectorEntryHardwareValue' do not interfere with each other.		2465	13	Unset	Unreview ed	
39564	Operations on variable 'Clock_Ip_Data.Clock_Ip_au16SelectorEntryRtcHardware Value' do not interfere with each other.		2532	13	Unset	Unreview ed	
39566	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxCgm' do not interfere with each other.		3329	27	Unset	Unreview ed	
39544	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxCgmPcfs' do not interfere with each other.		3410	37	Unset	Unreview ed	
39552	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxXosc' do not interfere with e ach other.		3415	27	Unset	Unreview ed	
39554	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxPll' do not interfere with each other.		3425	23	Unset	Unreview ed	
39543	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxCmu' do not interfere with e ach other.		3443	33	Unset	Unreview ed	
39571	Operations on variable 'Clock_Ip_Data.Clock_Ip_aeCmuNames' do not interfere w ith each other.		3458	24	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39579	Operations on variable 'Clock_Ip_Data.Clock_Ip_axCmuInfo' do not interfere wit h each other.		3473	27	Unset	Unreview ed	
39567	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxMcMeSetPartitions' do not in terfere with each other.		3536	46	Unset	Unreview ed	
39550	Operations on variable 'Clock_Ip_Data.Clock_Ip_apxMcMeTriggerPartitions' do n ot interfere with each other.		3571	50	Unset	Unreview ed	
39569	Operations on variable 'Clock_Ip_Data.Clock_Ip_aeSourceTypeClockName' do not interfere with each other.		3588	35	Unset	Unreview ed	
39559	Operations on variable 'Clock_Ip_Data.Clock_Ip_axFeatureExtensions' do not inte rfere with each other.		3667	34	Unset	Unreview ed	
39546	Operations on variable 'Clock_Ip_Data.Clock_Ip_axGateInfo' do not interfere wit h each other.		3801	28	Unset	Unreview ed	

Table 6.52. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Gate.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39622	Operations on variable 'Clock_Ip_Gate.Clock_Ip_axGateCallbacks' do not interfer e with each other.		281	32	Unset	Unreview ed	

Table 6.53. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_IntOsc.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39667	Operations on variable 'Clock_Ip_IntOsc.Clock_Ip_axIntOscCallbacks' do not interfere with each other.		319	34	Unset	Unreview ed	

Table 6.54. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Monitor.c

11	D	Detail	Protection	Line	Col	Severity	Status	Comment
3	9685	Operations on variable 'Clock_Ip_Monitor.HashCmu' do not interfere with each other.		146	14	Unset	Unreview ed	
3	9686	Operations on variable 'Clock_Ip_Monitor.Clock_Ip_axCmuCallbacks' do not interfere with each other.		540	40	Unset	Unreview ed	

Table 6.55.
D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_ProgFreqSwitc h.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39725	Operations on variable 'Clock_Ip_ProgFreqSwitch.HashPfs' do not interfere with each other.		130	14	Unset	Unreview ed	

Table 6.56. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Selector.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39792	Operations on variable 'Clock_Ip_Selector.Clock_Ip_axSelectorCallbacks' do not i nterfere with each other.		620	36	Unset	Unreview ed	

Table 6.57. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Clock_Ip_Specific.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40003	Operations on variable 'Clock_Ip_Specific.Clock_Ip_pxSircStdbyClock' do not interfere with each other.		150	42	Unset	Unreview ed	
40004	Operations on variable 'Clock_Ip_Specific.Clock_Ip_pxFircStdbyClock' do not interfere with each other.		151	42	Unset	Unreview ed	
40001	Operations on variable 'Clock_Ip_Specific.Clock_Ip_bObjectsAreInitialized' do not interfere with each other.		174	15	Unset	Unreview ed	

Table 6.58. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dem.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40096	Operations on variable 'Dem.Dem_EventId' do not interfere with each other.		127	16	Unset	Unreview ed	
40097	Operations on variable 'Dem.Dem_EventStatus' do not interfere with each other.		128	20	Unset	Unreview ed	
40094	Operations on variable 'Dem.Dem_OverflowEvent' do not interfere with each oth er.		142	20	Unset	Unreview ed	
40095	Operations on variable 'Dem.Dem_au8ExtendedData' do not interfere with each other.		144	6	Unset	Unreview ed	
40098	Operations on variable 'Dem.Dem_numEventErrors' do not interfere with each o ther.		158	20	Unset	Unreview ed	

Table 6.59. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Det.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40104	Operations on variable 'Det.Det_InstanceId' do not interfere with each other.		131	6	Unset	Unreview ed	
40099	Operations on variable 'Det.Det_ApiId' do not interfere with each other.		132	6	Unset	Unreview ed	
40107	Operations on variable 'Det.Det_ErrorId' do not interfere with each other.		133	6	Unset	Unreview ed	
40103	Operations on variable 'Det.Det_RuntimeInstanceId' do not interfere with each o ther.		135	6	Unset	Unreview ed	
40106	Operations on variable 'Det.Det_RuntimeApiId' do not interfere with each other.		136	6	Unset	Unreview ed	
40101	Operations on variable 'Det.Det_RuntimeErrorId' do not interfere with each othe r.		137	6	Unset	Unreview ed	
40105	Operations on variable 'Det.Det_TransientInstanceId' do not interfere with each other.		139	6	Unset	Unreview ed	
40110	Operations on variable 'Det.Det_TransientApiId' do not interfere with each other .		140	6	Unset	Unreview ed	
40100	Operations on variable 'Det.Det_TransientFaultId' do not interfere with each oth er.		141	6	Unset	Unreview ed	
40108	Operations on variable 'Det.Det_TransientModuleId' do not interfere with each o ther.		155	7	Unset	Unreview ed	
40102	Operations on variable 'Det.Det_ModuleId' do not interfere with each other.		156	7	Unset	Unreview ed	
40109	Operations on variable 'Det.Det_RuntimeModuleId' do not interfere with each ot her.		157	7	Unset	Unreview ed	

Table 6.60. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40111	Operations on variable 'Dma_Ip.Dma_Ip_pxInit' do not interfere with each other.		219	31	Unset	Unreview ed	

Table 6.61. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Driver_State.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40291	Operations on variable 'Dma_Ip_Driver_State.Dma_Ip_xHwState' do not interfere with each other.		112	26	Unset	Unreview ed	

Table 6.62. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Dma_Ip_Hw_Access.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40298	Operations on variable 'Dma_Ip_Hw_Access.Dma_Ip_paxHwv3BaseInst' do not in terfere with each other.		153	39	Unset	Unreview ed	

Table 6.63. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Emios_Mcl_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40353	Operations on variable 'Emios_Mcl_Ip.Emios_Ip_paxBase' do not interfere with e ach other.		97	12	Unset	Unreview ed	
40348	Operations on variable 'Emios_Mcl_Ip.Emios_Ip_axIpIsInitialized' do not interfer e with each other.		120	34	Unset	Unreview ed	
40350	Operations on variable 'Emios_Mcl_Ip.Emios_Ip_axChState' do not interfere with each other.		131	28	Unset	Unreview ed	
40347	Operations on variable 'Emios_Mcl_Ip.Emios_Ip_ChPeriodMasterBus' do not interfere with each other.		150	14	Unset	Unreview ed	

Table 6.64. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Flexio_Mcl_Ip.c

1	ID	Detail	Protection	Line	Col	Severity	Status	Comment
4	40588	Operations on variable 'Flexio_Mcl_Ip.Flexio_Ip_paxBase' do not interfere with each other.		112	20	Unset	Unreview ed	
4	40589	Operations on variable 'Flexio_Mcl_Ip.Flexio_Ip_baIpIsInitialized' do not interfer e with each other.		122	8	Unset	Unreview ed	

Table 6.65. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Gpt.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40594	Operations on variable 'Gpt.Gpt_pConfig' do not interfere with each other.		201	29	Unset	Unreview ed	
40598	Operations on variable 'Gpt.returnHwChannelInfo' do not interfere with each ot her.		1739	33	Unset	Unreview ed	

Table 6.66. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpi2c_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40808	Operations on variable 'Lpi2c_Ip.Lpi2c_Ip_pxBase' do not interfere with each oth er.		166	26	Unset	Unreview ed	
40809	Operations on variable 'Lpi2c_Ip.Lpi2c_Ip_MasterState' do not interfere with each other.		176	32	Unset	Unreview ed	
40807	Operations on variable 'Lpi2c_Ip.Lpi2c_Ip_pxMasterState' do not interfere with e ach other.		199	33	Unset	Unreview ed	
40810	Operations on variable 'Lpi2c_Ip.Lpi2c_Ip_pxSlaveState' do not interfere with each other.		200	32	Unset	Unreview ed	

Table 6.67. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Lpuart_Uart_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40953	Operations on variable 'Lpuart_Uart_Ip.Lpuart_Uart_Ip_apStateStructure' do not interfere with each other.		154	34	Unset	Unreview ed	
40950	Operations on variable 'Lpuart_Uart_Ip.Lpuart_Uart_Ip_apUserConfig' do not int erfere with each other.		157	38	Unset	Unreview ed	
40951	Operations on variable 'Lpuart_Uart_Ip.Lpuart_Uart_Ip_apStateStructuresArray' do not interfere with each other.		160	43	Unset	Unreview ed	
40952	Operations on variable 'Lpuart_Uart_Ip.Lpuart_Uart_Ip_apBases' do not interfere with each other.		169	27	Unset	Unreview ed	

Table 6.68. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Mcu.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40972	Operations on variable 'Mcu.Mcu_au8ClockConfigIds' do not interfere with each		179	13	Unset	Unreview	
	other.					ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40971	Operations on variable 'Mcu.Mcu_au8ModeConfigIds' do not interfere with each other.		185	13	Unset	Unreview ed	
40970	Operations on variable 'Mcu.Mcu_pConfigPtr' do not interfere with each other.		227	30	Unset	Unreview ed	

Table 6.69. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Pit_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40979	Operations on variable 'Pit_Ip.pitBase' do not interfere with each other.		152	17	Unset	Unreview ed	
40980	Operations on variable 'Pit_Ip_Pit_Ip_u32ChState' do not interfere with each othe r.		189	20	Unset	Unreview ed	

Table 6.70. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Port.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40991	Operations on variable 'Port.Port_pConfigPtr' do not interfere with each other.		125	31	Unset	Unreview ed	

Table 6.71. D:\jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41015	Operations on variable 'Power_Ip.Power_Ip_pfReportErrorsCallback' do not interfere with each other.		242	34	Unset	Unreview ed	

Table 6.72. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_ME.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41016	Operations on variable 'Power_Ip_MC_ME.Power_Ip_pxMC_ME' do not interfere with each other.		175	29	Unset	Unreview ed	

Table 6.73. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Power_Ip_MC_RGM.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41019	Operations on variable 'Power_Ip_MC_RGM.DesResetStatus' do not interfere with each other.		174	14	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41018	Operations on variable 'Power_Ip_MC_RGM.FesResetStatus' do not interfere with each other.		175	14	Unset	Unreview ed	
41020	Operations on variable 'Power_Ip_MC_RGM.StandbyResetStatus' do not interfere with each other.		179	14	Unset	Unreview ed	
41017	Operations on variable 'Power_Ip_MC_RGM.Power_Ip_pxMC_RGM' do not interfere with each other.		192	30	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41078	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_00' do not interfere with each other.		119	23	Unset	Unreview ed	
41124	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_00' do not interfere with each other.		120	23	Unset	Unreview ed	
41066	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_01' do not interfere with each other.		121	23	Unset	Unreview ed	
41070	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_01' do not interfere with each other.		122	23	Unset	Unreview ed	
41081	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_02' do not interfere with each other.		123	23	Unset	Unreview ed	
41126	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_02' do not interfere with each other.		124	23	Unset	Unreview ed	
41142	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_03' do not interfere with each other.		125	23	Unset	Unreview ed	
41090	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_03' do not interfere with each other.		126	23	Unset	Unreview ed	
41167	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_04' do not interfere with each other.		127	23	Unset	Unreview ed	
41123	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_04' do not interfere with each other.		128	23	Unset	Unreview ed	
41139	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_05' do not interfe re with each other.		129	23	Unset	Unreview ed	
41068	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_05' do not interfere with each other.		130	23	Unset	Unreview ed	
41092	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_06' do not interfere with each other.		131	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41125	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_06' do not interfere with each other.		132	23	Unset	Unreview ed	
41145	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_07' do not interfere with each other.		133	23	Unset	Unreview ed	
41071	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_07' do not interfere with each other.		134	23	Unset	Unreview ed	
41121	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_10' do not interfe re with each other.		135	23	Unset	Unreview ed	
41097	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_10' do not interfere with each other.		136	23	Unset	Unreview ed	
41065	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_11' do not interfere with each other.		137	23	Unset	Unreview ed	
41153	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_11' do not interfere with each other.		138	23	Unset	Unreview ed	
41094	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_12' do not interfere with each other.		139	23	Unset	Unreview ed	
41099	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_12' do not interfere with each other.		140	23	Unset	Unreview ed	
41074	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_17' do not interfe re with each other.		141	23	Unset	Unreview ed	
41173	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_17' do not interfere with each other.		142	23	Unset	Unreview ed	
41100	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_21' do not interfe re with each other.		143	23	Unset	Unreview ed	
41131	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_21' do not interfere with each other.		144	23	Unset	Unreview ed	
41157	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_22' do not interfe re with each other.		145	23	Unset	Unreview ed	
41160	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_22' do not interfere with each other.		146	23	Unset	Unreview ed	
41109	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_23' do not interfe re with each other.		147	23	Unset	Unreview ed	
41128	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_23' do not interfere with each other.		148	23	Unset	Unreview ed	
41163	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_24' do not interfere with each other.		149	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41073	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_24' do not interfere with each other.		150	23	Unset	Unreview ed	
41113	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_25' do not interfere with each other.		151	23	Unset	Unreview ed	
41129	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_25' do not interfere with each other.		152	23	Unset	Unreview ed	
41166	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_26' do not interfe re with each other.		153	23	Unset	Unreview ed	
41076	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_26' do not interfere with each other.		154	23	Unset	Unreview ed	
41112	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_27' do not interfere with each other.		155	23	Unset	Unreview ed	
41127	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_27' do not interfere with each other.		156	23	Unset	Unreview ed	
41168	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_28' do not interfere with each other.		157	23	Unset	Unreview ed	
41072	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_28' do not interfere with each other.		158	23	Unset	Unreview ed	
41116	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_29' do not interfe re with each other.		159	23	Unset	Unreview ed	
41171	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_29' do not interfere with each other.		160	23	Unset	Unreview ed	
41093	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_30' do not interfe re with each other.		161	23	Unset	Unreview ed	
41159	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_30' do not interfere with each other.		162	23	Unset	Unreview ed	
41148	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_31' do not interfe re with each other.		163	23	Unset	Unreview ed	
41104	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_31' do not interfere with each other.		164	23	Unset	Unreview ed	
41149	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_35' do not interfe re with each other.		165	23	Unset	Unreview ed	
41103	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_35' do not interfere with each other.		166	23	Unset	Unreview ed	
41095	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_36' do not interfere with each other.		167	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41133	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_36' do not interfere with each other.		168	23	Unset	Unreview ed	
41146	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_38' do not interfere with each other.		169	23	Unset	Unreview ed	
41158	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_38' do not interfere with each other.		170	23	Unset	Unreview ed	
41154	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_39' do not interfe re with each other.		171	23	Unset	Unreview ed	
41102	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_39' do not interfere with each other.		172	23	Unset	Unreview ed	
41130	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_40' do not interfere with each other.		173	23	Unset	Unreview ed	
41137	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_40' do not interfere with each other.		174	23	Unset	Unreview ed	
41075	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_41' do not interfere with each other.		175	23	Unset	Unreview ed	
41083	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_41' do not interfere with each other.		176	23	Unset	Unreview ed	
41106	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_42' do not interfe re with each other.		177	23	Unset	Unreview ed	
41134	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_42' do not interfere with each other.		178	23	Unset	Unreview ed	
41077	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_43' do not interfe re with each other.		179	23	Unset	Unreview ed	
41079	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_43' do not interfere with each other.		180	23	Unset	Unreview ed	
41132	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_44' do not interfere with each other.		181	23	Unset	Unreview ed	
41136	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_44' do not interfere with each other.		182	23	Unset	Unreview ed	
41080	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_45' do not interfere with each other.		183	23	Unset	Unreview ed	
41082	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_45' do not interfere with each other.		184	23	Unset	Unreview ed	
41135	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_46' do not interfere with each other.		185	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41151	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_46' do not interfere with each other.		186	23	Unset	Unreview ed	
41147	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_50' do not interfere with each other.		187	23	Unset	Unreview ed	
41111	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_50' do not interfere with each other.		188	23	Unset	Unreview ed	
41114	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_51' do not interfe re with each other.		189	23	Unset	Unreview ed	
41144	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_51' do not interfere with each other.		190	23	Unset	Unreview ed	
41169	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_52' do not interfere with each other.		191	23	Unset	Unreview ed	
41108	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_52' do not interfere with each other.		192	23	Unset	Unreview ed	
41120	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_53' do not interfere with each other.		193	23	Unset	Unreview ed	
41162	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_53' do not interfere with each other.		194	23	Unset	Unreview ed	
41064	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_54' do not interfe re with each other.		195	23	Unset	Unreview ed	
41110	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_54' do not interfere with each other.		196	23	Unset	Unreview ed	
41122	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_55' do not interfe re with each other.		197	23	Unset	Unreview ed	
41164	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_55' do not interfere with each other.		198	23	Unset	Unreview ed	
41067	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_56' do not interfe re with each other.		199	23	Unset	Unreview ed	
41107	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_56' do not interfere with each other.		200	23	Unset	Unreview ed	
41096	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_60' do not interfe re with each other.		201	23	Unset	Unreview ed	
41089	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_60' do not interfere with each other.		202	23	Unset	Unreview ed	
41152	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_61' do not interfere with each other.		203	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41138	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_61' do not interfere with each other.		204	23	Unset	Unreview ed	
41098	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_62' do not interfere with each other.		205	23	Unset	Unreview ed	
41085	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_62' do not interfere with each other.		206	23	Unset	Unreview ed	
41155	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_63' do not interfe re with each other.		207	23	Unset	Unreview ed	
41069	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_63' do not interfere with each other.		208	23	Unset	Unreview ed	
41105	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_64' do not interfere with each other.		209	23	Unset	Unreview ed	
41087	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_64' do not interfere with each other.		210	23	Unset	Unreview ed	
41156	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_65' do not interfere with each other.		211	23	Unset	Unreview ed	
41119	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_65' do not interfere with each other.		212	23	Unset	Unreview ed	
41101	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_66' do not interfe re with each other.		213	23	Unset	Unreview ed	
41084	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_66' do not interfere with each other.		214	23	Unset	Unreview ed	
41161	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_67' do not interfe re with each other.		215	23	Unset	Unreview ed	
41140	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_67' do not interfere with each other.		216	23	Unset	Unreview ed	
41150	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_68' do not interfe re with each other.		217	23	Unset	Unreview ed	
41086	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_68' do not interfere with each other.		218	23	Unset	Unreview ed	
41165	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_69' do not interfe re with each other.		219	23	Unset	Unreview ed	
41143	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_69' do not interfere with each other.		220	23	Unset	Unreview ed	
41141	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_70' do not interfere with each other.		221	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41172	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_70' do not interfere with each other.		222	23	Unset	Unreview ed	
41088	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_71' do not interfe re with each other.		223	23	Unset	Unreview ed	
41115	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_71' do not interfere with each other.		224	23	Unset	Unreview ed	
41118	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_72' do not interfere with each other.		225	23	Unset	Unreview ed	
41170	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_72' do not interfere with each other.		226	23	Unset	Unreview ed	
41091	Operations on variable 'SchM_Gpt.msr_GPT_EXCLUSIVE_AREA_73' do not interfere with each other.		227	23	Unset	Unreview ed	
41117	Operations on variable 'SchM_Gpt.reentry_guard_GPT_EXCLUSIVE_AREA_73' do not interfere with each other.		228	23	Unset	Unreview ed	

Table 6.75. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcl.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41256	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_03' do not interfere with each other.		125	23	Unset	Unreview ed	
41224	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_03' do not interfere with each other.		126	23	Unset	Unreview ed	
41239	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_10' do not interfere with each other.		139	23	Unset	Unreview ed	
41230	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_10' do not interfere with each other.		140	23	Unset	Unreview ed	
41199	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_13' do not interfere with each other.		145	23	Unset	Unreview ed	
41207	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_13' do not interfere with each other.		146	23	Unset	Unreview ed	
41245	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_14' do not interfere with each other.		147	23	Unset	Unreview ed	
41254	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_14' do not interfere with each other.		148	23	Unset	Unreview ed	
41200	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_15' do not interfere with each other.		149	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41205	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_15' do not interfere with each other.		150	23	Unset	Unreview ed	
41263	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_16' do not interfere with each other.		151	23	Unset	Unreview ed	
41250	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_16' do not interfere with each other.		152	23	Unset	Unreview ed	
41201	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_17' do not interfere with each other.		153	23	Unset	Unreview ed	
41242	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_17' do not interfere with each other.		154	23	Unset	Unreview ed	
41248	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_18' do not interfere with each other.		155	23	Unset	Unreview ed	
41253	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_18' do not interfere with each other.		156	23	Unset	Unreview ed	
41240	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_19' do not interfere with each other.		157	23	Unset	Unreview ed	
41219	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_19' do not interfere with each other.		158	23	Unset	Unreview ed	
41180	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_20' do not interfere with each other.		159	23	Unset	Unreview ed	
41246	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_20' do not interfere with each other.		160	23	Unset	Unreview ed	
41225	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_21' do not interfere with each other.		161	23	Unset	Unreview ed	
41188	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_21' do not interfere with each other.		162	23	Unset	Unreview ed	
41185	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_22' do not interfere with each other.		163	23	Unset	Unreview ed	
41267	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_22' do not interfere with each other.		164	23	Unset	Unreview ed	
41228	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_23' do not interfere with each other.		165	23	Unset	Unreview ed	
41191	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_23' do not interfere with each other.		166	23	Unset	Unreview ed	
41184	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_24' do not interfere with each other.		167	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41214	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_24' do not interfere with each other.		168	23	Unset	Unreview ed	
41231	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_25' do not interfere with each other.		169	23	Unset	Unreview ed	
41187	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_25' do not interfere with each other.		170	23	Unset	Unreview ed	
41189	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_26' do not interfere with each other.		171	23	Unset	Unreview ed	
41232	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_26' do not interfere with each other.		172	23	Unset	Unreview ed	
41271	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_27' do not interfere with each other.		173	23	Unset	Unreview ed	
41190	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_27' do not interfere with each other.		174	23	Unset	Unreview ed	
41196	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_28' do not interfere with each other.		175	23	Unset	Unreview ed	
41233	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_28' do not interfere with each other.		176	23	Unset	Unreview ed	
41236	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_29' do not interfere with each other.		177	23	Unset	Unreview ed	
41186	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_29' do not interfere with each other.		178	23	Unset	Unreview ed	
41216	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_30' do not interfere with each other.		179	23	Unset	Unreview ed	
41210	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_30' do not interfere with each other.		180	23	Unset	Unreview ed	
41265	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_31' do not interfere with each other.		181	23	Unset	Unreview ed	
41258	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_31' do not interfere with each other.		182	23	Unset	Unreview ed	
41217	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_32' do not interfere with each other.		183	23	Unset	Unreview ed	
41213	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_32' do not interfere with each other.		184	23	Unset	Unreview ed	
41266	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_33' do not interfere with each other.		185	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41261	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_33' do not interfere with each other.		186	23	Unset	Unreview ed	
41218	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_34' do not interfere with each other.		187	23	Unset	Unreview ed	
41221	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_34' do not interfere with each other.		188	23	Unset	Unreview ed	
41212	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_35' do not interfere with each other.		189	23	Unset	Unreview ed	
41257	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_35' do not interfere with each other.		190	23	Unset	Unreview ed	
41220	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_36' do not interfere with each other.		191	23	Unset	Unreview ed	
41244	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_36' do not interfere with each other.		192	23	Unset	Unreview ed	
41268	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_37' do not interfere with each other.		193	23	Unset	Unreview ed	
41260	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_37' do not interfere with each other.		194	23	Unset	Unreview ed	
41238	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_38' do not interfere with each other.		195	23	Unset	Unreview ed	
41197	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_38' do not interfere with each other.		196	23	Unset	Unreview ed	
41270	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_39' do not interfere with each other.		197	23	Unset	Unreview ed	
41255	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_39' do not interfere with each other.		198	23	Unset	Unreview ed	
41247	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_40' do not interfere with each other.		199	23	Unset	Unreview ed	
41193	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_40' do not interfere with each other.		200	23	Unset	Unreview ed	
41202	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_41' do not interfere with each other.		201	23	Unset	Unreview ed	
41235	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_41' do not interfere with each other.		202	23	Unset	Unreview ed	
41249	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_42' do not interfere with each other.		203	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41195	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_42' do not interfere with each other.		204	23	Unset	Unreview ed	
41203	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_43' do not interfere with each other.		205	23	Unset	Unreview ed	
41237	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_43' do not interfere with each other.		206	23	Unset	Unreview ed	
41252	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_44' do not interfere with each other.		207	23	Unset	Unreview ed	
41192	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_44' do not interfere with each other.		208	23	Unset	Unreview ed	
41209	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_45' do not interfere with each other.		209	23	Unset	Unreview ed	
41234	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_45' do not interfere with each other.		210	23	Unset	Unreview ed	
41269	Operations on variable 'SchM_Mcl.msr_MCL_EXCLUSIVE_AREA_46' do not interfere with each other.		211	23	Unset	Unreview ed	
41194	Operations on variable 'SchM_Mcl.reentry_guard_MCL_EXCLUSIVE_AREA_46' do not interfere with each other.		212	23	Unset	Unreview ed	

Table 6.76. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Mcu.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41272	Operations on variable 'SchM_Mcu.msr_MCU_EXCLUSIVE_AREA_00' do not interfere with each other.		119	23	Unset	Unreview ed	
41276	Operations on variable 'SchM_Mcu.reentry_guard_MCU_EXCLUSIVE_AREA_00' d o not interfere with each other.		120	23	Unset	Unreview ed	
41274	Operations on variable 'SchM_Mcu.msr_MCU_EXCLUSIVE_AREA_01' do not interfere with each other.		121	23	Unset	Unreview ed	
41275	Operations on variable 'SchM_Mcu.reentry_guard_MCU_EXCLUSIVE_AREA_01' d o not interfere with each other.		122	23	Unset	Unreview ed	
41273	Operations on variable 'SchM_Mcu.msr_MCU_EXCLUSIVE_AREA_02' do not interfere with each other.		123	23	Unset	Unreview ed	
41277	Operations on variable 'SchM_Mcu.reentry_guard_MCU_EXCLUSIVE_AREA_02' d o not interfere with each other.		124	23	Unset	Unreview ed	

Table 6.77. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Port.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41293	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_01' do not interfere with each other.		121	23	Unset	Unreview ed	
41325	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_01' d o not interfere with each other.		122	23	Unset	Unreview ed	
41329	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_02' do not interfere with each other.		123	23	Unset	Unreview ed	
41283	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_02' d o not interfere with each other.		124	23	Unset	Unreview ed	
41320	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_03' do not interfere with each other.		125	23	Unset	Unreview ed	
41281	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_03' d o not interfere with each other.		126	23	Unset	Unreview ed	
41319	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_04' do not interfere with each other.		127	23	Unset	Unreview ed	
41278	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_04' d o not interfere with each other.		128	23	Unset	Unreview ed	
41297	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_08' do not interfere with each other.		135	23	Unset	Unreview ed	
41324	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_08' d o not interfere with each other.		136	23	Unset	Unreview ed	
41294	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_09' do not interfere with each other.		137	23	Unset	Unreview ed	
41321	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_09' d o not interfere with each other.		138	23	Unset	Unreview ed	
41303	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_10' do not interfere with each other.		139	23	Unset	Unreview ed	
41302	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_10' d o not interfere with each other.		140	23	Unset	Unreview ed	
41310	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_11' do not interfere with each other.		141	23	Unset	Unreview ed	
41315	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_11' d o not interfere with each other.		142	23	Unset	Unreview ed	
41306	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_12' do not interfere with each other.		143	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41314	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_12' d o not interfere with each other.		144	23	Unset	Unreview ed	
41298	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_13' do not interfere with each other.		145	23	Unset	Unreview ed	
41313	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_13' d o not interfere with each other.		146	23	Unset	Unreview ed	
41295	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_14' do not interfere with each other.		147	23	Unset	Unreview ed	
41308	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_14' d o not interfere with each other.		148	23	Unset	Unreview ed	
41288	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_15' do not interfere with each other.		149	23	Unset	Unreview ed	
41317	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_15' d o not interfere with each other.		150	23	Unset	Unreview ed	
41279	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_16' do not interfere with each other.		151	23	Unset	Unreview ed	
41304	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_16' d o not interfere with each other.		152	23	Unset	Unreview ed	
41296	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_17' do not interfere with each other.		153	23	Unset	Unreview ed	
41301	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_17' d o not interfere with each other.		154	23	Unset	Unreview ed	
41322	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_18' do not interfere with each other.		155	23	Unset	Unreview ed	
41300	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_18' d o not interfere with each other.		156	23	Unset	Unreview ed	
41311	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_19' do not interfere with each other.		157	23	Unset	Unreview ed	
41299	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_19' d o not interfere with each other.		158	23	Unset	Unreview ed	
41289	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_20' do not interfere with each other.		159	23	Unset	Unreview ed	
41290	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_20' d o not interfere with each other.		160	23	Unset	Unreview ed	
41280	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_21' do not interfere with each other.		161	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41286	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_21' d o not interfere with each other.		162	23	Unset	Unreview ed	
41332	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_22' do not interfere with each other.		163	23	Unset	Unreview ed	
41284	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_22' d o not interfere with each other.		164	23	Unset	Unreview ed	
41323	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_23' do not interfere with each other.		165	23	Unset	Unreview ed	
41282	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_23' d o not interfere with each other.		166	23	Unset	Unreview ed	
41328	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_24' do not interfere with each other.		167	23	Unset	Unreview ed	
41292	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_24' d o not interfere with each other.		168	23	Unset	Unreview ed	
41318	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_25' do not interfere with each other.		169	23	Unset	Unreview ed	
41331	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_25' d o not interfere with each other.		170	23	Unset	Unreview ed	
41312	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_26' do not interfere with each other.		171	23	Unset	Unreview ed	
41330	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_26' d o not interfere with each other.		172	23	Unset	Unreview ed	
41307	Operations on variable 'SchM_Port.msr_PORT_EXCLUSIVE_AREA_27' do not interfere with each other.		173	23	Unset	Unreview ed	
41327	Operations on variable 'SchM_Port.reentry_guard_PORT_EXCLUSIVE_AREA_27' d o not interfere with each other.		174	23	Unset	Unreview ed	

Table 6.78. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Rm.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41358	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_00' do not interfer e with each other.		119	23	Unset	Unreview ed	
41339	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_00' do n ot interfere with each other.		120	23	Unset	Unreview ed	
41342	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_01' do not interfer e with each other.		121	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41359	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_01' do n ot interfere with each other.		122	23	Unset	Unreview ed	
41353	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_02' do not interfer e with each other.		123	23	Unset	Unreview ed	
41346	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_02' do n ot interfere with each other.		124	23	Unset	Unreview ed	
41336	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_03' do not interfer e with each other.		125	23	Unset	Unreview ed	
41352	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_03' do n ot interfere with each other.		126	23	Unset	Unreview ed	
41345	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_04' do not interfer e with each other.		127	23	Unset	Unreview ed	
41341	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_04' do n ot interfere with each other.		128	23	Unset	Unreview ed	
41355	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_05' do not interfer e with each other.		129	23	Unset	Unreview ed	
41335	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_05' do n ot interfere with each other.		130	23	Unset	Unreview ed	
41337	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_06' do not interfer e with each other.		131	23	Unset	Unreview ed	
41357	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_06' do n ot interfere with each other.		132	23	Unset	Unreview ed	
41348	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_07' do not interfer e with each other.		133	23	Unset	Unreview ed	
41351	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_07' do n ot interfere with each other.		134	23	Unset	Unreview ed	
41334	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_08' do not interfer e with each other.		135	23	Unset	Unreview ed	
41343	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_08' do n ot interfere with each other.		136	23	Unset	Unreview ed	
41340	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_09' do not interfer e with each other.		137	23	Unset	Unreview ed	
41347	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_09' do n ot interfere with each other.		138	23	Unset	Unreview ed	
41356	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_10' do not interfer e with each other.		139	23	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41344	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_10' do n ot interfere with each other.		140	23	Unset	Unreview ed	
41338	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_11' do not interfer e with each other.		141	23	Unset	Unreview ed	
41354	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_11' do n ot interfere with each other.		142	23	Unset	Unreview ed	
41349	Operations on variable 'SchM_Rm.msr_RM_EXCLUSIVE_AREA_12' do not interfer e with each other.		143	23	Unset	Unreview ed	
41350	Operations on variable 'SchM_Rm.reentry_guard_RM_EXCLUSIVE_AREA_12' do n ot interfere with each other.		144	23	Unset	Unreview ed	

Table 6.79. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\SchM_Uart.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41368	Operations on variable 'SchM_Uart.msr_UART_EXCLUSIVE_AREA_06' do not interfere with each other.		131	23	Unset	Unreview ed	
41366	Operations on variable 'SchM_Uart.reentry_guard_UART_EXCLUSIVE_AREA_06' do not interfere with each other.		132	23	Unset	Unreview ed	
41364	Operations on variable 'SchM_Uart.msr_UART_EXCLUSIVE_AREA_07' do not interfere with each other.		133	23	Unset	Unreview ed	
41377	Operations on variable 'SchM_Uart.reentry_guard_UART_EXCLUSIVE_AREA_07' do not interfere with each other.		134	23	Unset	Unreview ed	
41363	Operations on variable 'SchM_Uart.msr_UART_EXCLUSIVE_AREA_08' do not interfere with each other.		135	23	Unset	Unreview ed	
41365	Operations on variable 'SchM_Uart.reentry_guard_UART_EXCLUSIVE_AREA_08' do not interfere with each other.		136	23	Unset	Unreview ed	

Table 6.80. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Siul2_Port_Ip.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
4137	Operations on variable 'Siul2_Port_Ip.pPort_Setting' do not interfere with each ot her.		190	47	Unset	Unreview ed	
4138	Operations on variable 'Siul2_Port_Ip.u32MaxPinConfigured' do not interfere with each other.		198	14	Unset	Unreview ed	

Table 6.81. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\RTD\src\Uart_Ipw.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41449	Operations on variable 'Uart_Ipw.Uart_Ipw_apChnConfig' do not interfere with e ach other.		164	29	Unset	Unreview ed	

Table 6.82. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Clock_Ip_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
39700	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_IrcoscConfigurations_0' do not i nterfere with each other.		166	39	Unset	Unreview ed	
39698	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_XoscConfigurations_0' do not in terfere with each other.		203	37	Unset	Unreview ed	
39696	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_PllConfigurations_0' do not interfere with each other.		237	36	Unset	Unreview ed	
39699	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_SelectorConfigurations_0' do not interfere with each other.		268	41	Unset	Unreview ed	
39691	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_DividerConfigurations_0' do not interfere with each other.		328	40	Unset	Unreview ed	
39694	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_DividerTriggerConfigurations_0 ' do not interfere with each other.		473	47	Unset	Unreview ed	
39695	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_GatesConfigurations_0' do not i nterfere with each other.		485	37	Unset	Unreview ed	
39693	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_CmuConfigurations_0' do not in terfere with each other.		909	36	Unset	Unreview ed	
39697	Operations on variable 'Clock_Ip_PBcfg.Clock_Ip_ConfiguredFrequencyConfigurations_0' do not interfere with each other.		982	46	Unset	Unreview ed	
39692	Operations on variable 'Clock_Ip_PBcfg.Mcu_aClockConfigPB' do not interfere with each other.		1024	31	Unset	Unreview ed	

Table 6.83. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Dma_Ip_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40332	Operations on variable 'Dma_Ip_PBcfg.LogicInstance0ConfigPB_BOARD_INITPER IPHERALS' do not interfere with each other.		117	37	Unset	Unreview ed	

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40318	Operations on variable 'Dma_Ip_PBcfg.HwChannel0StatePB_BOARD_INITPERIPH ERALS' do not interfere with each other.		268	26	Unset	Unreview ed	
40315	Operations on variable 'Dma_Ip_PBcfg.HwChannel1StatePB_BOARD_INITPERIPH ERALS' do not interfere with each other.		271	26	Unset	Unreview ed	
40330	Operations on variable 'Dma_Ip_PBcfg.Dma_Ip_paxHwChannelStateArrayPB_BO ARD_INITPERIPHERALS' do not interfere with each other.		282	28	Unset	Unreview ed	
40324	Operations on variable 'Dma_Ip_PBcfg.Dma_Ip_xDmaInitPB_BOARD_INITPERIPH ERALS' do not interfere with each other.		297	22	Unset	Unreview ed	

Table 6.84. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Gpt_Ipw_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
40792	Operations on variable 'Gpt_Ipw_PBcfg.Gpt_Ipw_HwInstanceConfig_PB' do not in terfere with each other.		163	29	Unset	Unreview ed	
40788	Operations on variable 'Gpt_Ipw_PBcfg.Gpt_Ipw_ChannelConfig_PB' do not interfere with each other.		181	28	Unset	Unreview ed	

Table 6.85. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Mcu_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
	Operations on variable 'Mcu_PBcfg.Mcu_PreCompileConfig' do not interfere with each other.		130	21	Unset	Unreview ed	

Table 6.86. D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\src\Power_Ip_PBcfg.c

ID	Detail	Protection	Line	Col	Severity	Status	Comment
41054	Operations on variable 'Power_Ip_PBcfg.Power_Ip_aModeConfigPB' do not interfere with each other.		383	30	Unset	Unreview ed	
41045	Operations on variable 'Power_Ip_PBcfg.Power_Ip_MC_RGM_ConfigPB' do not interfere with each other.		409	40	Unset	Unreview ed	
41035	Operations on variable 'Power_Ip_PBcfg.Power_Ip_PMC_ConfigPB' do not interfere with each other.		436	37	Unset	Unreview ed	
41062	Operations on variable 'Power_Ip_PBcfg.Power_Ip_HwIPsConfigPB' do not interfere with each other.		452	31	Unset	Unreview ed	

Chapter 7. Appendix 1 - Configuration Settings

Polyspace Settings

Option	Value
-author	Pega_User
-check-subnormal	allow
-code-metrics	true
-compiler	gnu5.x
-date	03/01/2025
-dos	true
-float-rounding-mode	to-nearest
т	Dillacated NVD CODE CONVICTEDO4700 IOC Master DMA Function Test DTD2 0.0 CONVICTED MCAL Dillacated NVD CODE CONVICTED

-T

D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL,D:\Jessie\3_NXP\S32DS_S32K312EVB Q172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\\$32K312_I2C_Master_MCAL\board,D:\Jessie\3_NXP\\$32DS_\$32K312EVBQ172\8_I2C_Master_DMA_Fun ctionTest_RTD3.0.0\S32K312_I2C_Master_MCAL\generate\include,D:\Jessie\3_NXP\S32DS_S32K312EVBQ172\8_I2C_Master_DMA_FunctionTest_RTD3.0.0\ S32K312_I2C_Master_MCAL\RTD\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S 32K3\RTD\Adc_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ae_TS_T40D34M30I0R0\include,D:\NXP\S32DS. 3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNXP TS T40D34M30I0R0\header,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNX P TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNXP TS T40D34M30I0R0\wizard data,D:\NXP\S32DS.3. 5\S32DS\software\PlatformSDK_S32K3\RTD\CanIf_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\CanIr_cv_43 _AE_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\include,D:\NXP\S3 2DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crc_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\CryIf_TS_ ware\PlatformSDK S32K3\RTD\Csm TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Dem TS T40D34M30I0R0\ include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Det_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S 32K3\RTD\Dio_TS_T40D34M30I0R0\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\include,D:\NXP\S32DS\.3.5\S3 S.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EthIf_TS $_T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ software\\\ PlatformSDK\\\ S32K3\\\ RTD\\\ EthSwt\\\ TS\\\ T40D34M30I0R0\\\ include, D:\\\ NXP\\\ S32DS.3.5\\\ S32DS\\\ S32DS$ ware\PlatformSDK S32K3\RTD\EthTrcv TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 43 GMAC TS T40 D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\software atformSDK_S32K3\RTD\Gdu_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2c TS T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD D\I2s TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Icu TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS S\software\PlatformSDK S32K3\RTD\LinIf TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\LinTrcv 43 AE TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\include,D:\NXP\S32\RTD\Lin_43_LPUART_FL T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\include.D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\NemAcc TS T40D34M3\RTD\NemAcc T

tware\PlatformSDK S32K3\RTD\MemIf TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mem 43 Eep TS T40D $34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_43_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Mem_40_ExFls_TS_T40D34M30I0R0\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ S32DS\\ softwar$ ftware\PlatformSDK_S32K3\RTD\Mem_43_InFls_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocotp_TS_T40 D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocu TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\Pl atformSDK S32K3\RTD\Os TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\include,D:\NXP\S32DS\software\Platform\SDK S32K3\RTD\Platform\SDK de,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Port TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3 RTD\Pwm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D\side \PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D\side \PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D\side \PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\include,D\side \PlatformSDK_S32K3\RTD\Rm_TS_T40D34\RTD\Rm_TS_T40D34\RTD\Rm_TS_T40 S32DS\software\PlatformSDK S32K3\RTD\Rte TS T40D34M30I0R0\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Sent TS T40D34 M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Spi TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\Platfor mSDK S32K3\RTD\Uart TS T40D34M30I0R0\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\WdgIf TS T40D34M30I0R0\include,D:\ $NXP \ S32DS.3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ Wdg_TS_T40D34M30I0R0 \ include, D: \ NXP \ S32DS.3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ NXP \ S32DS.3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ NXP \ S32DS.3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ NXP \ S32DS.3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ NXP \ S32DS \ software$ Zipwire_TS_T40D34M30I0R0\include,D:\NXP\S32DS.\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\generate_PB\include,D:\NXP \S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32 K3\RTD\Ae_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ae_TS_T40D34M30I0R0\generate_PC\i nclude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNXP TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software e\PlatformSDK S32K3\RTD\BaseNXP TS T40D34M30I0R0\wizard data\S32K310,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNXP T S_T40D34M30I0R0\wizard_data\S32K311,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K3 12,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K314,D:\NXP\S32DS.3.5\S32DS\software\P latformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K322,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_ $T40D34M30I0R0\\ wizard_data\\ S32K324, D:\\ NXP\\ S32DS.3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ BaseNXP_TS_T40D34M30I0R0\\ wizard_data\\ S32K32K32R3D\\ software\\ PlatformSDK_S32K3\\ RTD\\ software\\ PlatformSDK_S$ D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K338,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32DS\softwar formSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K341,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T4 0D34M30I0R0\wizard_data\S32K342,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K344,D $: \NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K348,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32DS\software\platformSDK_S32DS\softw$ 34M30I0R0\wizard data\S32K388,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\BaseNXP TS T40D34M30I0R0\wizard data\S32K394,D:\N XP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32K396,D:\NXP\S32DS.3.5\S32DS\software\Platform SDK_S32K3\RTD\BaseNXP_TS_T40D34M30I0R0\wizard_data\S32M274,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\BaseNXP_TS_T40D34 M30I0R0\wizard_data\S32M276,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\CanIf_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32 DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\CanIf_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3 \RTD\CanTrcv_43_AE_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\CanTrcv_43_AE_TS_T40D34 M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\generate_PB\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\software\Platf are\PlatformSDK_S32K3\RTD\Crc_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\include,D:\nxP\S12\RTD\Crypto_TS_T40D34M30I0R0\generate_PC\inclu D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dem_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S 32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Dio TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K 3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\RTD\Dpga_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\square\PlatformSDK_S32M3\ PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\EcuM_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\sof $re\PlatformSDK_S32K3\RTD\Eth_43_GMAC_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Eth_4B\generate_PB\normsDK_S3$ 3 GMAC TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Fee TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS\side S32K3\RTD\Fee TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS\side S32K3\RTD\Fee TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS\side S32K3\RTD\Fee TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS\side S32DS\side S3 clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Gdu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Gdu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Gdu TS T40D34M30I\RTD\Gdu TS T40D34M3\RTD\Gdu TS T40D34M3\RTD\Gdu TS T40D34M3\R formSDK_S32K3\RTD\Gdu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0 RO\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S3 2DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_T S_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2s_TS_T40D34M30I0R0\generate_PB\include,D:\N XP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2s TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S 32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I\square\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M3\square\PlatformSDK_S32K3

 $PC\ include, D:\ NXP\ S32DS. 3.5\ S32DS\ software\ Platform SDK_S32K3\ RTD\ LinTrcv_43_AE_TS_T40D34M30I0R0\ generate\ include, D:\ NXP\ S32DS. 3.5\ S32DS\ software\ Platform SDK_S32DS\ software\ Platform SDK_S32DS\$ oftware\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S 32K3\RTD\Lin 43 LPUART FLEXIO_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D 34M30I0R0\generate PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcl TS T40D34M30I0R0\generate PC\include,D:\NXP\S32 DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RTD\NXP\S32DS\software\PlatformSDK S32K3\RT RTD\Mcu TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\generate _PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\MemAcc_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\MemAcc_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32K3\RTD\MemAcc_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS tware\PlatformSDK S32K3\RTD\Mem 43 Eep TS T40D34M30I0R0\generate PC\include.D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Me m 43 ExFls TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mem 43 InFls TS T40D34M30I0R0\g $enerate_PC\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Ocotp_TS_T40D34M30I0R0\\ generate_PB\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Ocotp_TS_T40D34M30I0R0\\ generate_PB\\ include, D:\\ NXP\\ S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ soft$ S\software\PlatformSDK S32K3\RTD\Ocotp TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocotp TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Ocotp TS T40D34M30I0R0\generate PC\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include,D\include, TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M30I\RTD\Ocu_TS_T40D34M3\Ocu_TS_T40D34M3\Ocu_TS_T40D34\RTD\OcuT NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32K3\RTD\Os_TS_T40D34M30I\RTD\Os_TS_T40D34M3\RTD\ $S32K3\RTD\Platform_TS_T40D34M30I0R0\generate\clube, D:\NXP\S32DS.3.5\S32DS\software\Platform\SDK_S32K3\RTD\Platform_TS_T40D34M30I0R0\software\clube, D:\NXP\S32DS.3.5\S32DS\software\Platform\SDK_S32K3\RTD\Platform_TS_T40D34M30I0R0\software\clube, D:\NXP\S32DS.3.5\S32DS\software\Platform\SDK_S32K3\RTD\Platform_TS_T40D34M30I0R0\software\clube, D:\NXP\S32DS\software\Platform\SDK_S32K3\software\clube, D:\NXP\S32DS\software\Platform\SDK_S32K3\software\clube, D:\NXP\S32DS\software\clube, D:\NXP\S32DS\software\software\clube, D:\NXP\S32DS\software\clube, D:\NXP\S32DS$ tartup\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Port TS T40D34M30I0R0\generate PB\include,D:\NXP\S32DS.3.5\S32DS\soft ware\PlatformSDK S32K3\RTD\Port TS T40D34M30I0R0\generate PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Pwm TS T40 D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S 32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K 3\RTD\Rm_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PB \include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\softw latformSDK_S32K3\RTD\Spi_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D34M30I 0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Uart_TS_T40D34M30I0R0\generate_PB\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK_S32K3\RTD\Uart_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\W dg_TS_T40D34M30I0R0\generate\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Wdg_TS_T40D34M30I0R0\generate_PB\include,D:\ NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Zipwire_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Zipwire_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Zipwire_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Zipwire_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Zipwire_TS_T40D34M30I0R0\generate_PC\include,D:\NXP\S32DS\software\PlatformSDK_S32DS\so SDK S32K3\RTD\Crypto TS T40D34M30I0R0\examples\S32DS\hse interface\S32K344,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Crypt o_TS_T40D34M30I0R0\examples\S32DS\hse_interface\S32K358,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\ examples\S32DS\hse_interface\S32K396,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\EBT\S32K358\A dc_example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\EBT\S32K396\Adc_example le_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Adc_example_S32K3 44\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\S32DS\S32K358\Adc_example_S32K358\inclu de,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\S32DS\S32K358\Adc_Sar_Bctu_Ip_example_S32K358\ include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\S32DS\S32K396\Adc_example_S32K396\include, $D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\S32DS\S32K396\Adc_Sar_Bctu_Ip_example_S32K396\end{2} includes the property of the proper$ lude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Adc_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Adc_example_S32K344\include,D:\ NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Adc TS T40D34M30I0R0\examples\S32DS\S32K3XX\Adc Sar Bctu Ip example S32K344\inclu $de, D: NXP \ S32DS. 3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ Ae_TS_T40D34M30I0R0 \ examples \ EBT \ S32M276 \ Ae_Handler_Faults_Example_EB_001_SBT \ and \ Ae_TS_T40D34M30I0R0 \ examples \ BT \ S32M276 \ Ae_Handler_Faults_Example_EB_001_SBT \ and \ BT \ Ae_TS_T40D34M30I0R0 \ examples \ examples \ Ae_TS_T40D34M30I0R0 \ examples$ 32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ae_TS_T40D34M30I0R0\examples\EBT\S32M276\Ae_Spi_Example_EB_001_ S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ae_TS_T40D34M30I0R0\examples\S32DS\S32M276\Aec_Ip_Spi_Example_ DS 001 S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ae TS T40D34M30I0R0\examples\S32DS\S32M276\Ae Handler F aults Example DS 001 S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ae TS T40D34M30I0R0\examples\S32DS\S32M276\include,D:\NXP\S32DS\S32DS\S32M276\include,D:\NXP\S32DS\S32DS\S32M276\include,D:\NXP\S32DS\S32DS\S32M276\include,D:\NXP\S32DS\S32DS\S32DS\S32M276\include,D:\NXP\S32DS 6\Ae Spi Example DS_001_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\CanTrcv_43_AE_TS_T40D34M30I0R0\example s\EBT\\$32M276\CanTrcv_Example_S32M276\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_S32K3\RTD\CanTrcv_43_AE_TS_T40D34M30I0R0\e xamples\S32DS\S32M276\CanTrcv_Example_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T40D3 4M30I0R0\examples\EBT\S32K358\Can_example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Can_43_FLEXCAN_TS_T4 0D34M30I0R0\examples\EBT\S32K396\Can example S32K396\include,D:\NXP\S32DS,3.5\S32DS\software\PlatformSDK S32K3\RTD\Can 43 FLEXCAN T S_T40D34M30I0R0\examples\EBT\S32K3XX\Can_example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3XX\Can_example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3XX\Can_example_S32K3X\Can_example_

AN TS T40D34M30I0R0\examples\S32DS\S32K358\Can Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Can 43 FLEXCAN_TS_T40D34M30I0R0\examples\S32DS\S32K358\FlexCAN_Ip_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K 3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\examples\S32DS\S32K396\Can_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSD $K_S32K3\RTD\Can_43_FLEXCAN_TS_T40D34M30I0R0\examples\S32DS\S32K396\FlexCAN_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\softwarrows and the sum of the sum of$ re\PlatformSDK S32K3\RTD\Can 43 FLEXCAN TS T40D34M30I0R0\examples\S32DS\S32K3XX\Can Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\ software\PlatformSDK S32K3\RTD\Can 43 FLEXCAN TS T40D34M30I0R0\examples\S32DS\S32K3XX\FlexCAN Ip Example S32K344\include,D:\NXP\S3 2DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crc_TS_T40D34M30I0R0\examples\EBT\S32K358\Crc_Example_S32K358\include,D:\NXP\S32DS.3.5\S3 2DS\software\PlatformSDK_S32K3\RTD\Crc_TS_T40D34M30I0R0\examples\EBT\S32K396\Crc_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K396\include,D:\NXP\S32DS\software\PlatformSDK_S32K396\include,D re\PlatformSDK S32K3\RTD\Crc TS T40D34M30I0R0\examples\EBT\S32K3XX\Crc Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\Platfor $mSDK_S32K3/RTD/Crc_TS_T40D34M30I0R0/examples/S32DS/S32K358/Crc_Example_S32K358/include,D:/NXP/S32DS.3.5/S32DS/s0ftware/PlatformSDK_S32K358/Crc_Example_S32K358/include,D:/NXP/S32DS.3.5/S32DS/s0ftware/PlatformSDK_S32K358/Crc_Example_S32K35/Crc_Example_S32K35/Crc_Example_S32K35/Crc_Example_S32K35/Crc_Examp$ 32K3\RTD\Crc_TS_T40D34M30I0R0\examples\S32DS\S32K358\Crc_Ip_Example_S32K358\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK_S32K3\ RTD\Crc_TS_T40D34M30I0R0\examples\S32DS\S32K396\Crc_Example_S32K396\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crc_Example_S32K3\RTD\C TS_T40D34M30I0R0\examples\S32DS\S32K396\Crc_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crc_TS_T 40D34M30I0R0\examples\S32DS\S32K3XX\Crc_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crc_TS_T40D34M 30I0R0\examples\S32DS\S32K3XX\Crc Ip Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Crypto TS T40D34M3 010R0\examples\EBT\S32K358\Crypto CmacCtr KeyGenBD S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Crypto TS T40 D34M30I0R0\examples\EBT\S32K358\Crypto_SymmetricPrimitives_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto TS_T40D34M30I0R0\examples\EBT\S32K358\Hse_Ip_AesEncAsyncIrq_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Cry pto_TS_T40D34M30I0R0\examples\EBT\S32K396\Crypto_CmacCtr_KeyGenBD_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\ RTD\Crypto_TS_T40D34M30I0R0\examples\EBT\S32K396\Crypto_SymmetricPrimitives_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSD K_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\EBT\S32K396\Hse_Ip_AesEncAsyncIrq_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\Platfor mSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Crypto_CmacCtr_KeyGenBD_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software \PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Crypto_SymmetricPrimitives_S32K344\include,D:\NXP\S32DS.3.5\S32DS\ software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Hse_Ip_AesEncAsyncIrq_S32K344\include,D:\NXP\S32DS.3.5\S3 2DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S32K358\Crypto_CmacCtr_KeyGenBD_S32K358\include,D:\NXP\S3 2DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Crypto TS T40D34M30I0R0\examples\S32DS\S32K358\Crypto SymmetricPrimitives S32K358\include D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S32K358\Hse_Ip_AesEncAsyncIrg_S32K358\in clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S32K396\Crypto_CmacCtr_KeyGenBD_S 32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S32K396\Crypto_SymmetricP rimitives_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S32K396\Hse_Ip_A ypto_CmacCtr_KeyGenBD_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examples\S32DS\S 32K3XX\Crypto_SymmetricPrimitives_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Crypto_TS_T40D34M30I0R0\examp les\S32DS\S32K3XX\Hse_Ip_AesEncAsyncIrq_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dio_TS_T40D34M30I0R0\exa $mples \ EBT \ S32K358 \ Dio_Example_S32K358 \ include, D: \ NXP \ S32DS. 3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ Dio_TS_T40D34M30I0R0 \ (examples \ EBT_S32K358) \ descriptions \ descripti$ \S32K396\Dio Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Dio TS T40D34M30I0R0\examples\EBT\S32K3XX\ Dio_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dio_TS_T40D34M30I0R0\examples\S32DS\S32K358\Dio_Example mple_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dio_TS_T40D34M30I0R0\examples\S32DS\S32K358\Siul2_Dio_Ip_Ex ample_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dio_TS_T40D34M30I0R0\examples\S32K396\Dio_Example_S 32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Dio TS T40D34M30I0R0\examples\S32DS\S32K396\Siul2 Dio Ip Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Dio TS T40D34M30I0R0\examples\S32DS\S32K3XX\Dio Example S32K34 4\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dio_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Siul2_Dio_Ip_Example_S32K3 44\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\examples\EBT\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276\Dpga_example_S32M276\include,D:\nxP\S32M276 ude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\examples\S32DS\S32M276\Dpga_example_S32M276\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Dpga_TS_T40D34M30I0R0\examples\S32DS\S32M276\Dpga_Ip_example_S32M276\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 43 GMAC TS T40D34M30I0R0\examples\EBT\S32K358\Eth InternalLoopback S32K35 8\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Eth_43_GMAC_TS_T40D34M30I0R0\examples\EBT\S32K396\Eth_InternalLoopbac

k S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 43 GMAC TS T40D34M30I0R0\examples\EBT\S32K3XX\Eth Interna $\label{loopback} Loopback S32K344\\ include, D:\NXP\\S32DS.3.5\\S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Eth_43_GMAC_TS_T40D34M30I0RO\\ examples\\ S32DS\\ S32K358\\ NTD\\ Eth_43_GMAC_TS_T40D34M30I0RO\\ examples\\ S32DS\\ S32DS\\$ Eth_InternalLoopback_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Eth_43_GMAC_TS_T40D34M30I0R0\examples\S32D S\S32K358\Gmac Ip InternalLoopback S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 43 GMAC TS T40D34M30I0R 0\examples\S32DS\S32K396\Eth InternalLoopback S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 43 GMAC TS T40 D34M30I0R0\examples\S32DS\S32K396\Gmac Ip InternalLoopback S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Eth 4 3_GMAC_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Eth_InternalLoopback_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K 3\RTD\Eth_43_GMAC_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Gmac_Ip_InternalLoopback_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\Pl atformSDK S32K3\RTD\Fee TS T40D34M30I0R0\examples\EBT\S32K358\Fee Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSD K S32K3\RTD\Fee TS T40D34M30I0R0\examples\EBT\S32K396\Fee Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\R $TD\Fee_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Fee_Example_S32K344\clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Fee_Example_S32K344\clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Fee_Example_S32K344\clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3XX\Fee_Example_S32K3A4\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\PlatformSDK_S32K3X\clude,D:\NXP\S32DS\software\SDK_S32K3X\clude,D:\NXP\S32DS$ T40D34M30I0R0\examples\S32D\$\S32D\$\S32K358\Fee_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Fee_TS_T40D34 M30I0R0\examples\S32DS\S32K396\Fee Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Fee TS T40D34M30I0R $0 | wamples S32DS S32K3XX | Fee_Example_S32K344 | include_D: NXP | S32DS | software | PlatformSDK_S32K3 | RTD | Gdu_TS_T40D34M30I0R0 | examples | S32K3 | RTD | Gdu_TS_T40D34M30I0R0 | examples | examples | Gdu_TS_T40D34M30I0R0 | examples | Gdu_TS_T40D34M30I0R0 | examples | example$ ples/EBT\S32M276\Gdu Example EBT S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Gdu TS T40D34M30I0R0\example s\S32DS\S32M276\Gdu Example DS S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Gpt TS T40D34M30I0R0\examples\E BT\\$32K358\Gpt_Example_\$32K358\include,D:\NXP\\$32D\$.3.5\\$32D\$\software\Platform\$DK_\$32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\EBT\\$32K39 6\Gpt_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Gpt_Exa mple_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\S32D5\S32K358\Gpt_Example_S3 2K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\S32DS\S32K358\Pit_Gpt_Ip_Example_S32 clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\S32DS\S32K396\Pit_Gpt_Ip_Example_S32K396\incl ude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Gpt_Example_S32K344\include,D:\ NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Gpt_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Pit_Gpt_Ip_Example_S32K344\include,D:\ NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\EBT\S32K358\I2c_HLD_FLEXIO_Transfer_S32K358\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2c TS T40D34M30I0R0\examples\EBT\S32K396\I2c HLD FLEXIO Transfer S32K396\inclu de,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\EBT\S32K3XX\I2c_HLD_FLEXIO_Transfer_S32K344\in clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2c TS T40D34M30I0R0\examples\EBT\S32K3XX\I2c HLD FLEXIO Transfer S32K344 _Q172\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\S32DS\S32K358\I2c_HLD_FLEXIO_Transfe r_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\S32DS\S32K358\I2c_IP_FLEXIO_Trans fer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\S32DS\S32K396\I2c_HLD_FLEXIO_T ransfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\S32DS\S32K396\I2c_IP_FLEXIO Transfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2c_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\I2c_HLD_FL $EXIO_Transfer_S32K344 \ \ include, D:\ \ NXP\ \ S32DS. 3.5\ \ S32DS\ \ software\ \ PlatformSDK_S32K3\ \ RTD\ \ \ 12c_TS_T40D34M30I0R0\ \ \ examples\ \ S32K3X\ \ \ \ I2c_HL$ $D_FLEXIO_Transfer_S32K344_Q172\\ include, D:\\ NXP\\S32DS.3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ I2c_TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32DS\\$ 3XX\I2c IP FLEXIO Transfer S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2c TS T40D34M30I0R0\examples\S32DS\S3 2K3XX\I2c_IP_FLEXIO_Transfer_S32K344_Q172\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2s_TS_T40D34M30I0R0\examples\ EBT\S32K358\I2s Flexio_Sai_example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2s_TS_T40D34M30I0R0\examples\ EBT\S32K396\I2s_Flexio_example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\I2s_TS_T40D34M30I0R0\examples\EBT\ S32K3XX\I2s Flexio Sai example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2s TS T40D34M30I0R0\examples\S32D S\S32K396\I2s Example Flexio Master Tx Flexio Slave Rx S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\I2s TS T40D3 4M30I0R0\examples\S32DS\S32K3XX\I2s_Example_Flexio_Master_Tx_Sai_Slave_Rx_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S 32K3\RTD\Icu_TS_T40D34M30I0R0\examples\EBT\\$32K358\Icu_example_S32K358\include,D:\NXP\\$32D\$.3.5\S32D\$\software\Platform\$DK_\$32K3\RTD\I cu TS T40D34M30I0R0\examples\EBT\S32K396\Icu example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Icu TS T40 D34M30I0R0\examples\EBT\S32K3XX\Icu_example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M30I0 R0\examples\S32DS\S32K358\Emios Icu Ip BlinkLed S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Icu TS T40D34M30I 0R0\examples\S32DS\S32K358\Icu_BlinkLed_ASR_Emios_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40D34M

30I0R0\examples\S32DS\S32K396\Emios Icu Ip BlinkLed S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Icu TS T40D34 M30I0R0\examples\S32DS\S32K396\Icu_BlinkLed_ASR_Emios_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Icu_TS_T40 D34M30I0R0\examples\S32DS\S32K3XX\Emios Icu Ip BlinkLed S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Icu TS T 40D34M30I0R0\examples\S32DS\S32K3XX\Icu BlinkLed ASR Emios S32K344\include,D:\NXP\S32DS\Software\PlatformSDK S32K3\RTD\Lin 43 LPUART FLEXIO TS T40D34M30I0R0\examples\EBT\S32K358\Lin MasterFrameTransfer S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\Platfo rmSDK S32K3\RTD\Lin 43 LPUART FLEXIO TS T40D34M30I0R0\examples\EBT\S32K396\Lin Flexio MasterFrameTransfer S32K396\include,D:\NXP\S 32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\EBT\S32K396\Lin_Lpuart_MasterFrameTra nsfer S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Lin 43 LPUART FLEXIO TS T40D34M30I0R0\examples\EBT\S32K3 XX\Lin MasterFrameTransfer S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Lin 43 LPUART FLEXIO TS T40D34M30I0 RO\examples\EBT\S32M276\Lin LinTrcv Flexio MasterFrameTransfer S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Li n_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\EBT\S32M276\Lin_LinTrcv_Lpuart_MasterFrameTransfer_S32M276\include,D:\NXP\S32DS.3.5\S3 $2DS | software | PlatformSDK_S32K3 | RTD | Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0 | examples | S32DS | S32K358 | Lin_1p_FrameTransfer S32K358 | include | S32K358 | Lin_1p_FrameTransfer S32K358 | Lin_1p_FrameTransfer$ e,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32K358\Lin_MasterFram eTransfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\ S32K396\Lin Ip FrameTransfer S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Lin 43 LPUART FLEXIO TS T40D34M30 IORO\examples\S32DS\S32K396\Lin MasterFrameTransfer S32K396\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK S32K3\RTD\Lin 43 LPUAR T_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Lin_Ip_FrameTransfer_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32 K3\RTD\Lin_43 LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Lin_MasterFrameTransfer_S32K344\include,D:\NXP\S32DS.3.5\S32DS\s $of tware \ | Platform SDK_S32K3 \ | TD \ | Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0 \ | examples \ | S32DS \ | S32M276 \ | Lin_LinTrcv_Flexio_MasterFrameTransfer_FLEXIO_TS_T40D34M30I0R0 \ | Platform SDK_S32M276 \ | Platf$ S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32M276\include,D:\NXP\S32DS.4.5\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32M276\include,D:\NXP\S32DS\S32DS\software\PlatformSDK_S32K3\RTD\Lin_43_LPUART_FLEXIO_TS_T40D34M30I0R0\examples\S32DS\S32DS\S32DS\S32M276\include,D:\NXP\S32DS\S3 Lin_LinTrcv_Lpuart_MasterFrameTransfer_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\exaction xales and the contraction of the contraction mples\EBT\S32K358\Mcl_DmaTransfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\example s\EBT\\$32K358\Mcl LcuControlMotor \$32K358\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK \$32K3\RTD\Mcl TS T40D34M30I0R0\examples\ EBT\S32K396\Mcl_DmaTransfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\examples\EBT\S $32K396\colored{Mcl_LcuControlMotor_S32K396\colored{Mcl_LcuContro$ 2K3XX\Mcl DmaTransfer S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcl TS T40D34M30I0R0\examples\EBT\S32K3X X\Mcl_LcuControlMotor_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\S32K3 $58 Dma_Ip_DmaTransfer_S32K358 include, D: NXP\S32DS. 3.5\S32DS is of tweel-Platform SDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0 is examples in S32DS include, D: NXP\S32DS include, D:$ $358\Lcu_Ip_LcuControlMotor_S32K358\colored{line} include, D:\NXP\S32DS.3.5\S32DS\software\label{line} software\label{line} are larger for the line of the line o$ $32K358\\Mcl_DmaTransfer_S32K358\\include,D:\\NXP\\S32DS.3.5\\S32DS\\software\\PlatformSDK_S32K3\\RTD\\Mcl_TS_T40D34M30I0RO\\examples\\S32DS\\S32DS\\software\\PlatformSDK_S32K3\\RTD\\Mcl_TS_T40D34M30I0RO\\examples\\S32DS\\$ K396\Dma_Ip_DmaTransfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\S32DS\S3 2K396\Lcu_Ip_LcuControlMotor_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl_TS_T40D34M30I0R0\examples\S32D S\S32K396\Mcl DmaTransfer S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcl TS_T40D34M30I0R0\examples\S32DS\S $32K3XX\backslash Dma_Ip_DmaTransfer_S32K344\backslash include, D:\NXP\S32DS.3.5\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\PlatformSDK_S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\backslash software\Platform\S32K3\XTD\Mcl_TS_T40D34M30I0R0\examples\S32DS\NS3$ 2DS\S32K3XX\Mcl DmaTransfer S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\EBT\ S32K358\Mcu_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\EBT\S32K396\ Mcu_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Mcu_Ex ample_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\EBT\S32M276\Mcu_Example_S 32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32K358\Clock Ip Example S3 2K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32K358\Mcu Example S32K358\ $include, D: \NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\S32DS\S32K358\Nower_Ip_Example S32K358\Nower_Ip_Example S32K358\Nower_Ip_Ex$ nclude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\S32DS\S32K396\Clock_Ip_Example_S32K396\inc lude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32K396\Mcu Example S32K396\include,D :\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32K396\Power Ip Example S32K396\include,D:\ NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32K3XX\Clock Ip Example S32K344\include,D:\N XP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Mcu_Example_S32K344\include,D:\NXP\S3

2DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32K3XX\Power Ip Example S32K344\include,D:\NXP\S32 DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\S32DS\S32M276\Clock_Ip_Example_S32M276\include,D:\NXP\S32D S.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mcu_TS_T40D34M30I0R0\examples\S32DS\S32M276\Mcu_Example_S32M276\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK S32K3\RTD\Mcu TS T40D34M30I0R0\examples\S32DS\S32M276\Power Ip Example S32M276\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\examples\EBT\S32K358\MemAcc Example S32K358\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\examples\EBT\S32K396\MemAcc Example S32K396\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK_S32K3\RTD\MemAcc_TS_T40D34M30I0R0\examples\EBT\S32K3XX\MemAcc_Example_S32K344\include,D:\NXP\S32DS.3.5\ S32DS\software\PlatformSDK_S32K3\RTD\MemAcc_TS_T40D34M30I0R0\examples\S32DS\S32K358\MemAcc_Example_S32K358\include,D:\NXP\S32DS.3. 5\S32DS\software\PlatformSDK S32K3\RTD\MemAcc TS T40D34M30I0R0\examples\S32DS\S32K396\MemAcc Example S32K396\include,D:\NXP\S32DS. $3.5 \ S32DS \ software \ Platform SDK_S32K3\ RTD \ mem Acc_TS_T40D34M30I0R0 \ examples \ S32K3XX \ Mem Acc_Example_S32K344 \ include, D:\ NXP\ S32K3XX \ Mem Acc_Example_S32K3XX \ Mem Acc_Example_S32K344 \ include, D:\ NXP\ S32K3XX \ Mem Acc_Example_S32K3XX \ Mem A$ DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_Eep_TS_T40D34M30I0R0\examples\EBT\S32K358\Mem_Eep_Example_S32K358\include,D:\NX P\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_Eep_TS_T40D34M30I0R0\examples\S32DS\S32K358\Mem_Eep_Example_S32K358\includ e,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mem 43 Eep_TS T40D34M30I0R0\examples\S32DS\S32K358\Usdhc Ip Example S32K358 \include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_ExFls_TS_T40D34M30I0R0\examples\EBT\S32K358\Mem_43_ExFls_Exam ple S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mem 43 ExFls TS T40D34M30I0R0\examples\EBT\S32K396\Mem 43 ExFls Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Mem 43 ExFls TS T40D34M30I0R0\examples\EBT\S32K3 XX\Mem_43_ExFls_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_ExFls_TS_T40D34M30I0R0\example s\S32DS\S32K358\Mem_43_ExFls_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_ExFls_TS_T40D34M3 $010RO \ | S32DS \ S32DS \ S32DS \ S32R358 \ | Ip_Example_S32K358 \$ D34M30I0R0\examples\S32DS\S32K396\Mem_43_ExFls_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43 _ExFls_TS_T40D34M30I0R0\examples\S32DS\S32K396\Qspi_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\ Mem 43 ExFls TS T40D34M30I0R0\examples\S32DS\S32K3XX\Mem 43 ExFls Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformS DK_S32K3\RTD\Mem_43_ExFls_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Qspi_Ip_Example_S32K344\include,D:\NXP\S32DS\S32DS\software\Pl $at form SDK_S32K3\RTD\Mem_43_InFls_TS_T40D34M30I0R0\examples\EBT\S32K358\Mem_InFls_Example_S32K358\end{term}. S32K358\end{term}$ $ware \ Platform SDK_S32K3\ RTD\ Mem_43_InFls_TS_T40D34M30I0R0\ examples\ EBT\ S32K396\ Mem_InFls_Example_S32K396\ include, D:\ NXP\ S32DS.3.5\ S3DS.3.5\ S$ 2DS\software\PlatformSDK S32K3\RTD\Mem 43 InFls TS T40D34M30I0R0\examples\EBT\S32K3XX\Mem InFls Example S32K344\include,D:\NXP\S32 DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_InFls_TS_T40D34M30I0R0\examples\S32DS\S32K358\C40_Ip_Example_S32K358\include,D:\N $XP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\mbox{$Mem_43_InFls_TS_T40D34M30I0R0}\examples\S32DS\S32K358\mbox{$Mem_InFls_Example_S32K358}\noindent with the property of the property of$ $clude, D: NXP \ S32DS. 3.5 \ S32DS \ software \ Platform SDK_S32K3 \ RTD \ Mem_43_InFls_TS_T40D34M30I0R0 \ examples \ S32DS \ S32K396 \ C40_Ip_Example_S32K3 \ RTD \ Mem_43_InFls_TS_T40D34M30I0R0 \ examples \ S32DS \ S32K396 \ C40_Ip_Example_S32K3 \ RTD \ Mem_43_InFls_TS_T40D34M30I0R0 \ examples \ S32DS \ S3$ $396\ \ Logard Logard$ p_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Mem_43_InFls_TS_T40D34M30I0R0\examples\S32DS\S32DS\S32K3XX \Mem_InFls_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocotp_TS_T40D34M30I0R0\examples\EBT\S32M276 $\label{lem:cotop_example_S32M276} $$ \cotop_Example_S32M276\\ include,D:\NXP\S32DS.3.5\\ S32DS\software\PlatformSDK_S32K3\\ RTD\Ocotop_TS_T40D34M30I0R0\\ examples\S32M276\\ MD\Ocotop_TS_T40D34M30I0R0\\ examples\S32M276\\ MD\Ocotop_TS_T50M276\\ MD\Ocoto$ $em_Otp_Ip_Example_S32M276 \\ include, D: \\ NXP\\S32DS. 3.5\\ S32DS\\ software\\ PlatformSDK_S32K3\\ RTD\\ Ocotp_TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32M2\\ TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32M2\\ TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32M2\\ TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32M2\\ TS_T40D34M30I0R0\\ examples\\ S32DS\\ S32M2\\ TS_T4DS\\ S32DS\\ S32M2\\ TS_T4DS\\ S32DS\\ S32M2\\ S32DS\\ S32M2\\ S32$ 76\Ocotp Example \$32M276\include,D:\NXP\\$32D\$.3.5\\$32D\$\software\Platform\$DK \$32K3\RTD\Ocu T\$ T40D34M30I0R0\examples\EBT\\$32K358\Ocu Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocu TS T40D34M30I0R0\examples\EBT\S32K396\Ocu Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Ocu_Example_S32K344 \include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\examples\S32DS\S32K358\eMios_Ocu_Ip_Example_S32K3 58\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocu TS T40D34M30I0R0\examples\S32DS\S32K358\Ocu Example S32K358\include,D:\NXP\S32DS\S32DS\S32DS\S32K358\Ocu Example S32K358\include,D:\NXP\S32DS\S32DS\S32DS\S32BS\Ocu Example S32K358\include,D:\NXP\S32DS\S32DS\S32DS\S32BS\S ude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocu TS T40D34M30I0R0\examples\S32DS\S32K396\eMios Ocu Ip Example S32K396\i $nclude, D: NXP \ S32DS, S32DS \ S32DS \ S32K396 \ Ocu_Example_S32K396 \ include, S32K396 \ Ocu_Example_S32K396 \ Ocu_Example_S32K3$ D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Ocu_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\eMios_Ocu_Ip_Example_S32K344\inclu de,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Ocu TS T40D34M30I0R0\examples\S32DS\S32K3XX\Ocu Example S32K344\include,D:\N XP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\EBT\S32K358\Platform MPU Example S32K358\includ e,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\EBT\S32K396\Platform MPU Example S32K396\i nclude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Platform_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Platform_MPU_Example_S32

K344\include,D:\NXP\S32DS,3.5\S32DS\software\Platform SDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\EBT\S32M276\Platform MPU Examp le_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Platform_TS_T40D34M30I0R0\examples\S32DS\S32K358\Platform_MP U HLD Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\S32DS\S32K358 \Platform MPU IP Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\S32 DS\S32K396\Platform MPU HLD Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D34M30I0R0\ examples\S32DS\S32K396\Platform MPU IP Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Platform TS T40D 34M30I0R0\examples\\\$32D\$\\\$32D\$\\\$32K3XX\Platform_MPU_HLD_Example_\\$32K344\include,D:\NXP\\\$32D\$\.35\\\$32D\$\\software\Platform\\$DK_\\$32K3\RTD\Platf orm TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Platform_MPU_IP_Example_S32K344\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK_S32 K3\RTD\Platform TS T40D34M30I0R0\examples\S32DS\S32M276\Platform MPU HLD Example S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\Pla tformSDK S32K3\RTD\Platform TS T40D34M30I0R0\examples\S32DS\S32M276\Platform MPU IP Example S32M276\include,D:\NXP\S32DS.3.5\S32DS\ software\PlatformSDK S32K3\RTD\Port TS T40D34M30I0R0\examples\EBT\S32K358\Port Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K358\include,D:\NXP\S32DS\software\PlatformSDK S32K358\include,D:\NXP\S32DS\ $Platform SDK\ S32K3\ RTD\ Port_TS_T40D34M3010R0\ | examples\ EBT\ S32K396\ | Port_Example_S32K396\ | include, D:\ NXP\ S32DS. 3.5\ | S32DS\ | software\ | Platform\ | Platfo$ SDK_S32K3\RTD\Port_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Port_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32 K3\RTD\Port_TS_T40D34M30I0R0\examples\EBT\S32M276\Port_Example_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\ Port TS T40D34M30I0R0\examples\S32DS\S32K358\Port Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Port T S T40D34M30I0R0\examples\S32DS\S32K358\Siul2 Port Ip Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Port TS_T40D34M30I0R0\examples\S32DS\S32K396\Port_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Port_TS_T4 0D34M30I0R0\examples\S32DS\S32K396\Siul2_Port_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Port_TS_ $T40D34M30I0R0 \ | xamples \ | S32DS \ | S32K3XX \ | Port_Example_S32K344 \ | include_D: \ | NXP \ | S32DS \ | Software \ | Platform SDK_S32K3 \ | RTD \ | Port_TS_T40D3 \ |$ 4M30I0R0\examples\S32DS\S32K3XX\Siul2_Port_Ip_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Port_TS_T40 D34M30I0R0\examples\S32DS\S32M276\Port_Example_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Port_TS_T40D34M 30I0R0\examples\S32DS\S32M276\Siul2_Port_Ip_Example_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D 34M30I0R0\examples\EBT\S32K358\Pwm_example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M30I $0R0 | examples | EBT | S32K396 | Pwm_example_S32K396 | include, D: | NXP | S32DS, 3.5 | S32DS | software | Platform SDK_S32K3 | RTD | Pwm_TS_T40D34M30I0R0 | example_S32K3 | Property | P$ $mples \ EBT \ S32K3XX \ Pwm_example_S32K344 \ include, D:\ NXP \ S32DS. Software \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ RTD \ Pwm_TS_T40D34M30I0R0 \ examples \ Platform SDK_S32K3 \ Pwm_Examples \ Pwm_Exam$ S32DS\S32K358\Emios Pwm Ip Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Pwm TS T40D34M30I0R0\exa mples\S32DS\S32K358\Flexio_Pwm_Ip_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M30I0R 0\examples\S32DS\S32K358\Pwm_example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M30I0R0\examplex mples\S32DS\S32K396\Emios_Pwm_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M30I0R 0\examples\S32DS\S32K396\Flexio_Pwm_Ip_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T40D34M $30I0R0\c xamples\c S32DS\c S32K396\c Pwm_example_S32K396\c include,D:\c NXP\c S32DS\c S32DS\c software\c PlatformSDK_S32K3\c RTD\c Pwm_TS_T40D34M30I0$ R0\examples\S32DS\S32K3XX\Emios Pwm Ip Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Pwm TS T40D34 M30I0R0\examples\S32DS\S32K3XX\Flexio_Pwm_Ip_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Pwm_TS_T $40D34M30I0R0 \\ | examples \\ S32DS \\ S32K3XX \\ Pwm_example_S32K344 \\ | include,D: \\ | NXP \\ S32DS. \\ | S32DS \\ | software \\ | PlatformSDK_S32K3 \\ | RTD \\ | Rm_TS_T40D34M30I0R0 \\ | PlatformSDK_S32K3 \\ | RTD \\ | RTD$ $M3010R0 \ | Example \ EBT \ S32K358 \ | Example \ S32K358 \ | Ex$ M30I0R0\examples\EBT\S32K358\Rm Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\example xamples\EBT\S32K358\Rm_Mscm_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\exa mples\EBT\S32K358\Rm_Sema42_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\exam ples/EBT\S32K358\Rm_Virt_Wrap_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\exa mples\EBT\S32K358\Rm Xbic Example S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\example s/EBT\S32K396\Rm DMAMUX Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\example s\EBT\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\S32K 396\Rm_Mscm_Example_\$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_\$32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\\$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_\$32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\\$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_\$32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\\$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_\$32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\\$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK_\$\text{S32K3}\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\\$32K396\include,D:\NXP\\$32DS\square\Platform\$DR_\$\text{S32K3}\RTD\Rm_\$\text{TS_T40D34M30I0R0}\examples\EBT\\$32K396\include,D:\NXP\\$32DS\square\Platform\$\text{S32K3}\RTD\Rm_\$\text{TS_T40D34M30I0R0}\examples\EBT\\$32K396\include,D:\ndot\text{S32K396\include}\examples\EBT\\$32K396\include\text{S32K3}\examples\EBT\\$32K396\include\text{S32K396\include}\example\text{S32K396\include}\example\text{S32K396\include}\example\text{S32K396\include}\example\text{S32K396\include}\example\exa Rm Sema42 Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\S32K396\R m Virt Wrap Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\S32K396\include,D:\NXP\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\S32K3\RTD\Rm TS T40D34M3\RTD\Rm TS T40D34M3\RTD\Rm TS T40D34M3\RTD\Rm TS T40D34M3\RTD\Rm TS T40D34M3\RTD\Rm TS T40D34M3\RTD\RTD\RTD\R Rm Xbic Example \$32K396\include,D:\NXP\\$32DS.3.5\\$32DS\software\Platform\$DK \$32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\\$32K3XX\Rm DMAMUX_Example_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Rm

Example S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\S32K3XX\Rm Mscm Ex ample_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Rm_Sema42_Exa mple_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\EBT\S32K3XX\Rm_Virt_Wrap_Ex ample S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\EBT\S32K3XX\Rm Xbic Exampl e S32K344\include,D:\NXP\S32DS,3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\S32DS\S32K358\Rm DMAMUX Exa mple S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\S32DS\S32K358\Rm Example S3 2K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K358\Rm_Mscm_Example_S32 K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K358\Rm_Virt_Wrap_Example_ S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\S32DS\S32K358\Rm Xbic Example S32 K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K396\Rm_DMAMUX_Example_S 32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS\S32DS\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS\S32DS\S32DS\S32DS\S32K396\Rm_Example_S32K396\include,D:\NXP\S32DS\S32D nclude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K396\Rm_Mscm_Example_S32K396\in clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K396\Rm_Virt_Wrap_Example_S32K396 \include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K396\Rm_Xbic_Example_S32K396\in clude,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\S32DS\S32K3XX\Rm DMAMUX Example S32K344 \include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Rm TS T40D34M30I0R0\examples\S32DS\S32K3XX\Rm Example S32K344\include D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Rm_Mscm_Example_S32K344\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Rm_Virt_Wrap_Example_S32K344\inclu de,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Rm_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Rm_Xbic_Example_S32K344\include, D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\examples\EBT\S32K358\Sent_Receiver_S32K358\include,D:\NXP\S S32DS\software\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\examples\EBT\\$32K3XX\Sent_Receiver_S32K344\include,D:\NXP\S32DS.3.5\S32DS\s oftware\PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\examples\EBT\S32M276\Sent_Receiver_S32M276\include,D:\NXP\S32DS.3.5\S32DS\software \PlatformSDK_S32K3\RTD\Sent_TS_T40D34M30I0R0\examples\S32DS\S32K358\Sent_Receiver_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\Platfo K S32K3\RTD\Sent TS T40D34M30I0R0\examples\S32DS\S32K3XX\Sent Receiver S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32 K3\RTD\Spi_TS_T40D34M30I0R0\examples\EBT\S32K358\Spi_HalfDuplexTransfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32 K3\RTD\Spi_TS_T40D34M30I0R0\examples\EBT\S32K358\Spi_Transfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi _TS_T40D34M30I0R0\examples\EBT\S32K396\Spi_HalfDuplexTransfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi _TS_T40D34M30I0R0\examples\EBT\S32K396\Spi_Transfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D3 4M30I0R0\examples\EBT\S32K3XX\Spi_HalfDuplexTransfer_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D 34M30I0R0\examples\EBT\S32K3XX\Spi_Transfer_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D34M30I0R0 \examples\S32DS\S32K358\Lpspi_Flexio_Ip_Transfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D34M30I 0R0\examples\S32DS\S32K358\Lpspi_Ip_HalfDuplexTransfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D 34M30I0R0\examples\S32DS\S32K358\Spi_HalfDuplexTransfer_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T4 0D34M30I0R0\examples\S32DS\S32K358\Spi Transfer S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Spi TS T40D34M30I0R0\examples\S32DS\software\PlatformSDK S32K3\RTD\Spi TS T40D34M30I0R0\examples\S12\RTD\Spi TS T4 IORO\examples\S32DS\S32K396\Lpspi_Flexio_Ip_Transfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS_T40D34 M30I0R0\examples\S32DS\S32K396\Lpspi Ip_HalfDuplexTransfer_S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Spi_TS $T40D34M30I0RO \ | cxamples \ | S32DS \ | S32K396 \ | Find the property of th$ S T40D34M30I0R0\examples\S32DS\S32K396\Spi Transfer S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Spi TS T40D3 4M30I0R0\examples\S32DS\S32K3XX\Lpspi Flexio Ip Transfer S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Spi TS T4 0D34M30I0R0\examples\S32DS\S32K3XX\Lpspi_Ip_HalfDuplexTransfer_S32K344\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3XRTD\S pi_TS_T40D34M30I0R0\examples\S32DS\S32K3XX\Spi_HalfDuplexTransfer_S32K344\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK_S32K3\RT D\Spi TS T40D34M30I0R0\examples\S32DS\S32K3XX\Spi Transfer S32K344\include,D:\NXP\S32DS\.3.5\S32DS\software\PlatformSDK S32K3\RTD\Uart T S_T40D34M30I0R0\examples\EBT\S32K358\Uart_Example_S32K358\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK_S32K3\RTD\Uart_TS_T40D3 4M30I0R0\examples\EBT\S32K396\Uart Example S32K396\include,D:\NXP\S32DS.3.5\S32DS\software\PlatformSDK S32K3\RTD\Uart TS T40D34M30I0R 0\examples\EBT\\$32K3XX\Uart_Example_\$32K344\include,D:\NXP\\$32D\$.3.5\\$32D\$\software\Platform\$DK_\$32K3\RTD\Uart_TS_T40D34M30I0R0\examples\EBT\\$32K3\RTD\Uart_TS_T40D34M30I0R0\examples\EBT\\$32K3\RTD\Uart_TS_T40D34M30I0R0\examples\EBT\\$32K3\RTD\Uart_TS_T40D34M30I0R0\examples\EBT\\$32K3\RTD\Uart_TS_T40D34M30I0R0\examples\EXAMPLES\E

Option	Value
	ples\\$32DS\\$32K358\LpuartFlexio_Uart_Ip_Example_\$32K358\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Uart_TS_T40D34M30\ 10R0\examples\\$32DS\\$32K358\Uart_Example_\$32K358\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Uart_TS_T40D34M30\ 10R0\examples\\$32DS\\$32EX396\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Uart_TS_T40D34M30\ 10R0\examples\\$32DS\\$32EX396\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Uart_TS_T40D34M30\ 10R0\examples\\$32DS\\$32K334\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Uart_TS_T4\ 0034M30\ 10R0\examples\\$22DS\\$32K3X\Uart_Example_\$32K344\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M30\ 10R0\examples\\$2EBT\\$32K358\\$8\Ugt_Example_\$32K344\include,D:\NXP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M30\ 10R0\examples\\$2EBT\\$32K358\s\gr_1\text{2}\dot_2\dot_2\nxP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M30\ 10R0\examples\\$2EBT\\$32K358\gr_1\text{2}\dot_2\nxP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M30\ 10R0\examples\\$2EBT\\$32K358\gr_1\text{2}\dot_2\nxP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M3\ 001\text{2}\dot_2\nxP\\$32\dot_3\x\\\ 00\bar\\$4\dot_2\nxP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M3\ 001\text{2}\dot_2\nxP\\$32\dot_3\x\\\ 00\bar\\$4\dot_2\nxP\\$32DS.3.5\\$32DS\software\PlatformSDK_\$32K3\RTD\Udg_TS_T4\ 0034M3\ 001\text{2}\dot_2\nxP\\$32\dot_3\x\\\\ 00\bar\\$4\dot_2\nxP\\$32\dot_3\x\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
-import-comments	D:\Jessie\6_Polyspace\I2C_MCAL_FunctionTest_20250103\Module_1\CP_Result
-lang	С
-little-endian	true
-main-generator	true
-main-generator-calls	unused
-main-generator-writes-variables	public
-misra3	mandatory-required
-O	-02
-pointer-is-32bits	true
-prog	I2C_MCAL_FunctionTest_20250103
-report-output-format	PDF
-report-template	Polyspace-Doc\Developer.rpt
-results-dir	D:\Jessie\6_Polyspace\I2C_MCAL_FunctionTest_20250103\Module_1\CP_Result
-signed-integer-overflows	forbid
-target	тсри

Option	Value
-to	Software Safety Analysis level 2
-uncalled-function-checks	none
-unsigned-integer-overflows	allow
-verif-version	1.0

Analysis Assumptions

Assumption	Issuer
External arrays of unspecified size can be safely accessed at any index	Product
Nonfinite floats (infinities and NaNs) are not considered	Product
Computations involving unsigned integers do not overflow	Product
Results of floating-point arithmetic are rounded following the IEE754 rule: round to nearest, ties to even	Product
Structure fields are not volatile unless the entire structure is volatile-qualified	Product
Stack pointers can be safely dereferenced even outside the pointed variable's scope	Product
External pointers cannot be null. They point to allocated data of sufficient size for safe dereference	Product
Absolute addresses can be safely dereferenced	Product

Coding Standard Configuration

Table 7.1. MISRA C:2012 Guidelines Configuration

Guideli ne	Description	Mode	Comment	Enabled
D1.1	Any implementation-defined behaviour on which the output of the program depends shall be documente d and understood.	required	-	yes
D2.1	All source files shall compile without any compilation errors.	required	-	yes
D3.1	All code shall be traceable to documented requirements.	required	Not enforceable	no
D4.1	Run-time failures shall be minimized.	required	-	yes
D4.2	All usage of assembly language should be documented.	advisory	Not enforceable	no
D4.3	Assembly language shall be encapsulated and isolated.	required	-	yes
D4.4	Sections of code should not be "commented out".	advisory	-	no
D4.5	Identifiers in the same name space with overlapping visibility should be typographically unambiguous.	advisory	-	no

Guideli ne	Description	Mode	Comment	Enabled
D4.6	typedefs that indicate size and signedness should be used in place of the basic numerical types.	advisory	-	no
D4.7	If a function returns error information, then that error information shall be tested.	required	Supported in Bug Finder only	no
D4.8	If a pointer to a structure or union is never dereferenced within a translation unit, then the implementati on of the object should be hidden.	advisory	-	no
D4.9	A function should be used in preference to a function-like macro where they are interchangeable.	advisory	-	no
D4.10	Precautions shall be taken in order to prevent the contents of a header file being included more than once .	required	-	yes
D4.11	The validity of values passed to library functions shall be checked.	required	-	yes
D4.12	Dynamic memory allocation shall not be used.	required	-	yes
D4.13	Functions which are designed to provide operations on a resource should be called in an appropriate seq uence.	advisory	Supported in Bug Finder only	no
D4.14	The validity of values received from external sources shall be checked.	required	Supported in Bug Finder only	no
1.1	The program shall contain no violations of the standard C syntax and constraints, and shall not exceed the implementation's translation limits.	required	-	yes
1.2	Language extensions should not be used.	advisory	-	no
1.3	There shall be no occurrence of undefined or critical unspecified behaviour.	required	-	yes
2.1	A project shall not contain unreachable code.	required	-	yes
2.2	There shall be no dead code.	required	-	yes
2.3	A project should not contain unused type declarations.	advisory	-	no
2.4	A project should not contain unused tag declarations.	advisory	-	no
2.5	A project should not contain unused macro declarations.	advisory	-	no
2.6	A function should not contain unused label declarations.	advisory	-	no
2.7	There should be no unused parameters in functions.	advisory	-	no
3.1	The character sequences /* and // shall not be used within a comment.	required	-	yes
3.2	Line-splicing shall not be used in // comments.	required	-	yes
4.1	Octal and hexadecimal escape sequences shall be terminated.	required	-	yes
4.2	Trigraphs should not be used.	advisory	-	no
5.1	External identifiers shall be distinct.	required	-	yes
5.2	Identifiers declared in the same scope and name space shall be distinct.	required	-	yes
5.3	An identifier declared in an inner scope shall not hide an identifier declared in an outer scope.	required	-	yes
5.4	Macro identifiers shall be distinct.	required	-	yes

Guideli ne	Description	Mode	Comment	Enabled
5.5	Identifiers shall be distinct from macro names.	required	-	yes
5.6	A typedef name shall be a unique identifier.	required	-	yes
5.7	A tag name shall be a unique identifier.	required	-	yes
5.8	Identifiers that define objects or functions with external linkage shall be unique.	required	-	yes
5.9	Identifiers that define objects or functions with internal linkage should be unique.	advisory	-	no
6.1	Bit-fields shall only be declared with an appropriate type.	required	-	yes
6.2	Single-bit named bit fields shall not be of a signed type.	required	-	yes
7.1	Octal constants shall not be used.	required	-	yes
7.2	A "u" or "U" suffix shall be applied to all integer constants that are represented in an unsigned type.	required	-	yes
7.3	The lowercase character "l" shall not be used in a literal suffix.	required	-	yes
7.4	A string literal shall not be assigned to an object unless the object's type is "pointer to const-qualified char" .	required	-	yes
8.1	Types shall be explicitly specified.	required	-	yes
8.2	Function types shall be in prototype form with named parameters.	required	-	yes
8.3	All declarations of an object or function shall use the same names and type qualifiers.	required	-	yes
8.4	A compatible declaration shall be visible when an object or function with external linkage is defined.	required	-	yes
8.5	An external object or function shall be declared once in one and only one file.	required	-	yes
8.6	An identifier with external linkage shall have exactly one external definition.	required	-	yes
8.7	Functions and objects should not be defined with external linkage if they are referenced in only one translation unit.	advisory	-	no
8.8	The static storage class specifier shall be used in all declarations of objects and functions that have internal linkage.	required	-	yes
8.9	An object should be defined at block scope if its identifier only appears in a single function.	advisory	-	no
8.10	An inline function shall be declared with the static storage class.	required	-	yes
8.11	When an array with external linkage is declared, its size should be explicitly specified.	advisory	-	no
8.12	Within an enumerator list, the value of an implicitly-specified enumeration constant shall be unique.	required	-	yes
8.13	A pointer should point to a const-qualified type whenever possible.	advisory	-	no
8.14	The restrict type qualifier shall not be used.	required	-	yes
9.1	The value of an object with automatic storage duration shall not be read before it has been set.	mandatory	-	yes
9.2	The initializer for an aggregate or union shall be enclosed in braces.	required	-	yes
9.3	Arrays shall not be partially initialized.	required	-	yes

Guideli ne	Description	Mode	Comment	Enabled
9.4	An element of an object shall not be initialized more than once.	required	-	yes
9.5	Where designated initializers are used to initialize an array object the size of the array shall be specified explicitly.	required	-	yes
10.1	Operands shall not be of an inappropriate essential type.	required	-	yes
10.2	Expressions of essentially character type shall not be used inappropriately in addition and subtraction op erations.	required	-	yes
10.3	The value of an expression shall not be assigned to an object with a narrower essential type or of a differe nt essential type category.	required	-	yes
10.4	Both operands of an operator in which the usual arithmetic conversions are performed shall have the sa me essential type category.	required	-	yes
10.5	The value of an expression should not be cast to an inappropriate essential type.	advisory	-	no
10.6	The value of a composite expression shall not be assigned to an object with wider essential type.	required	-	yes
10.7	If a composite expression is used as one operand of an operator in which the usual arithmetic conversions are performed then the other operand shall not have wider essential type.	required	-	yes
10.8	The value of a composite expression shall not be cast to a different essential type category or a wider essential type.	required	-	yes
11.1	Conversions shall not be performed between a pointer to a function and any other type.	required	-	yes
11.2	Conversions shall not be performed between a pointer to an incomplete type and any other type.	required	-	yes
11.3	A cast shall not be performed between a pointer to object type and a pointer to a different object type.	required	-	yes
11.4	A conversion should not be performed between a pointer to object and an integer type.	advisory	-	no
11.5	A conversion should not be performed from pointer to void into pointer to object.	advisory	-	no
11.6	A cast shall not be performed between pointer to void and an arithmetic type.	required	-	yes
11.7	A cast shall not be performed between pointer to object and a non-integer arithmetic type.	required	-	yes
11.8	A cast shall not remove any const or volatile qualification from the type pointed to by a pointer.	required	-	yes
11.9	The macro NULL shall be the only permitted form of integer null pointer constant.	required	-	yes
12.1	The precedence of operators within expressions should be made explicit.	advisory	-	no
12.2	The right hand operand of a shift operator shall lie in the range zero to one less than the width in bits of t he essential type of the left hand operand.	required	-	yes
12.3	The comma operator should not be used	advisory	-	no
12.4	Evaluation of constant expressions should not lead to unsigned integer wrap-around.	advisory	-	no
12.5	The sizeof operator shall not have an operand which is a function parameter declared as "array of type".	mandatory	-	yes
13.1	Initializer lists shall not contain persistent side effects.	required	-	yes

Guideli ne	Description	Mode	Comment	Enabled
13.2	The value of an expression and its persistent side effects shall be the same under all permitted evaluation orders.	required	-	yes
13.3	A full expression containing an increment (++) or decrement () operator should have no other potential s ide effects other than that caused by the increment or decrement operator.	advisory	-	no
13.4	The result of an assignment operator should not be used.	advisory	-	no
13.5	The right hand operand of a logical && or operator shall not contain persistent side effects.	required	-	yes
13.6	The operand of the sizeof operator shall not contain any expression which has potential side effects.	mandatory	-	yes
14.1	A loop counter shall not have essentially floating type.	required	-	yes
14.2	A for loop shall be well-formed.	required	-	yes
14.3	Controlling expressions shall not be invariant.	required	-	yes
14.4	The controlling expression of an if statement and the controlling expression of an iteration-statement shal l have essentially Boolean type.	required	-	yes
15.1	The goto statement should not be used.	advisory	-	no
15.2	The goto statement shall jump to a label declared later in the same function.	required	-	yes
15.3	Any label referenced by a goto statement shall be declared in the same block, or in any block enclosing the goto statement.	required	-	yes
15.4	There should be no more than one break or goto statement used to terminate any iteration statement.	advisory	-	no
15.5	A function should have a single point of exit at the end.	advisory	-	no
15.6	The body of an iteration-statement or a selection-statement shall be a compound-statement.	required	-	yes
15.7	All if else if constructs shall be terminated with an else statement.	required	-	yes
16.1	All switch statements shall be well-formed.	required	-	yes
16.2	A switch label shall only be used when the most closely-enclosing compound statement is the body of a switch statement.	required	-	yes
16.3	An unconditional break statement shall terminate every switch-clause.	required	-	yes
16.4	Every switch statement shall have a default label.	required	-	yes
16.5	A default label shall appear as either the first or the last switch label of a switch statement.	required	-	yes
16.6	Every switch statement shall have at least two switch-clauses.	required	-	yes
16.7	A switch-expression shall not have essentially Boolean type.	required	-	yes
17.1	The features of <stdarg.h> shall not be used.</stdarg.h>	required	-	yes
17.2	Functions shall not call themselves, either directly or indirectly.	required	-	yes
17.3	A function shall not be declared implicitly.	mandatory	-	yes

Guideli ne	Description	Mode	Comment	Enabled
17.4	All exit paths from a function with non-void return type shall have an explicit return statement with an expression.	mandatory	-	yes
17.5	The function argument corresponding to a parameter declared to have an array type shall have an appropriate number of elements.	advisory	-	no
17.6	The declaration of an array parameter shall not contain the static keyword between the [].	mandatory	-	yes
17.7	The value returned by a function having non-void return type shall be used.	required	-	yes
17.8	A function parameter should not be modified.	advisory	-	no
18.1	A pointer resulting from arithmetic on a pointer operand shall address an element of the same array as th at pointer operand.	required	-	yes
18.2	Subtraction between pointers shall only be applied to pointers that address elements of the same array.	required	-	yes
18.3	The relational operators >, >=, < and <= shall not be applied to objects of pointer type except where they p oint into the same object.	required	-	yes
18.4	The +, -, += and -= operators should not be applied to an expression of pointer type.	advisory	-	no
18.5	Declarations should contain no more than two levels of pointer nesting.	advisory	-	no
18.6	The address of an object with automatic storage shall not be copied to another object that persists after the first object has ceased to exist.	required	-	yes
18.7	Flexible array members shall not be declared.	required	-	yes
18.8	Variable-length array types shall not be used.	required	-	yes
19.1	An object shall not be assigned or copied to an overlapping object.	mandatory	-	yes
19.2	The union keyword should not be used.	advisory	-	no
20.1	#include directives should only be preceded by preprocessor directives or comments.	advisory	-	no
20.2	The ', " or \ characters and the /* or // character sequences shall not occur in a header file name.	required	-	yes
20.3	The #include directive shall be followed by either a <filename> or "filename"sequence.</filename>	required	-	yes
20.4	A macro shall not be defined with the same name as a keyword.	required	-	yes
20.5	#undef should not be used.	advisory	-	no
20.6	Tokens that look like a preprocessing directive shall not occur within a macro argument.	required	-	yes
20.7	Expressions resulting from the expansion of macro parameters shall be enclosed in parentheses.	required	-	yes
20.8	The controlling expression of a #if or #elif preprocessing directive shall evaluate to 0 or 1.	required	-	yes
20.9	All identifiers used in the controlling expression of #if or #elif preprocessing directives shall be #define'd before evaluation.	required	-	yes
20.10	The # and ## preprocessor operators should not be used.	advisory	-	no

Guideli ne	Description	Mode	Comment	Enabled
20.11	A macro parameter immediately following a # operator shall not immediately be followed by a ## operator.	required	-	yes
20.12	A macro parameter used as an operand to the # or ## operators, which is itself subject to further macro re placement, shall only be used as an operand to these operators.	required	-	yes
20.13	A line whose first token is # shall be a valid preprocessing directive.	required	-	yes
20.14	All #else, #elif and #endif preprocessor directives shall reside in the same file as the #if, #ifdef or #ifndef d irective to which they are related.	required	-	yes
21.1	#define and #undef shall not be used on a reserved identifier or reserved macro name.	required	-	yes
21.2	A reserved identifier or macro name shall not be declared.	required	-	yes
21.3	The memory allocation and deallocation functions of <stdlib.h> shall not be used.</stdlib.h>	required	-	yes
21.4	The standard header file <setjmp.h> shall not be used.</setjmp.h>	required	-	yes
21.5	The standard header file <signal.h> shall not be used.</signal.h>	required	-	yes
21.6	The Standard Library input/output functions shall not be used.	required	-	yes
21.7	The atof, atoi, atol, and atoll functions of <stdlib.h> shall not be used.</stdlib.h>	required	-	yes
21.8	The library functions abort, exit and system of <stdlib.h> shall not be used.</stdlib.h>	required	-	yes
21.9	The library functions bsearch and qsort of <stdlib.h> shall not be used.</stdlib.h>	required	-	yes
21.10	The Standard Library time and date functions shall not be used.	required	-	yes
21.11	The standard header file <tgmath.h> shall not be used.</tgmath.h>	required	-	yes
21.12	The exception handling features of <fenv.h> should not be used.</fenv.h>	advisory	-	no
21.13	Any value passed to a function in <ctype.h> shall be representable as an unsigned char or be the value EO F.</ctype.h>	mandatory	Supported in Bug Finder only	no
21.14	The Standard Library function memcmp shall not be used to compare null terminated strings.	required	Supported in Bug Finder only	no
21.15	The pointer arguments to the Standard Library functions memcpy, memmove and memcmp shall be point ers to qualified or unqualified versions of compatible types.	required	-	yes
21.16	The pointer arguments to the Standard Library function memcmp shall point to either a pointer type, an essentially signed type, an essentially Boolean type or an essentially enum type.	required	-	yes
21.17	Use of the string handling functions from <string.h> shall not result in accesses beyond the bounds of the objects referenced by their pointer parameters.</string.h>	mandatory	Supported in Bug Finder only	no
21.18	The size_t argument passed to any function in <string.h> shall have an appropriate value.</string.h>	mandatory	Supported in Bug Finder only	no
21.19	The pointers returned by the Standard Library functions localeconv, getenv, setlocale or, strerror shall only be used as if they have pointer to const-qualified type.	mandatory	Supported in Bug Finder only	no

Guideli ne	Description	Mode	Comment	Enabled
21.20	The pointer returned by the Standard Library functions asctime, ctime, gmtime, localtime, localeconv, get env, setlocale or strerror shall not be used following a subsequent call to the same function.	mandatory	Supported in Bug Finder only	no
22.1	All resources obtained dynamically by means of Standard Library functions shall be explicitly released.	required	Supported in Bug Finder only	no
22.2	A block of memory shall only be freed if it was allocated by means of a Standard Library function.	mandatory	Supported in Bug Finder only	no
22.3	The same file shall not be open for read and write access at the same time on different streams.	required	Supported in Bug Finder only	no
22.4	There shall be no attempt to write to a stream which has been opened as read-only.	mandatory	Supported in Bug Finder only	no
22.5	A pointer to a FILE object shall not be dereferenced.	mandatory	-	yes
22.6	The value of a pointer to a FILE shall not be used after the associated stream has been closed.	mandatory	Supported in Bug Finder only	no
22.7	The macro EOF shall only be compared with the unmodified return value from any Standard Library function capable of returning EOF.	required	Supported in Bug Finder only	no
22.8	The value of errno shall be set to zero prior to a call to an errno-setting-function.	required	Supported in Bug Finder only	no
22.9	The value of errno shall be tested against zero after calling an errno-setting-function.	required	Supported in Bug Finder only	no
22.10	The value of errno shall only be tested when the last function to be called was an errno-setting-function.	required	Supported in Bug Finder only	no

Files with compilation errors (files partially analyzed)

Table 7.2. Files with compilation errors (files partially analyzed)

File
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:
$D:\ \ D:\ \ \ \ D:\ \ \ \ \ \ \ \ \ \ \ $
$D:\ \ D:\ \ \ \ D:\ \ \ \ \ \ \ \ \ \ \ $
$D:\ \ D:\ \ \ \ D:\ \ \ \ \ \ \ \ \ \ \ $
D: lem:lem:lem:lem:lem:lem:lem:lem:lem:lem:

Chapter 8. Appendix 2 - Definitions

Table 8.1. Abbreviations

Abbreviation	Definition
Col	Column
Jus	Justified
SQO	Software Quality Objectives
NA	Not Available