# 1. Description

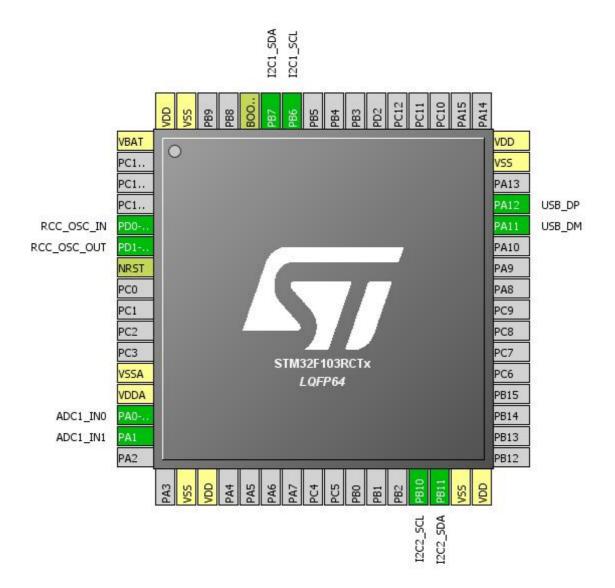
## 1.1. Project

Project Name	HAL_I2C
Board Name	HAL_I2C
Generated with:	STM32CubeMX 4.23.0
Date	11/19/2017

## 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RCTx
MCU Package	LQFP64
MCU Pin number	64

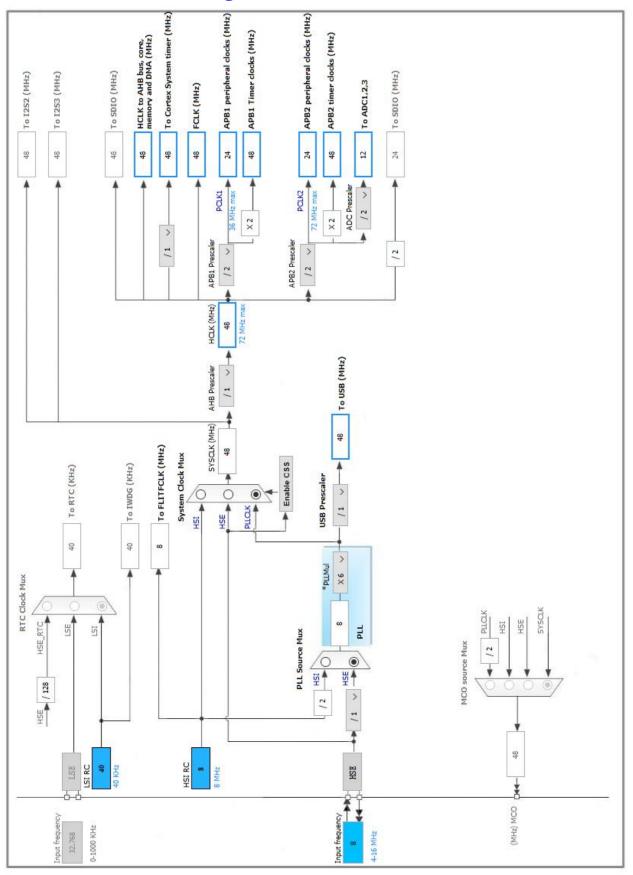
# 2. Pinout Configuration



# 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
14	PA0-WKUP	I/O	ADC1_IN0	
15	PA1	I/O	ADC1_IN1	
18	VSS	Power		
19	VDD	Power		
29	PB10	I/O	I2C2_SCL	
30	PB11	I/O	I2C2_SDA	
31	VSS	Power		
32	VDD	Power		
44	PA11	I/O	USB_DM	
45	PA12	I/O	USB_DP	
47	VSS	Power		
48	VDD	Power		
58	PB6	I/O	I2C1_SCL	
59	PB7	I/O	I2C1_SDA	
60	воото	Boot		
63	VSS	Power		
64	VDD	Power		

# 4. Clock Tree Configuration



# 5. IPs and Middleware Configuration

#### 5.1. ADC1

mode: IN0 mode: IN1

### 5.1.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Data Alignment Right alignment
Scan Conversion Mode Disabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled

ADC\_Regular\_ConversionMode:

Enable Regular Conversions Enable
Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

Rank 1

Channel Channel 0
Sampling Time 1.5 Cycles

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

#### 5.2. I2C1

12C: 12C

### 5.2.1. Parameter Settings:

**Master Features:** 

I2C Speed Mode Standard Mode
I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0
General Call address detection Disabled

#### 5.3. I2C2

12C: 12C

### 5.3.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

### 5.4. RCC

### High Speed Clock (HSE): Crystal/Ceramic Resonator

### 5.4.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 1 WS (2 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

### 5.5. SYS

**Debug: No Debug** 

Timebase Source: SysTick

#### 5.6. TIM4

mode: Clock Source

### 5.6.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 48-1 \*

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 1000-1 \*

Internal Clock Division (CKD) No Division auto-reload preload Enable \*

#### **Trigger Output (TRGO) Parameters:**

Master/Slave Mode Disable (no sync between this TIM (Master) and its Slaves

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

#### 5.7. USB

mode: Device (FS)

### 5.7.1. Parameter Settings:

#### **Basic Parameters:**

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 8 Bytes

**Power Parameters:** 

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

### 5.8. USB DEVICE

### Class For FS IP: Communication Device Class (Virtual Port Com)

#### 5.8.1. Parameter Settings:

#### **Basic Parameters:**

USBD\_MAX\_NUM\_INTERFACES (Maximum number of supported interfaces)

1
USBD\_MAX\_NUM\_CONFIGURATION (Maximum number of supported configuration)

1
USBD\_MAX\_STR\_DESC\_SIZ (Maximum size for the string descriptors)

512
USBD\_SUPPORT\_USER\_STRING (Enable user string descriptor)

Disabled
USBD\_SELF\_POWERED (Enabled self power)

Enabled

USBD\_DEBUG\_LEVEL (USBD Debug Level) 0: No debug message

**Class Parameters:** 

USB CDC Rx Buffer Size 2048
USB CDC Tx Buffer Size 2048

#### 5.8.2. Device Descriptor:

#### **Device Descriptor:**

VID (Vendor IDentifier) 1155

LANGID\_STRING (Language Identifier) English(United States)

MANUFACTURER\_STRING (Manufacturer Identifier) STMicroelectronics

#### **Device Descriptor FS:**

PID (Product IDentifier) 22336

PRODUCT\_STRING (Product Identifier)

STM32 Virtual ComPort

SERIALNUMBER\_STRING (Serial number) 0000000001A
CONFIGURATION\_STRING (Configuration Identifier) CDC Config
INTERFACE\_STRING (Interface Identifier) CDC Interface

<sup>\*</sup> User modified value

# 6. System Configuration

## 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA0-WKUP	ADC1_IN0	Analog mode	n/a	n/a	
	PA1	ADC1_IN1	Analog mode	n/a	n/a	
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	n/a	High *	
	PB7	I2C1_SDA	Alternate Function Open Drain	n/a	High *	
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	
RCC	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	

## 6.2. DMA configuration

DMA request	Stream	Direction	Priority
I2C1_RX	DMA1_Channel7	Peripheral To Memory	Low
I2C1_TX	DMA1_Channel6	Memory To Peripheral	Low

### I2C1\_RX: DMA1\_Channel7 DMA request Settings:

Mode: Circular \*
Peripheral Increment: Disable
Memory Increment: Enable \*

Peripheral Data Width: Byte
Memory Data Width: Byte

### I2C1\_TX: DMA1\_Channel6 DMA request Settings:

Mode: Circular \*

Peripheral Increment: Disable

Memory Increment: Enable \*

Peripheral Data Width: Byte
Memory Data Width: Byte

## 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
·			•
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
DMA1 channel6 global interrupt	true	0	0
DMA1 channel7 global interrupt	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
TIM4 global interrupt	true	0	0
I2C1 event interrupt	true	0	0
I2C1 error interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC1 and ADC2 global interrupts	unused		
USB high priority or CAN TX interrupts	unused		
I2C2 event interrupt	unused		
I2C2 error interrupt		unused	

<sup>\*</sup> User modified value

# 7. Power Consumption Calculator report

#### 7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103RCTx
Datasheet	14611_Rev12

#### 7.2. Parameter Selection

Temperature	25
Vdd	3.3

# 8. Software Project

## 8.1. Project Settings

Name	Value
Project Name	HAL_I2C
Project Folder	C:\Users\Zgeny\Desktop\I2C\HAL_I2C
Toolchain / IDE	EWARM
Firmware Package Name and Version	STM32Cube FW_F1 V1.6.0

## 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	