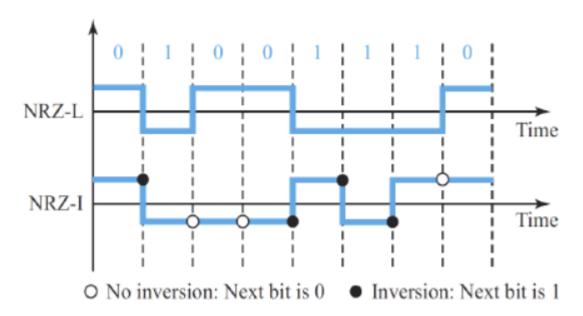
## Q1

#### a.

**Polar NRZ-L scheme**, because it matches the definition of NRZ-L: two voltage levels represent bit values — a positive voltage for 0 and a negative voltage for 1.



#### b.

It is a problem because it uses a single amplitude to represent a particular symbol. This means that if a long sequence of identical symbols appears, the average value of the signal becomes inaccurate. Since the baseline is determined using this average, it will shift toward the side where the repeated symbols occur more frequently.

#### C.

Decrease. Because the bandwidth B required by a digital signal is proportional to the signal rate S.

### d.

The relationship between data rate N and signal rate S is

$$S = \frac{N}{r}$$

Therefore

$$2S = rac{2N}{r}$$

e.

$$S_{avg} = rac{cN}{r} \ 300,000,000 = rac{rac{1}{2}N}{3} \ N = 1,800,000,000bps \ = 1.8Gbps$$

Q2

# Q 2

a.

$$n_b = \log_2 L = \log_2 128 = 7$$
 $for\ Low - pass: B = f_{
m max}$ 
 $2f_{
m max} = f_s$ 
 $N = f_s imes n_b$ 
 $= 2B imes \log_2 L$ 
 $= 600kHz imes \log_2 128$ 
 $= 600kHz imes 7$ 
 $= 4.2Mbps$ 

b.

$$When~c=rac{1}{2}, r=1: \ B_{\min}=n_b imes B \ =7 imes 300kHz \ =2.1MHz$$

 $Q_3$ 

a.

$$egin{aligned} \Delta &= rac{V_{ ext{max}} - V_{ ext{min}}}{L} \ &= rac{2 - (-2)}{4} \ &= 1 \end{aligned}$$

b.

$$0:[-2,-1)$$
 $1:[-1,0)$ 
 $2:[0,1)$ 
 $3:[1,2]$ 

c.

$$-1.5, -0.5, 0.5, 0.5$$

d.

$$0,1,2,2$$

e.

$$n_b = \log_2 L \ = \log_2 4 \ = 2$$

$$0, 1, 2, 2$$
  
 $\Rightarrow 00, 01, 10, 11$ 

Q 4

a.

$$\log_2 16 = 4bits$$

b.

$$log_2 128 = 7bits \\$$

c.

$$2^8 = 256$$