

Collector-based xv6 Primes Sieve Architecture

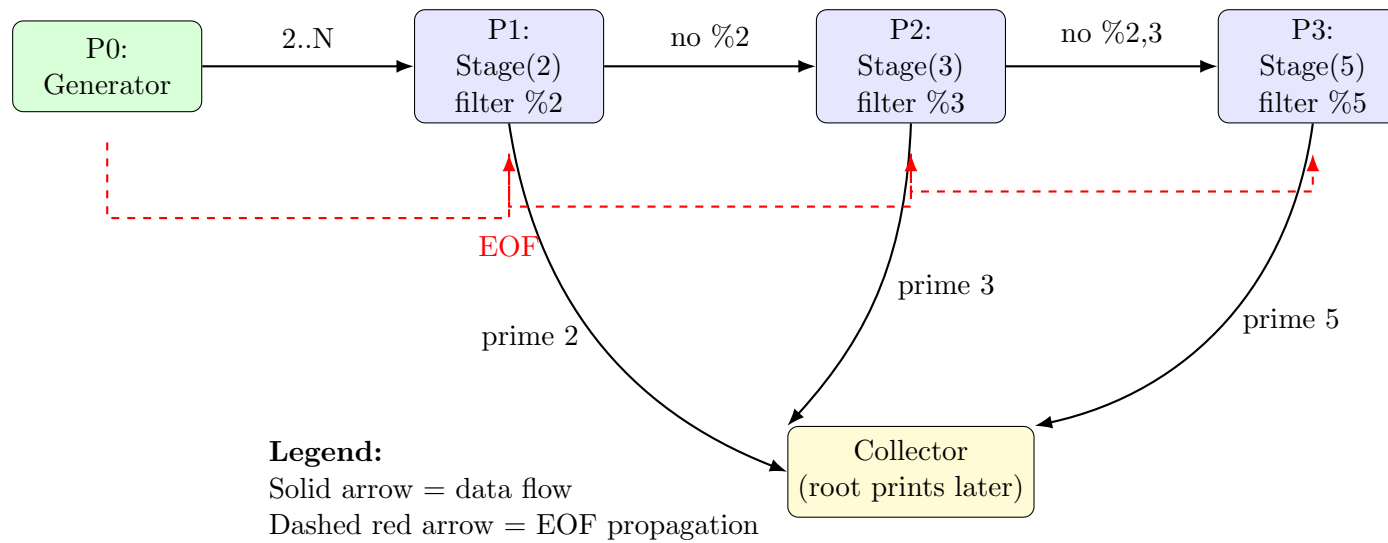


Figure 1: Process-pipeline for collector-based xv6 prime sieve

This diagram shows the generator feeding Stage(2), which filters multiples of 2, and so on. Each stage sends discovered primes to the collector through a shared report pipe. EOFs propagate left-to-right when each stage closes its write end.