

## Today's Outline

#### 7. First Order Circuits

sequential switching



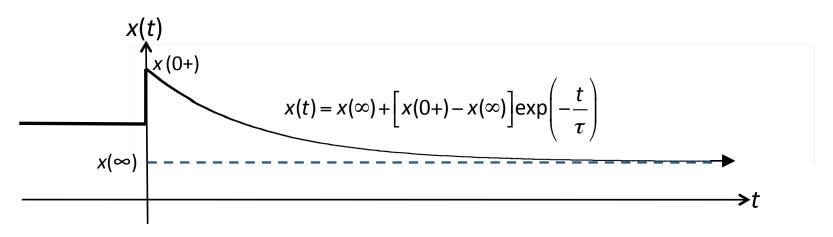
## constant input: general procedure

**step #1:** Find the initial value of the circuit variable of interest, x(0+), using circuit analysis and continuity of capacitor voltage or inductor current.

**step #2:** Find the final value of the variable of interest,  $x(\infty)$ , using dc steady state models for the capacitor or inductor.

**step #3:** Find the Thévenin equivalent resistance  $R_T$  as seen from the terminals of the capacitor or inductor. The time constant  $\tau = R_T C$  or  $\tau = L/R_T$ .

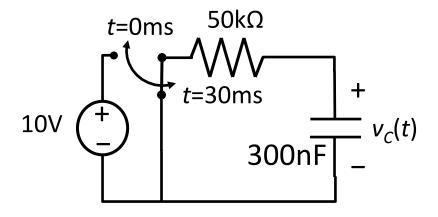
**step #4:** Construct the solution.





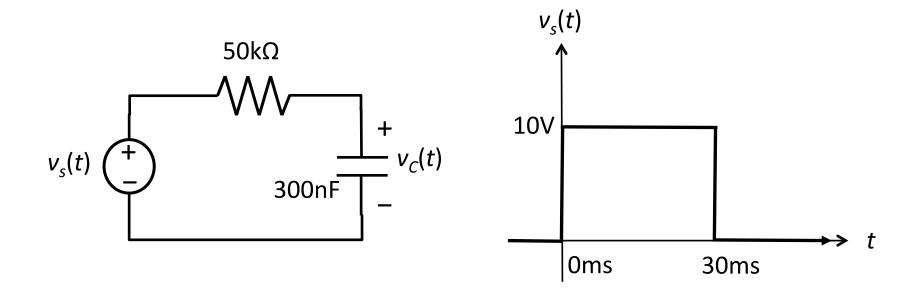
## multiple switching events

In many circuits, we are concerned with more than one switching event. Consider the RC circuit below where the switch moves twice (to the source at t=0ms, and back to the short at t=30ms).





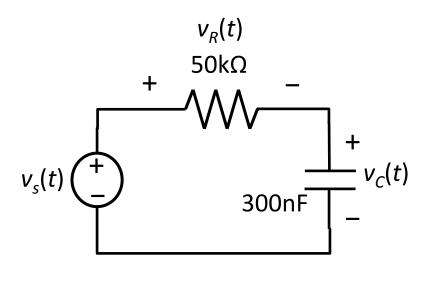
An equivalent *RC* circuit with a time-dependent source instead of a switch can be used:

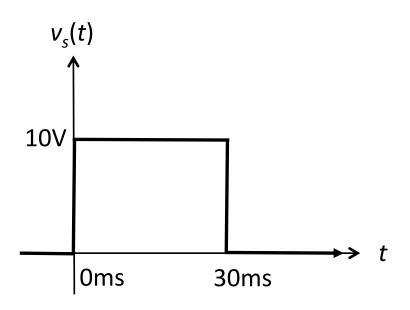


The time dependent source description is more natural when considering voltages or currents that carry information (such as audio signals, radio signals and radar signals).



For a circuit with sequential switching, we apply the general solution technique **for each time interval**. We illustrate for the *RC* circuit below. Assuming dc steady state for t<0, we find the voltages  $v_c(t)$  and  $v_R(t)$ .



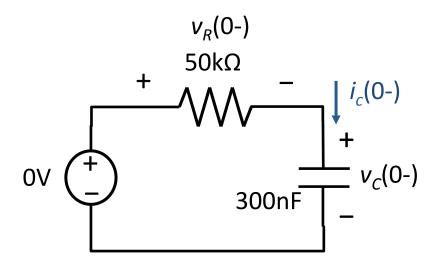




t < 0: capacitor is an open at dc steady state:  $i_c(0-) = 0$ 

Ohm's Law:  $v_R(0-) = 50k\Omega i_C(0-) = 0V$ 

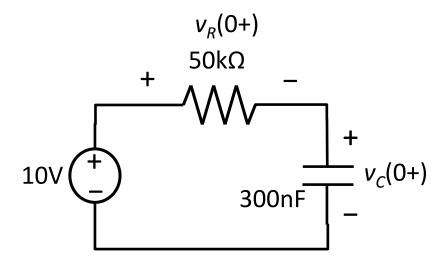
KVL: 
$$0 = 0 + v_R(0-) + v_C(0-) = 0 + 0 + v_C(0-)$$
  
 $\therefore v_C(0-) = 0$ 





t = 0+: The source voltage instantaneously changes value from 0V to 10V.

Capacitor voltage is continuous:  $v_c(0+) = v_c(0-) = 0V$ 





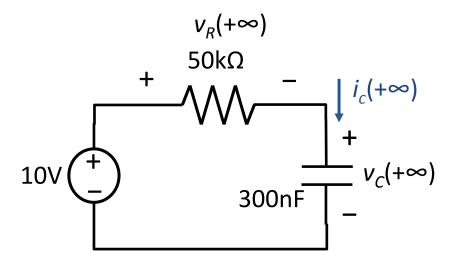
0 < *t* < 30ms :

Find the anticipated dc steady state, *assuming no further switching events* (ie. the circuit cannot be used to detect future events, time travel, etc...).

Capacitor is an open at dc steady state:  $i_c(+\infty) = 0$ 

Ohm's Law: 
$$v_R(+\infty) = 50k\Omega i_C(+\infty) = 0V$$

KVL: 
$$0 = -10V + v_R(+\infty) + v_C(+\infty) = -10V + 0V + v_C(+\infty)$$
$$\therefore v_C(+\infty) = 10V$$



The time constant is:

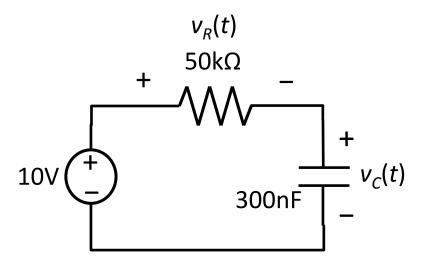
$$\tau = RC = 50$$
k $\Omega$  300nF = 15ms

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0 < t < 30 ms: Construct the capacitor voltage using the values  $v_c(0+) = 0 \text{V}, v_c(+\infty) = 10 \text{V}, \tau = 15 \text{ms}.$ 

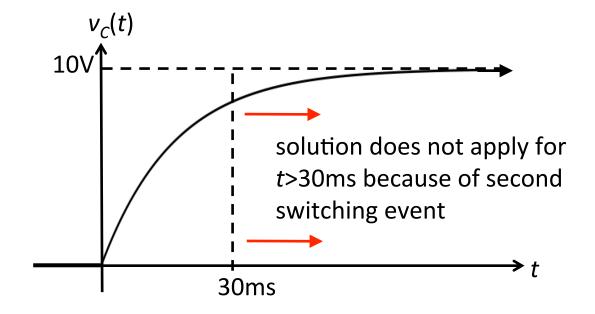
$$v_c(t) = v_c(+\infty) + \left[v_c(0+) - v_c(+\infty)\right] \exp(-t/\tau)$$
$$= 10V - 10V \exp(-t/15ms)$$



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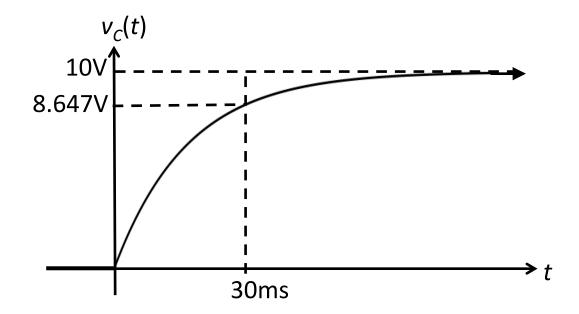
0 < t < 30 ms:



The solution found to this point applies for 0 < t < 30ms.

The switching event at t=30ms disturbs the subsequent evolution of capacitor voltage, which we now solve.

t = 30ms - :



$$v_c$$
 (30ms-) = 10V - 10V exp(-30ms / 15ms)  
= 10V - 10V exp(-2)  
= 8.647V

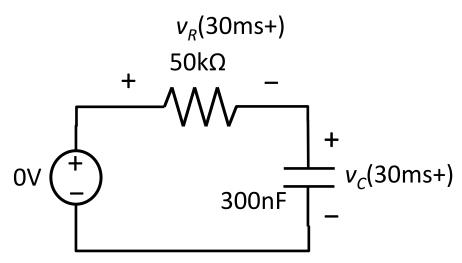


t = 30ms+: The source voltage instantaneously changes value from 10V back to 0V.

We consider the response for t>30ms using the same general solution method, but with different initial conditions and different switching time.

Capacitor voltage is continuous:

$$v_c(30\text{ms+}) = v_c(30\text{ms-}) = 8.647\text{V}$$





t > 30 ms:

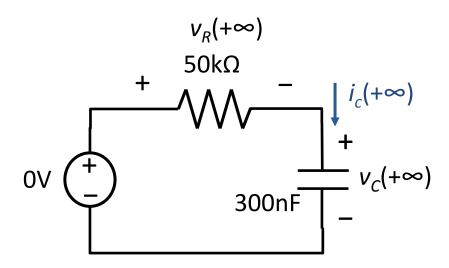
Consider the final dc steady state now anticipated.

Capacitor is an open at dc steady state:  $i_c(+\infty) = 0$ 

Ohm's Law:  $v_R(+\infty) = 50k\Omega i_C(+\infty) = 0V$ 

KVL: 
$$0 = 0V + v_R(+\infty) + v_C(+\infty) = 0V + 0V + v_C(+\infty)$$

$$\therefore v_{c}(+\infty) = 0V$$



The time constant is:

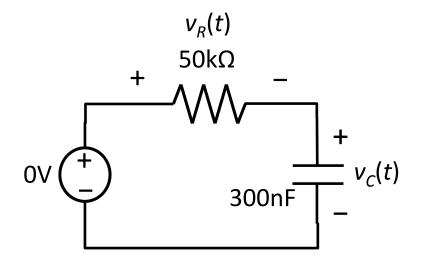
$$\tau = RC = 50$$
k $\Omega$  300nF = 15ms



t > 30 ms:

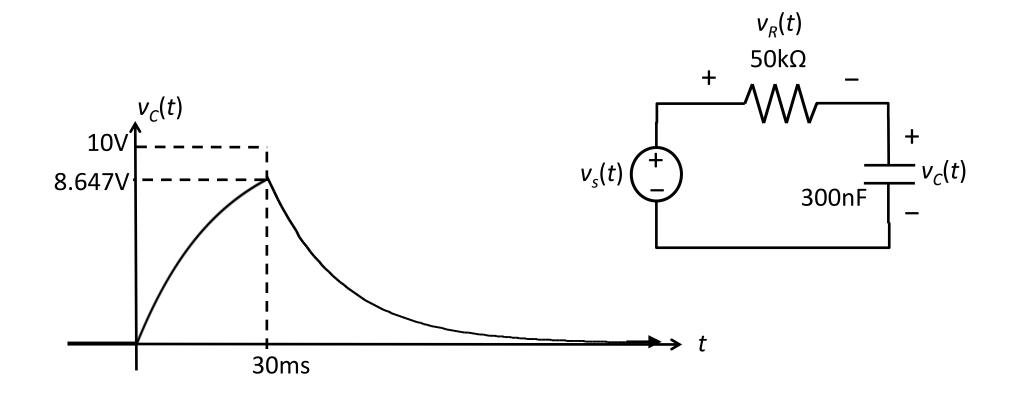
Construct the capacitor voltage using the values  $v_c(30\text{ms+}) = 8.647\text{V}, v_c(+\infty) = 0\text{V}, \tau = 15\text{ms}.$ 

$$v_c(t) = v_c(+\infty) + [v_c(30\text{ms}+) - v_c(+\infty)] \exp(-(t - 30\text{ms})/\tau)$$
  
= 8.647V exp(-(t - 30ms)/15ms)



**IMPORTANT**: note the time shift, because the switching event occurs at  $t=30\text{ms}\neq0$ . Check the value  $v_c(30\text{ms})$  to be sure.



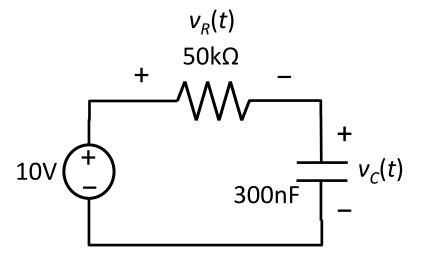


$$v_c(t) = \begin{cases} 0V & t < 0 \\ 10V - 10V \exp(-t/15\text{ms}) & 0 < t < 30\text{ms} \\ 8.647V \exp(-(t-30\text{ms})/15\text{ms}) & 30\text{ms} < t \end{cases}$$



0 < t < 30 ms: We then construct the resistor voltage over the different time intervals, using KVL here:

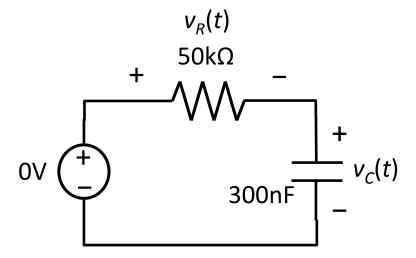
$$v_R(t) = 10V - v_C(t)$$
  
=  $10V - [10V - 10V \exp(-t / 15ms)]$   
=  $10V \exp(-t / 15ms)$ 



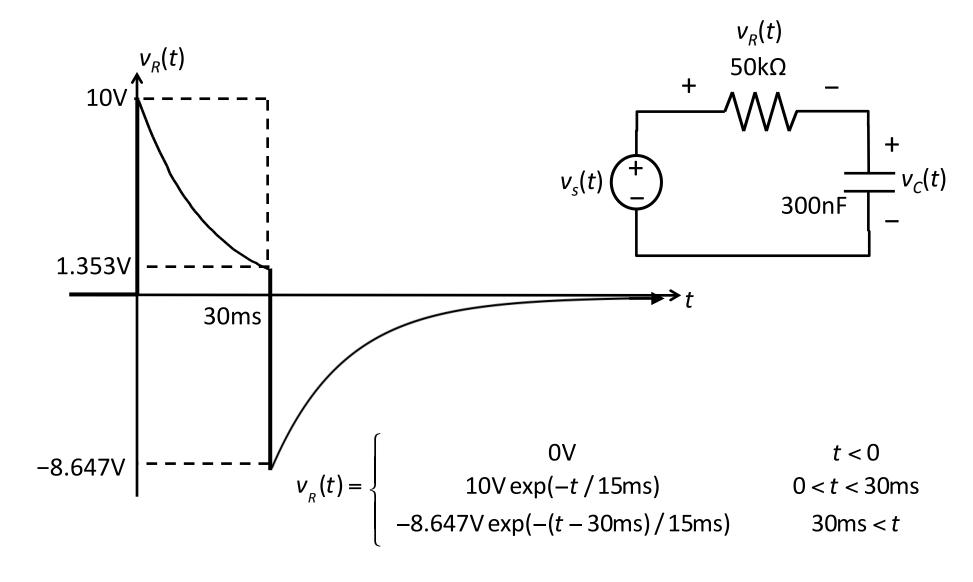


t > 30ms: We construct the resistor voltage again using KVL here:

$$v_R(t) = 0V - v_C(t)$$
  
=  $0V - [8.647V \exp(-(t - 30ms) / 15ms)]$   
=  $-8.647V \exp(-(t - 30ms) / 15ms)$ 

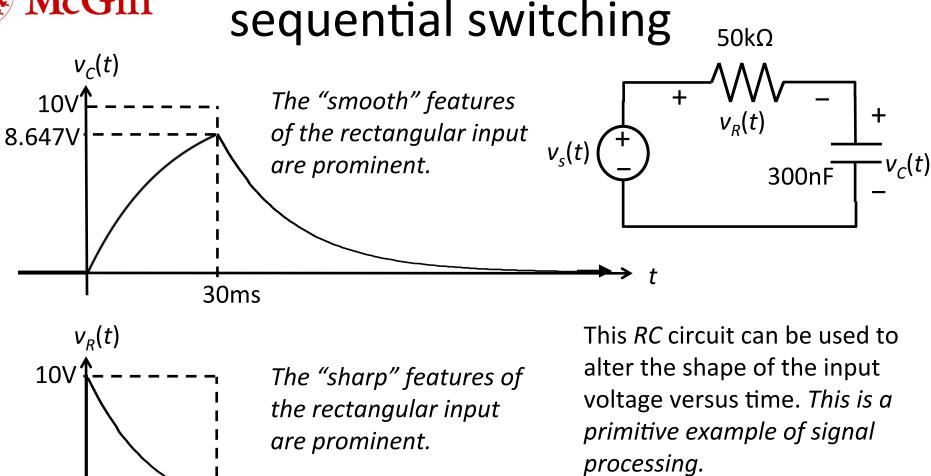








1.353V

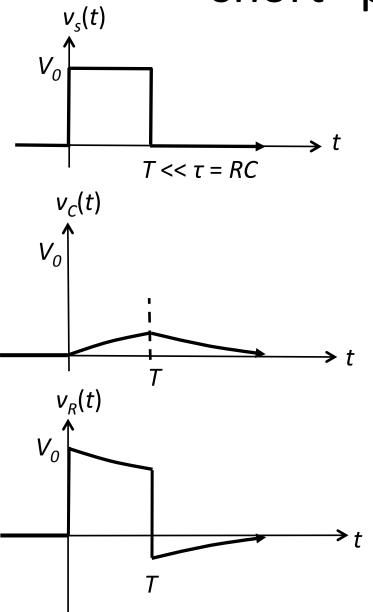


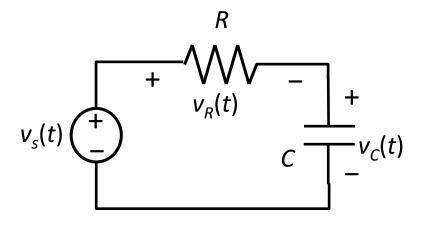
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## "short" pulse RC response





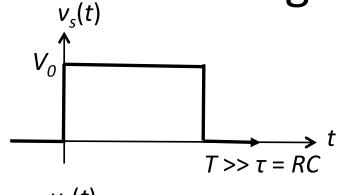
source pulse width  $T \ll \tau = RC$ 

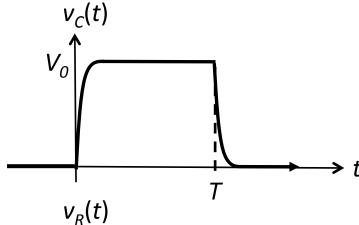
Capacitor voltage responds too slowly to reproduce the shape of the short pulse.

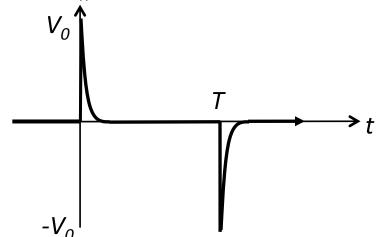
Resistor voltage (proportional to capacitor current) response reproduces the shape of the short pulse.

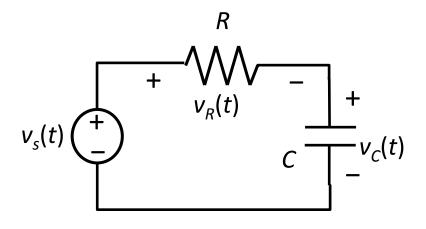


## "long" pulse RC response









source pulse width  $T \gg \tau = RC$ 

Capacitor voltage reproduces the shape of the long pulse.

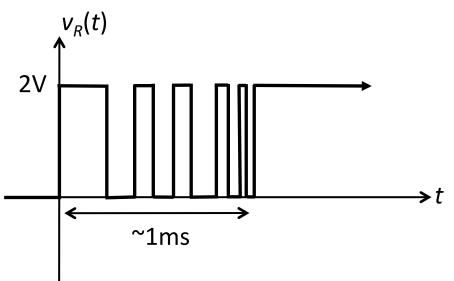
Resistor voltage (proportional to capacitor current) decays too quickly, capturing only the rising and falling edges of the pulse.

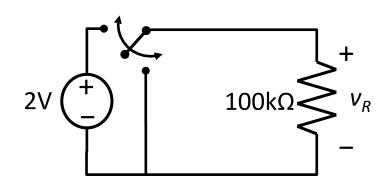


# application: "debouncer"



A contact switch "bounces" in the process of forming or breaking an electrical contact.



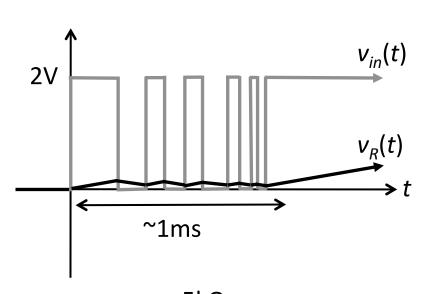


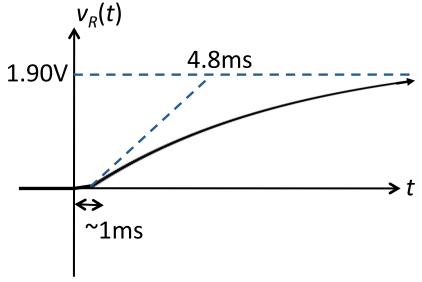
The multiple transitions can cause a single switching event to be counted several times by a digital counting circuit that acts on the transition.

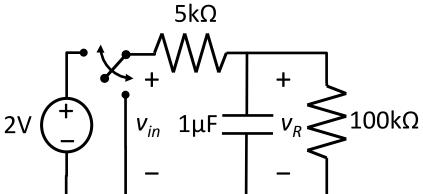
An *RC* circuit can be added to smooth out the transitions, or "debounce" the switch.



## application: "debouncer"







Analysis of the first order response:

$$v_{R}(0) = 0V$$

$$v_{R}(+\infty) = 2V \frac{100k\Omega}{100k\Omega + 5k\Omega} = 1.90V$$

$$\tau = \left(100k\Omega \mid |5k\Omega\right) \cdot 1\mu F = 4.8ms$$

The penalty is a slower response and voltage division. Can this be avoided?



### sequential switching: general procedure

#### For each time interval of constant input:

**step #1:** Find the initial value of the circuit variable of interest,  $x(t_0+)$ , using circuit analysis and continuity of capacitor voltage or inductor current.

**step #2:** Find the anticipated final value of the variable of interest,  $x(\infty)$ , using dc steady state models for the capacitor or inductor.

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