

#### Today's Outline

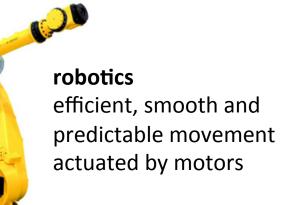
#### **6. Energy Storage Elements**

op-amp circuits with energy storage elements



#### Motivation:

#### Why is doing calculus with circuits useful?



industrial processes
maintaining temperature
(via electrical heating)
with minimum deviation
from specified target



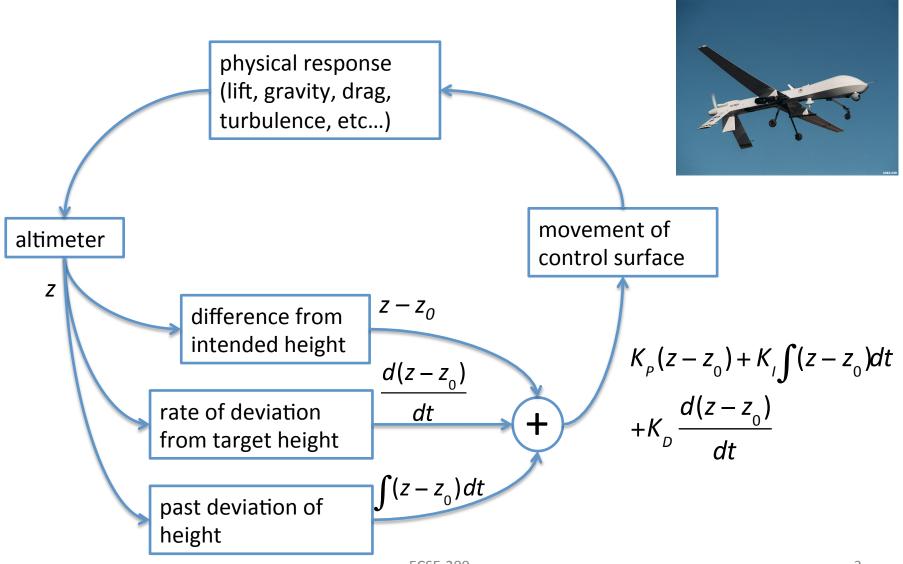
maintaining stability and direction in the presence of perturbations (eg. wind)





#### Motivation:

Why is doing calculus with circuits useful?

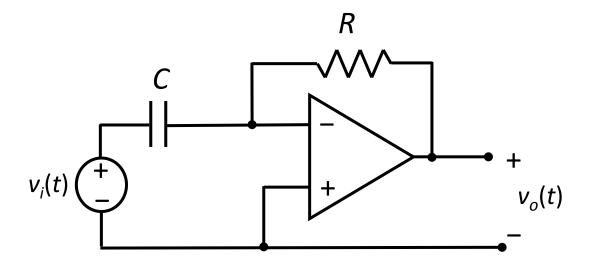


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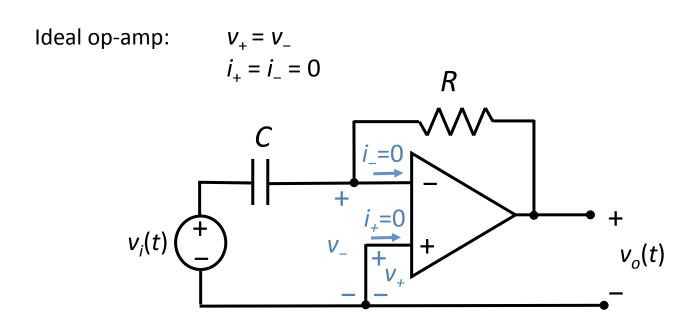
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Assuming ideal op-amp behaviour, what is  $v_o(t)$  as a function of  $v_i(t)$ ?

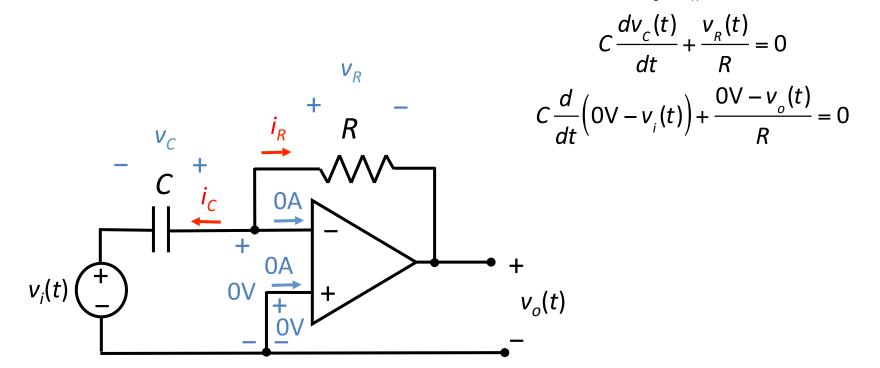








node-voltage equation at inverting input node:



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 $i_C + i_R = 0$ 

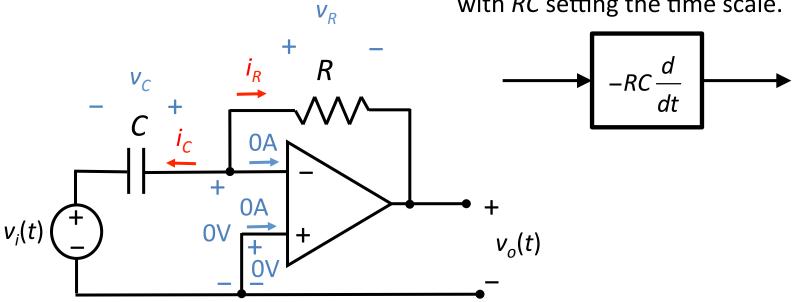


simplifying the equation:

$$C\frac{d}{dt}(0V - v_{i}(t)) + \frac{0V - v_{o}(t)}{R} = 0$$

$$v_o(t) = -RC\frac{d}{dt}v_i(t)$$

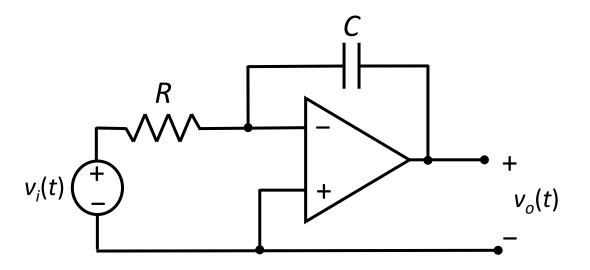
This circuit acts as a *differentiator*, with *RC* setting the time scale.



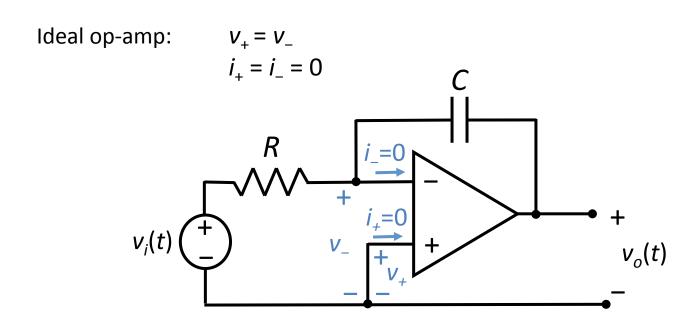
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Assuming ideal op-amp behaviour, what is  $v_o(t)$  as a function of  $v_i(t)$ ?

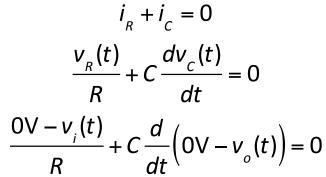


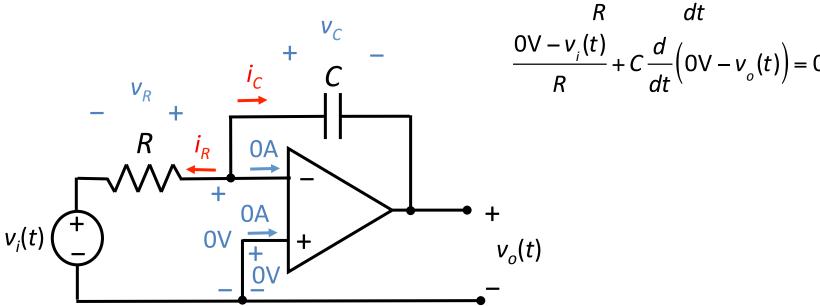






node-voltage equation at inverting input node:





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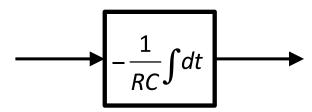


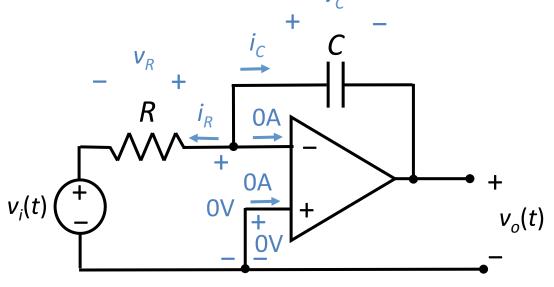
$$\frac{OV - v_{i}(t)}{R} + C \frac{d}{dt} \left( OV - v_{o}(t) \right) = 0$$

$$v_{i}(t) = -RC \frac{d}{dt} v_{o}(t)$$

$$v_o(t_1) - v_o(t_1) = -\frac{1}{RC} \int_{t_1}^{t_2} v_i(t) dt$$

This circuit acts as an *integrator*, with *RC* setting the time scale.

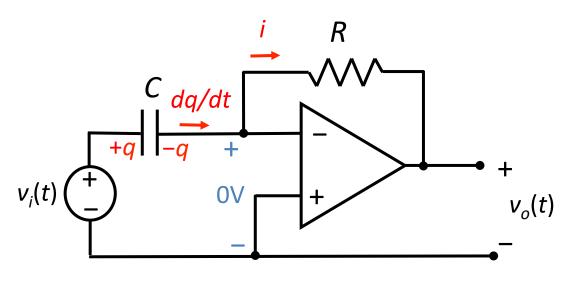


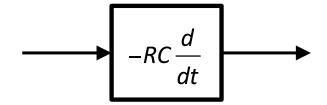


Question: How can the circuit be "reset", so that the result of a past integration is "erased"?

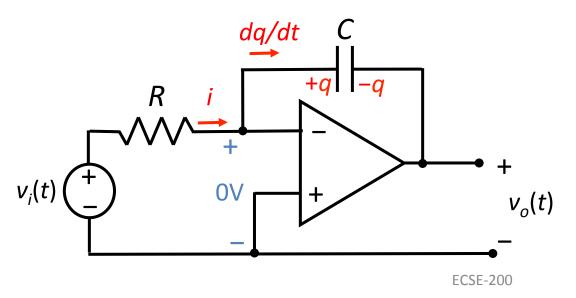


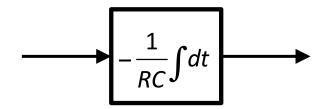
#### physical interpretation





C passes a current *i* in proportion to the time rate of change of input voltage, and R produces a voltage proportional to *i*.





R passes a current i in proportion to the input voltage, and C produces a voltage proportional to the collected charge.

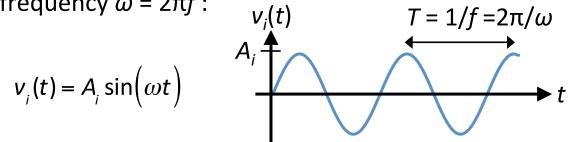


#### special case: sinusoidal inputs

Voltage signals are often of an *ac* form, being a sinusoidal function of amplitude

 $A_i$  and radial frequency  $\omega = 2\pi f$ :

$$v_{i}(t) = A_{i} \sin(\omega t)$$



The corresponding output signals for the differentiator and integrator are:

$$v_{i}(t) = A_{i} \sin(\omega t) \qquad \qquad -RC \frac{d}{dt} \qquad \qquad v_{0}(t) = -RC \omega A_{i} \cos(\omega t)$$

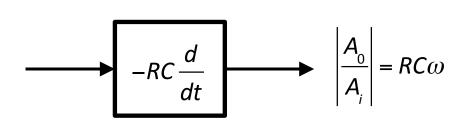
$$v_{i}(t) = A_{i} \sin(\omega t) \qquad \qquad -\frac{1}{RC} \int dt \qquad \qquad v_{0}(t) = \frac{1}{RC\omega} A_{i} \cos(\omega t)$$

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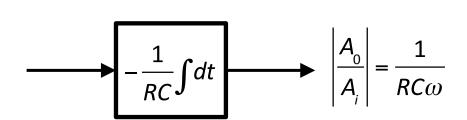


### special case: sinusoidal inputs

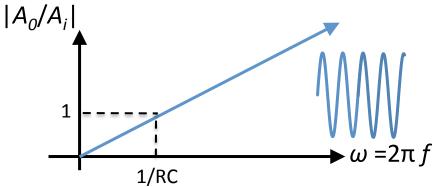
The voltage gain of the sinusoidal amplitude is highly dependent on the frequency of the input sinusoid.

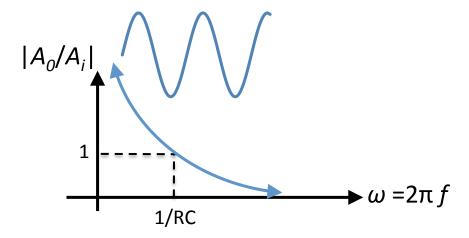


High frequencies are amplified more than low frequencies.



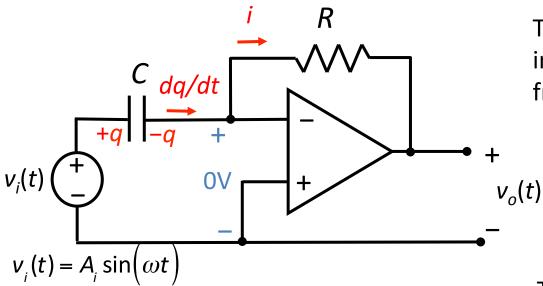
Low frequencies are amplified more than high frequencies.





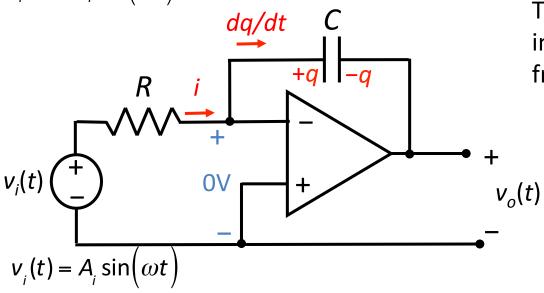


#### physical interpretation



The current on the capacitor *C* increases with increasing frequency:

$$i = \frac{dq}{dt} = C \frac{dv_i}{dt} \propto \omega$$



The voltage on the capacitor *C* increases with decreasing frequency:

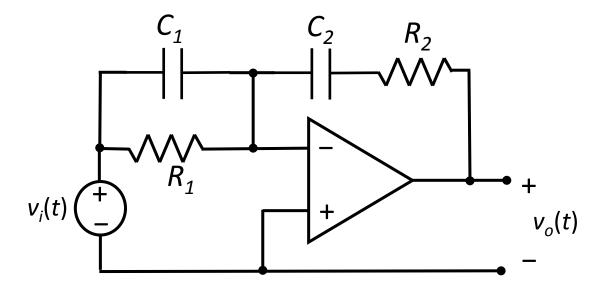
$$v_o = -\frac{q}{C} = -\frac{\int idt}{C} \propto \frac{1}{\omega}$$

Question: What is the output voltage if a triangular pulse is applied to the input of the differentiator and the integrator?

Is the circuit linear?



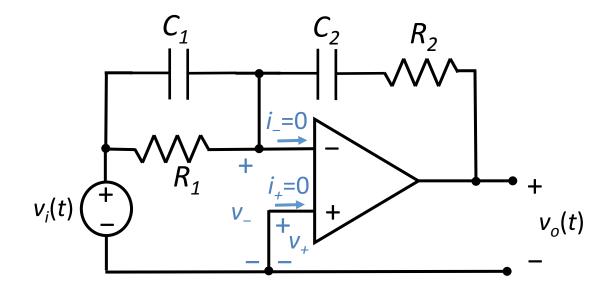
Assuming ideal op-amp behaviour, what is  $v_o(t)$  as a function of  $v_i(t)$ ? Assume that at time t=0, the capacitors  $C_1$  and  $C_2$  have zero charge separation.





Ideal op-amp:  $v_+ = v_-$ 

$$i_{+} = i_{-} = 0$$

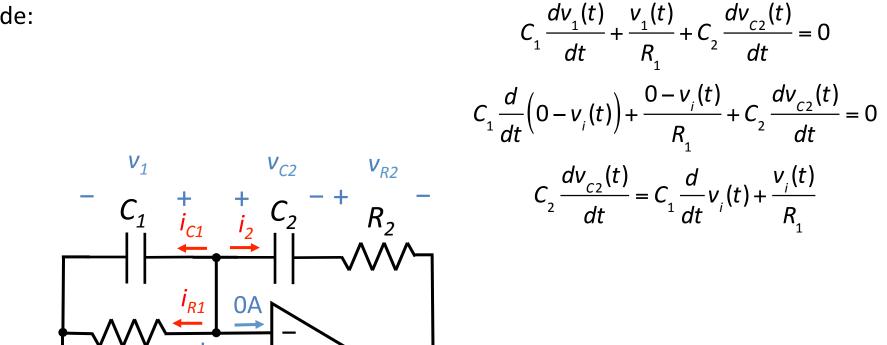




 $v_i(t)$ 

### example 5

node-voltage equation at inverting input node:



 $v_o(t)$ 

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 $i_{c1} + i_{R1} + i_2 = 0$ 



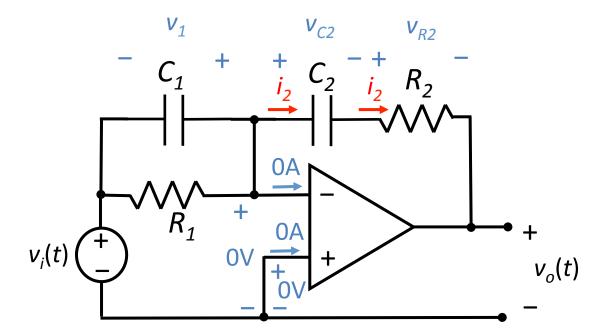
node equation between  $R_2$  and  $C_2$ :

$$i_{2} = i_{2}$$

$$\frac{v_{R2}(t)}{R_{2}} = C_{2} \frac{dv_{C2}(t)}{dt}$$

KVL:

$$0 = 0 + v_{c2}(t) + v_{R2}(t) + v_{o}(t)$$
$$v_{o}(t) = -v_{c2}(t) - v_{R2}(t)$$





putting the results together:  $C_2 \frac{dv_{c2}(t)}{dt} = C_1 \frac{d}{dt} v_i(t) + \frac{v_i(t)}{R}$ 

$$C_2 \frac{dv_{c2}(t)}{dt} = C_1 \frac{d}{dt} v_i(t) + \frac{v_i(t)}{R_1}$$

$$v_{c2}(t) - v_{c2}(0) = \frac{C_1}{C_2} \left( v_i(t) - v_i(0) \right) + \frac{1}{R_1 C_2} \int_0^t v_i(t') dt'$$

$$v_{C2}(t) = \frac{C_1}{C_2}v_i(t) + \frac{1}{R_1C_2}\int_0^t v_i(t')dt'$$
 given that zero charge sep. at  $t=0$ 

$$\frac{v_{R2}(t)}{R_2} = C_2 \frac{dv_{C2}(t)}{dt}$$

$$V_{R2}(t) = R_2 C_1 \frac{dV_i(t)}{dt} + \frac{R_2}{R_1} V_i(t)$$

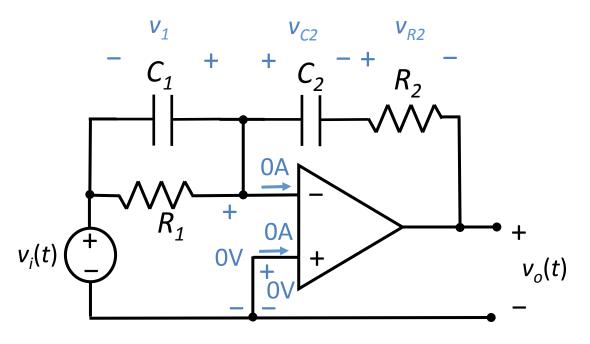
$$V_{o}(t) = -V_{c2}(t) - V_{R2}(t)$$

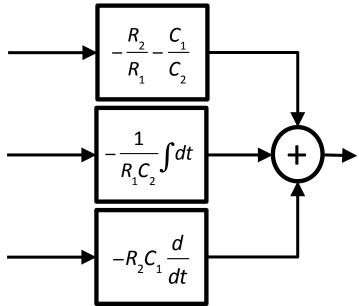
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This single circuit has a *proportional*, *integral* and *differential* output.

$$v_o(t) = -\left(\frac{R_2}{R_1} + \frac{C_1}{C_2}\right)v_i(t) - \frac{1}{R_1C_2}\int_0^t v_i(t')dt' - R_2C_1\frac{dv_i(t)}{dt}$$

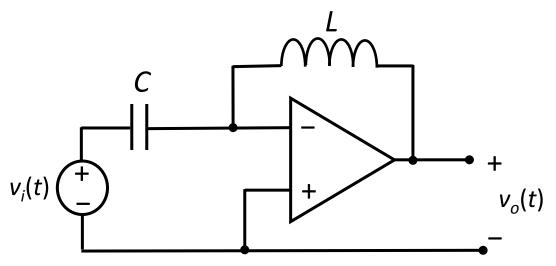




Question: One could also sum the response from an inverting amp., a differentiator and an integrator. What are the advantages and disadvantages of the two approaches?



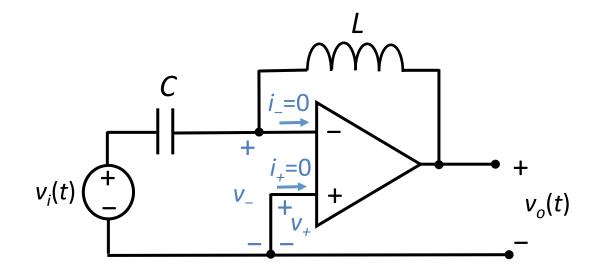
Assuming ideal op-amp behaviour, what is the  $v_o(t)$  as a function of  $v_i(t)$ ?





Ideal op-amp:  $v_+ = v_-$ 

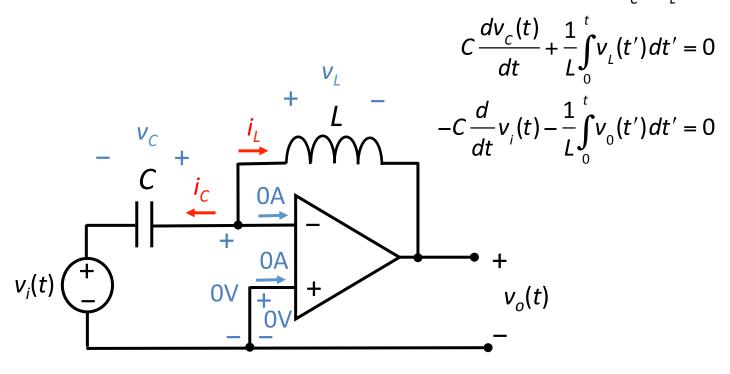
$$i_{+} = i_{-} = 0$$





node-voltage equation at inverting input node:

$$i_{c} + i_{L} = 0$$

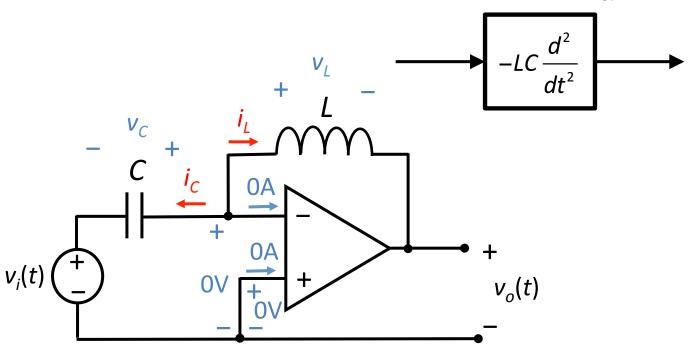




simplifying the equation: 
$$-C\frac{d}{dt}v_i(t) - \frac{1}{L}\int_0^t v_0(t')dt' = 0$$

$$v_0(t) = -LC \frac{d^2}{dt^2} v_i(t)$$

This circuit acts as a **second order differentiator**.





#### **Section 6 Summary**

**Ideal capacitors:** Store electric energy  $\frac{1}{2}Cv^2$ , charge separation q=Cv and has terminal equation  $i=C \, dv/dt$ . Voltage is continuous. Replaced by open in dc steady state.

**Ideal inductors:** Store magnetic energy  $\frac{1}{2}Li^2$ , flux linkage  $\lambda = Li$  and has terminal equation  $v = L \, di/dt$ . Current is continuous. Replaced by short in dc steady state.

**Coupled inductors:** Self-inductance and mutual inductance describe flux linkage between coupled ideal inductors.

**Op-amps + energy storage elements:** Op-amps can be programmed to perform many calculus operations by including capacitors or inductors.