ESD PROJECT

WINTER
2019

RFID BASED ATTENDANCE AND SECURITY SYSTEM

Project Report

Embedded System Design

In

ICT

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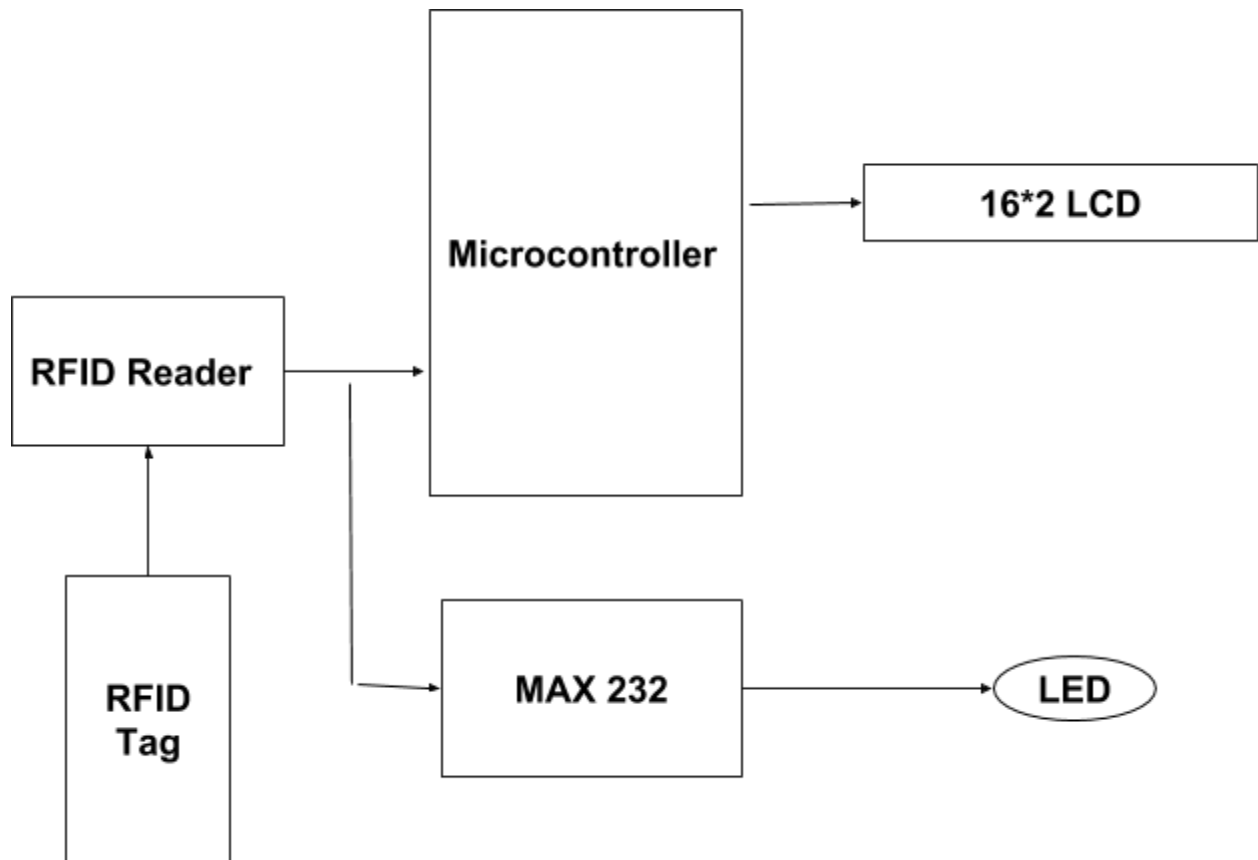
February 2019

SUMMARY

This report provides a brief introduction to a new way of taking attendance, limiting entries and providing a seamless, time-efficient design for the given purposes. The primitive methods for attendance are the roll call, signature takings or biometric system. As we have found that these methods are either outdated, expensive or time-consuming. We found a comparatively newer and cheaper way to serve the purpose. It is the RFID based attendance system. Each student and lecturer has been issued an RFID tag using which they mark their attendance. This can increase the efficiency of attendance and save lecturers' time.

Outcome:

Through this , any student cannot enter any cabin or any place without ID card or tag . Through this , master can increase his security and no other documents or any paper of them can be missed or replaced or are stolen or leak.
Through attendance system, no other student can be marked absent if student have his or her tag and through this efficiency of attendance and save lecturers' time. So this system is efficient to save attendance and also for security
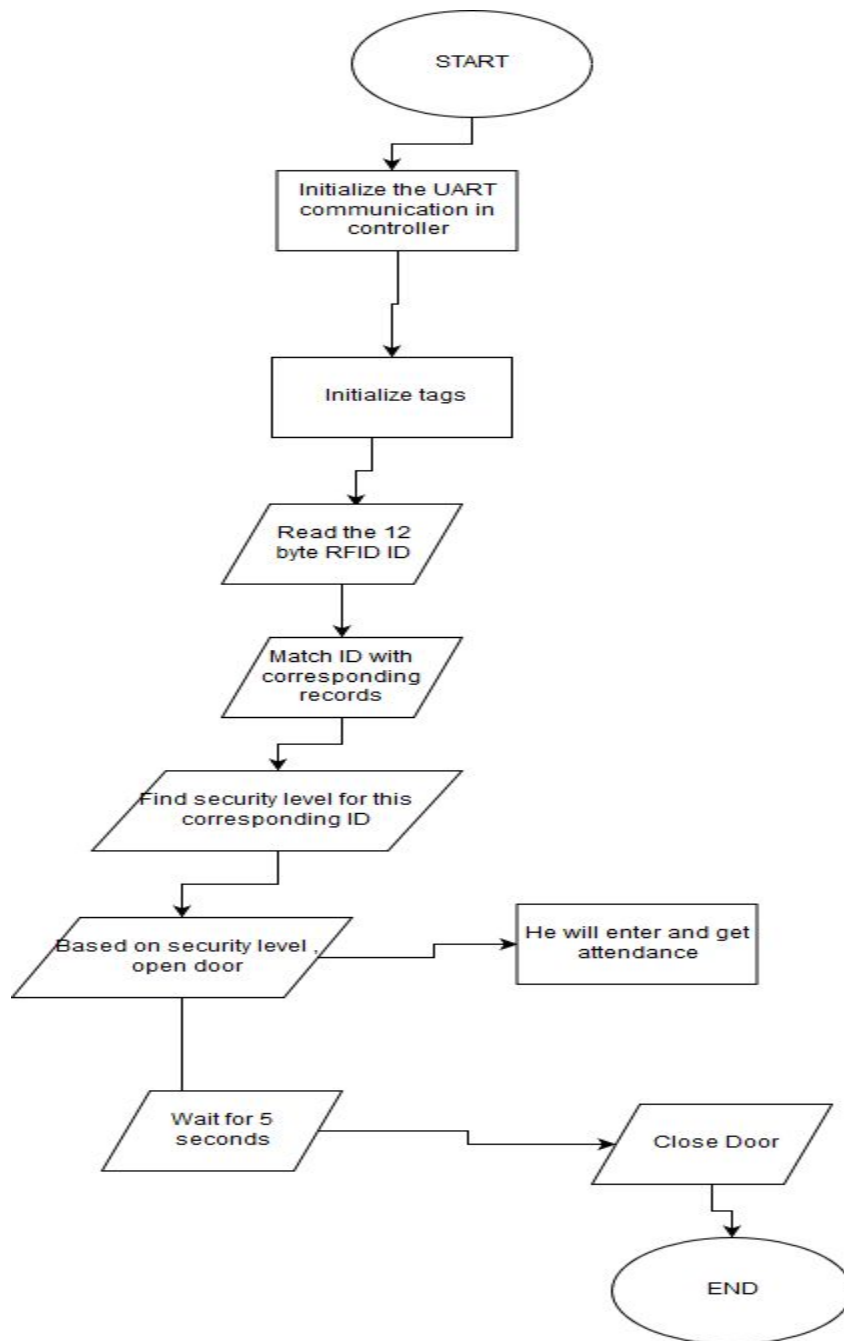
BLOCK DIAGRAM

Microcontroller

16*2 LCD

RFID Reader

RFID Tag

MAX 232

LED

Components needed:-

Index	Name of component
1	ATMEGA32 Microcontroller
2	Breadboard
3	RFID Reader
4	RFID tags
5	Jumper wires
6	16*2 LCD Display
7	MAX 232
8	Relay
9	LED (*3)

FLOW CHART

TIMELINE

Date	Basic Design of our Circuit and second report	Coding part of our Circuit and third report	Entire Project and Final Report	Project demo and viva
05/03/2019	✔			
19/03/2019		✔		
02/4/2019			✔	
17/04/2019				✔