

# CS 354 - Machine Organization & Programming

## Tuesday, November 5, 2019

### Midterm Exam (~18%): Thursday, November 7th, 7:15 - 9:15 pm

- **Lec 1 (2:30 pm):** room 3650 of Humanities
- **Lec 2 (4:00 pm):** room B10 of Ingraham Hall
- ♦ UW ID required
- ♦ #2 pencils required
- ♦ closed book, no notes, no electronic devices (e.g., calculators, phones, watches)
- ♦ see “Midterm Exam 2” on course site Assignments for topics

**Project p4A (~2%):** DUE TODAY at 10 pm on Tuesday, November 5th

**Homework hw5 (1.5%):** DUE TOMORROW at 10 pm Wednesday, November 6th

**Project p4b (~4%):** DUE at 10 pm on Wednesday, November 13th

### Last Time

Instructions - MOV, PUSH, POP  
Operand Specifiers  
Operands Practice  
Operand/Instruction Caveats  
Instruction - LEAL  
Instructions - Arithmetic and Shift

### Today

Instructions - Arithmetic and Shift (from last time)  
Instructions - CMP and TEST, Condition Codes  
Instructions - SET  
Instructions - Jumps  
Encoding Targets  
Converting Loops  
----- END of Exam 2 Material -----  
Midterm 2 Reference Page

### Next Time

Stack Frames  
**Read:** B&O 3.7 intro - 3.7.3  
Exam Mechanics

## Instructions - CMP and TEST, Condition Codes

### What?

- ♦ compare values either arithmetically or logically  
(CMP) (Test)
- ♦ don't change sources.

### Why?

enables relational and logical operation

### How?

CMP S2, S1

CMPb  
w  
l

← condition code  
CC ← S1 - S2 like sub, but only sets condition code.

TEST S2, S1

TESTb  
w  
l

CC ← S1 & S2 like and, but only sets condition code.

➤ What is done by `testl %eax, %eax`

### Condition Codes (CC)

`total = a + b` assume variables are ints in 2's complement representation

ZF: zero flag

set if `total == 0`

CF: carry flag

set if `(unsigned) total < (unsigned) a`

SF: sign flag

set if `total < 0`

OF: overflow flag

set if `(a < 0 == b < 0) && (total < 0 != a < 0)`

a and b signs are the same  
total and a have different signs.

## Instructions - SET

**What?** set a byte register to 1 if a condition is true  
0 otherwise.

**How?**

sete D	D $\leftarrow$ ZF	$==$ equal
setne D	D $\leftarrow$ $\sim$ ZF	$\neq$ not equal.
sets D	D $\leftarrow$ SF	$< 0$ signed.
setns D	D $\leftarrow$ $\sim$ SF	$\geq 0$ not signed.

### Unsigned Comparisons

setb D	D $\leftarrow$ CF	$<$ below.
setbe D	D $\leftarrow$ CF   ZF	$\leq$ below or equal.
seta D	D $\leftarrow$ $\sim$ CF & $\sim$ ZF	$>$ above.
setae D	D $\leftarrow$ $\sim$ CF	$\geq$ above or equal.

### Signed (2's Complement) Comparisons

setl D	D $\leftarrow$ SF $\wedge$ OF	$<$ less.
setle D	D $\leftarrow$ (SF $\wedge$ OF)   ZF	$\leq$ less or equal
setg D	D $\leftarrow$ $\sim$ (SF $\wedge$ OF) & $\sim$ ZF	$>$ greater
setge D	D $\leftarrow$ $\sim$ (SF $\wedge$ OF)	$\geq$ greater or equal.

**Example: a < b** (assume int a is in %eax, int b is in %ebx)

1. <sup>s2</sup> <sup>s1</sup> <code>cmpl %ebx, %eax</code>	$a - b$
2. <code>setl %cl</code>	set %cl to 1 if $a < b$
3. <code>movzbl %cl, %ecx</code>	zero out remaining byte of %ecx.

## Instructions - Jumps

**What?** transfers execution to another location in code

target: desired location to jump to.

**Why?** enables selection & repetition control flow

## How? Unconditional Jump

Jump always

indirect jump: target address is in register or mem location

```
jmp *Operand
```

$\text{jmp } * \% \text{eax} \quad \% \text{eip} \leftarrow \text{operand value.}$   
                    $\uparrow$   
                   value in  $\text{eax}$  is addr to jump to.

```

    jmp *(%eax)

```

direct jump: target addr is encoded in the instruction.

```
jmp Label
```

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$\%eip \leftarrow \text{label's addr.}$

$\frac{L_1}{\vdots}$

## How? Conditional Jumps

- Jump if condition is met based on condition code of the previous instruction.
- can only be direct jumps.

both:	je Label	jne Label	js Label	jns Label
unsigned:	jb Label	jbe Label	ja Label	jae Label
signed:	jl Label	jle Label	jg Label	jge Label

## Encoding Targets

**What?** techniques used by direct jumps for specifying the target.

### Absolute Encoding

target address is a specific 32 bit addr.

### Problems?

- code is not compact - target always require 32 bits.
- code cannot be move to different address without changing target.

**Solution?** relative encoding.

target specified as a distance from the jump.

IA-32: distance can be 1, 2, 4 bytes, in 2's complement.

\* distance is calculated from the instruction immediately after the jump.

→ What is the distance (in hex) encoded in the `jne` instruction?

Assembly Code	Address	Machine Code
<code>cmpl %eax, %ecx</code>		
<code>jne .L1</code>		75 <sup>04</sup>
<code>movl \$11, %eax</code>	0x_B8	
<code>movl \$22, %edx</code>	0x_BA	
	0x_BC	
.L1:	0x_BE	

do not start from jump

start from instruction after jump

$0x_{BE} - 0x_{BA} = 0x_4$

→ If the `jb` instruction is 2 bytes in size and is at 0x08011357 and the target is at 0x8011340 then what is the distance (hex) encoded in the `jb` instruction?

$$0x_{-40} - (0x_{-57} + 0x_2) = -0x_{19}$$

↑  
jb is 2 byte.

$$0x_{19} = 00011001$$
$$-0x_{19} = 11100111$$
$$= 0xE7$$

## Converting Loops

→ Which kind of C loop does each goto code fragment correspond?

```
loop1:
    loop_body
    t = loop_condition
    if (t) goto loop1:
```

*Do-while*

```
        t = loop_condition
        if (!t) goto done:
loop2:
    loop_body
    t = loop_condition
    if (t) goto loop2
done:
```

*] do while*

*while .*

```
        loop_init
        t = loop_condition
        if (!t) goto done:
loop3:
    loop_body
    loop_update
    t = loop_condition
    if (t) goto loop3
done:
```

*For*

*] do while .*

\* Most compilers (gcc included) base loop assembly code on the do while form.  
shown above.

## Exam 2 Reference Page

### Powers of 2

$2^5 = 32$ ,  $2^6 = 64$ ,  $2^7 = 128$ ,  $2^8 = 256$ ,  $2^9 = 512$ ,  $2^{10} = 1024$

$2^{10} = \text{K}$ ,  $2^{20} = \text{M}$ ,  $2^{30} = \text{G}$

$2^A \times 2^B = 2^{A+B}$

$2^A / 2^B = 2^{A-B}$

### Hexadecimal Digits

$9_{16} = 9_{10} = 1001_2$

$A_{16} = 10_{10} = 1010_2$

$B_{16} = 11_{10} = 1011_2$

$C_{16} = 12_{10} = 1100_2$

$D_{16} = 13_{10} = 1101_2$

$E_{16} = 14_{10} = 1110_2$

$F_{16} = 15_{10} = 1111_2$

### Registers

32 bit	16 bit	8 bit
%eax	%ax	%ah, %al
%ecx	%cx	%ch, %cl
%edx	%dx	%bh, %bl
%ebx	%bx	%dh, %dl
%edi	%di	
%esi	%si	
%ebp	%bp	
%esp	%sp	

### Assembly

Most instructions with two operands have the order: Source, Destination

e.g., `subl s, d` means  $d = d - s$ ; `imull s, d` means  $d = d * s$

Comparison (`cmp`) and test instructions have operand order: Source2, Source1

e.g., `cmpl s2, s1` means  $s1 - s2$ ; `test s2, s1` means  $s1 \& s2$

Suffixes for set, jump, and conditional move instructions are:

e.g., `j1` means jump if less, `setns` means set not signed

general - e: equal, ne: not equal, s: signed, ns: not signed

unsigned - b: below, be: below or equal, a: above, ae: above or equal

signed - l: less, le: less or equal, g: greater, ge: greater or equal