## RTMPose: Real-Time Multi-Person Pose Estimation based on MMPose

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#### **Abstract**

Recent studies on 2D pose estimation have achieved excellent performance on public benchmarks, yet its application in the industrial community still suffers from heavy model parameters and high latency. To bridge this gap, we empirically explore key factors in pose estimation including paradigm, model architecture, training strategy, and deployment, and present a high-performance real-time multiperson pose estimation framework, RTMPose, based on MMPose. Our RTMPose-m achieves 75.8% AP on COCO with 90+ FPS on an Intel i7-11700 CPU and 430+ FPS on an NVIDIA GTX 1660 Ti GPU, and RTMPose-x achieves 65.3% AP on COCO-WholeBody. To further evaluate RTMPose's capability in critical real-time applications, we also report the performance after deploying on the mobile device. Our RTMPose-s model achieves 72.2% AP on COCO with 70+ FPS on a Snapdragon 865 chip, outperforming existing open-source libraries. Our code and models are available at <a href="https://github.com/open-">https://github.com/open-</a> mmlab/mmpose/tree/main/projects/rtmpose.

## 1. Introduction

Real-time human pose estimation is appealing to various applications such as human-computer interaction, action recognition, sports analysis, and VTuber techniques. Despite the stunning progress [52,61] on academic benchmarks [31,37], it remains a challenging task to perform robust and real-time multi-person pose estimation on devices with limited computing power. Recent attempts narrow the gap with efficient network architecture [3,56,63] and detection-free paradigms [19,29,51], which is yet inadequate to reach satisfactory performance for industrial applications.

In this work, we empirically study key factors that affect the performance and latency of 2D multi-person pose estimation frameworks from five aspects: paradigm, backbone network, localization method, training strategy, and

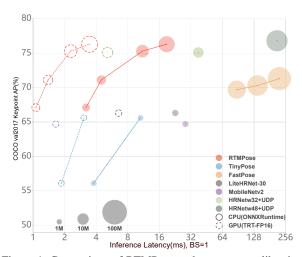


Figure 1. Comparison of RTMPose and open-source libraries on COCO val set regarding model size, latency, and precision. The circle size represents the relative size of model parameters.

deployment. With a collection of optimizations, we introduce **RTMPose**, a new series of **Real-Time Models** for **Pose** estimation.

First, RTMPose employs a top-down approach by using an off-the-shelf detector to obtain bounding boxes and then estimating the pose of each person individually. Top-down algorithms have been stereotyped as accurate but slow, due to the extra detection process and increasing workload in crowd scenes. However, benefiting from the excellent efficiency of real-time detectors [42, 46], the detection part is no longer a bottleneck of the inference speed of top-down methods. In most scenarios (within 6 persons per image), the proposed lightweight pose estimation network is able to perform multiple forward passes for all instances in real time.

Second, RTMPose adopts CSPNeXt [42] as the backbone, which is first designed for object detection. Backbones designed for image classification [20, 49] are suboptimal for dense prediction tasks like object detection, pose estimation and semantic segmentation, etc. Some back-

bones leveraging high-resolution feature maps [52, 63] or advanced transformer architectures [15] achieve high accuracy on public pose estimation benchmarks, but suffer from high computational cost, high inference latency, or difficulties in deployment. CSPNeXt shows a good balance of speed and accuracy and is deployment-friendly.

Third, RTMPose predicts keypoints using a SimCC-based [35] algorithm that treats keypoint localization as a classification task. Compared with heatmap-based algorithms [24, 59, 61, 65], the SimCC-based algorithm achieves competitive accuracy with lower computational effort. Moreover, SimCC uses a very simple architecture of two fully-connected layers for prediction, making it easy to deploy on various backends.

Fourth, we revisit the training settings in previous works [3, 30, 42], and empirically introduce a collection of training strategies applicable to the pose estimation task. Our experiments demonstrate that this set of strategies bring significant gains to proposed RTMPose as well as other pose estimation models.

Finally, we jointly optimize the inference pipeline of the pose estimation framework. We use the skip-frame detection strategy proposed in [3] to reduce the latency and improve the pose-processing with pose Non-Maximum Suppression (NMS) and smoothing filtering for better robustness. In addition, we provide a series of RTMPose models with t/s/m/l/x sizes to cover different application scenarios with the optimum performance-speed trade-off.

We deploy RTMPose with different inference frameworks (PyTorch, ONNX Runtime, TensorRT, ncnn) and hardwares (i7-11700, GTX1660Ti, Snapdragon865) to test the efficiency.

As shown in Fig. 1, We evaluate the efficiency of RTM-Pose with various inference frameworks (PyTorch, ONNX Runtime, TensorRT, ncnn) and hardwares (Intel i7-11700, GTX 1660Ti, Snapdragon 865). Our RTMPose-m achieves 75.8% AP (with flipping) on COCO val set with 90+FPS on an Intel i7-11700 CPU, 430+ FPS on an NVIDIA GeForce GTX 1660 Ti GPU, and 35+ FPS on a Snapdragon 865 chip. Using the high-performance real-time object detection model RTMDet-nano in our pose estimation pipeline, RTMPose-m can achieve 73.2% AP. With the help of MMDeploy [12], RTMPose can also be easily deployed to various backends like RKNN, OpenVINO, PPLNN, etc.

#### 2. Related Work

**Bottom-up Approaches.** Bottom-up algorithms [7, 10, 19, 27, 29, 41, 44, 45] detect instance-agnostic keypoints in an image and partition these keypoints to obtain the human pose. The bottom-up paradigm is considered suitable for crowd scenarios because of the stable computational cost regardless the number of people increases. However, these algorithms often require a large input resolution to handle

various person scales, making it challenging to reconcile accuracy and inference speed.

**Top-down Approaches.** Top-down algorithms use off-the-shelf detectors to provide bounding boxes and then crop the human to a uniform scale for pose estimation. Algorithms [5, 38, 52, 59, 61] of the top-down paradigm have been dominating public benchmarks. The two-stage inference paradigm allows both the human detector and the pose estimator to use relatively small input resolutions, which allows them to outperform bottom-up algorithms in terms of speed and accuracy in non-extreme scenarios (i.e. when the number of people in the image is no more than 6). Additionally, most previous work has focused on achieving state-of-the-art performance on public datasets, while our work aims to design models with better speed-accuracy trade-offs to meet the needs of industrial applications.

Coordinate Classification. Previous pose estimation approaches usually regard keypoint localization as either coordinate regression (e.g. [30, 43, 54]) or heatmap regression (e.g. [25, 59, 61, 65]). SimCC [35] introduces a new scheme that formulates keypoint prediction as classification from sub-pixel bins for horizontal and vertical coordinates respectively, which brings about several advantages. First, SimCC is freed from the dependence on high-resolution heatmaps, thus allowing for a very compact architecture that requires neither high-resolution intermediate representations [52] nor costly upscaling layers [59]. Second, SimCC flattens the final feature map for classification instead of involving global pooling [54] and therefore avoids the loss of spatial information. Third, the quantization error can be effectively alleviated by coordinate classification at the sub-pixel scale, without the need for extra refinement post-processing [65]. These qualities make SimCC attractive for building lightweight pose estimation models. In this work, we further exploit the coordinate classification scheme with optimizations on model architecture and training strategy.

**Vision Transformers.** Transformer-based architectures [55] ported from modern Natural Language Processing (NLP) have achieved great success in various vision tasks like representation learning [15, 39], object detection [8, 34, 67], semantic segmentation [66], video understanding [4, 17, 40], as well as pose estimation [36, 43, 51, 61, 62]. **ViTPose** [61] leverages the state-of-the-art transformer backbones to boost pose estimation accuracy, while TransPose [62] integrates transformer encoders with CNNs to efficiently capture long-range spatial relationships. **Token-based keypoint embedding** is introduced to incorporate visual cue querying and anatomic constraint learning, shown effective in both

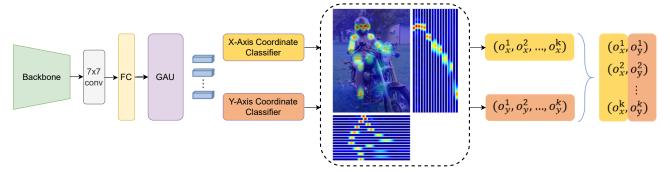


Figure 2. The overall architecture of RTMPose, which contains a convolutional layer, a fully-connected layer and a Gated Attention Unit (GAU) to refine K keypoint representations. After that 2d pose estimation is regarded as two classification tasks for x-axis and y-axis coordinates to predict the horizontal and vertical locations of keypoints.

heatmap-based [36] and regression-based [43] approaches. PRTR [33] and PETR [51] propose end-to-end multi-person pose estimation frameworks with transformers, inspired by the pioneer in detection [8]. Previous pose estimation approaches with transformers either use a heatmap-based representation or retained both pixel tokens and keypoint tokens, which results in high computation costs and makes real-time inference difficult. In contrast, we incorporate the self-attention mechanism with a compact SimCC-based representation to capture the keypoint dependencies, which significantly reduces the computation load and allows real-time inference with advanced accuracy and efficiency.

#### 3. Methodology

In this section, we expound the roadmap we build RTM-Pose following the coordinate classification paradigm. We start by refitting SimCC [35] with more efficient backbone architectures, which gives a lightweight yet strong baseline (3.1). We adopt the training strategies proposed in [42] with minor tweaks to make them more effective on the pose estimation task. The model performance is further improved with a series of delicate modules (3.3) and micro designs (3.4). Finally, we jointly optimize the entire top-down inference pipeline toward higher speed and better reliability. The final model architecture is shown in Fig. 2, and Fig. 3 illustrates the step-by-step gain of the roadmap.

## 3.1. SimCC: A lightweight yet strong baseline

Preliminary SimCC [35] formulates the keypoint localization as a classification problem. The core idea is to divide the horizontal and vertical axes into equal-width numbered bins and discretize continuous coordinates into integral bin labels. Then the model is trained to predict the bin in which the keypoint is located. The quantization error can be reduced to a subpixel level by using a large number of bins.

Thanks to this novel formulation, SimCC has a very simple structure that uses a  $1 \times 1$  convolution layer to convert

features extracted by the backbone into vectorized keypoint representations, and two fully-connected layers to perform classification, respectively.

Inspired by label smoothing in traditional classification tasks [53], SimCC proposes a Gaussian label smoothing strategy that replaces the one-hot label with Gaussian distributed soft label centered at the ground-truth bin, which integrates the inductive bias in the model training and brings about significant performance improvement. We find this technique also coincides with the idea of SORD [16] in the ordinal regression task. The soft label naturally encapsulates the rank likelihoods of the keypoint locations given the inter-class penalty distance defined by the label distribution.

**Baseline** We first remove the costly upsampling layers from the standard SimCC. Results in Table 1 show that the trimmed SimCC has significantly lower complexity compared to the SimCC and heatmap-based baselines [59], and still achieves promising accuracy. This indicates the efficiency of encoding global spatial information into disentangled one-dimension representations in localization tasks. By replacing the ResNet-50 [21] backbone with the more compact CSPNext-m [42], we further reduce the model size and obtain a lightweight yet strong baseline, 69.7% AP.

Table 1. Computational costs and accuracy of baseline methods. We show FLOPs and model parameters of prediction heads for a detailed comparison. "SimCC\*" denotes the removal of upsampling layers from the standard SimCC head.

	Heatmap	SimCC	SimCC*
Repr. Size	64×48	512+384	512+384
AP	71.8	72.1	71.3
Total FLOPs(G)	5.45	5.50	4.03
Total Params(M)	34.00	36.75	23.59
Head FLOPs(G)	1.425	1.472	0.002
Head Params(M)	10.492	13.245	0.079

#### 3.2. Training Techniques

**Pre-training** Previous works [3, 30] show that pre-training the backbone using the heatmap-based method can improve the model accuracy. We adopt UDP [24] method for the backbone pre-training. This improves the model from 69.7% AP to 70.3% AP. We use this technique as a default setting in the following sections.

**Optimization Strategy** We adopt the optimization strategy from [42]. The Exponential Moving Average (EMA) is used for alleviating overfitting (70.3% to 70.4%). The flat cosine annealing strategy improves the accuracy to 70.7% AP. We also inhibit weight decay on normalization layers and biases.

Two-stage training augmentations Following the training strategy in [42], we use a strong-then-weak two-stage augmentation. First using strong data augmentations to train 180 epochs and then a weak strategy for 30 epochs. During the strong stage, we use a large random scaling range [0.6, 1.4], and a large random rotation factor, 80, and set the Cutout [14] probability to 1. According to AID [26], Cutout helps to prevent the model from overfitting to the image textures and encourages it to learn the pose structure information. In the weak strategy stage, we turn off the random shift, use a small random rotation range, and set the Cutout probability to 0.5 to allow the model to fine-tune in a domain that more closely matches the real image distribution.

### 3.3. Module Design

**Feature dimension** We observe that the model performance increases along with higher feature resolution. Therefore, we use a fully connected layer to expand the 1D keypoint representations to a desired dimension controlled by the hyper-parameter. In this work, we use 256 dimensions and the accuracy is improved from 71.2% AP to 71.4% AP.

**Self-attention module** To further exploit the global and local spatial information, we refine the keypoint representations with a self-attention module, inspired by [36, 62]. We adopt the transformer variant, Gated Attention Unit (GAU) [23], which has faster speed, lower memory cost, and better performance compared to the vanilla transformer [55]. Specifically, GAU improves the Feed-Forward Networks (FFN) in the transformer layer with Gated Linear Unit (GLU) [50], and integrates the attention mechanism in an elegant form:

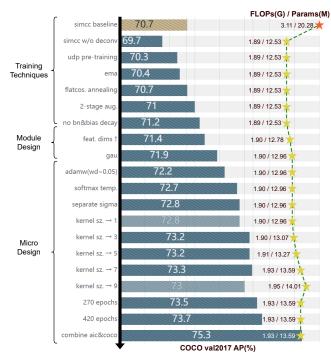


Figure 3. Step-by-step improvements from a SimCC baseline.

$$U = \phi_u(XW_u)$$

$$V = \phi_v(XW_v)$$

$$O = (U \odot AV)W_o$$
(1)

where  $\odot$  is the pairwise multiplication (Hadamard product) and  $\phi$  is the activation function. In this work we implement the self-attention as follows:

$$A = \frac{1}{n} relu^2 \left(\frac{Q(X)K(Z)^{\top}}{\sqrt{s}}\right), Z = \phi_z(XW_z)$$
 (2)

where  $s=128,\,Q$  and K are simple linear transformations, and  $relu^2(\cdot)$  is ReLU then squared. This self-attention module brings about a 0.5% AP (71.9%) improvement to the model performance.

#### 3.4. Micro Design

**Loss function** We treat the coordinate classification as an ordinal regression task and follow the **soft label** encoding proposed in SORD [16]:

$$y_i = \frac{e^{\phi(r_t, r_i)}}{\sum_{k=1}^K e^{\phi(r_t, r_k)}}$$
(3)

where  $\phi(r_t, r_i)$  is a metric loss function of our choice that penalizes how far the true metric value of  $r_t$  is from

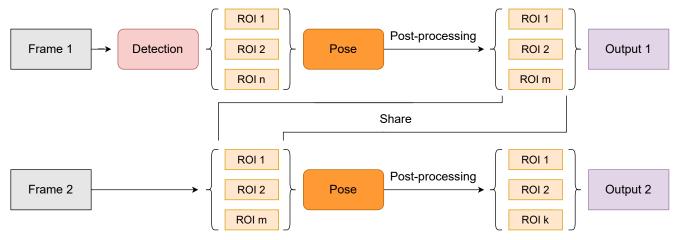


Figure 4. Inference pipeline of RTMPose.

the rank  $r_i \in Y$ . In this work, we adopt the unnormalized Gaussian distribution as the inter-class distance metric:

$$\phi(r_t, r_i) = e^{\frac{-(r_t - r_i)^2}{2\sigma^2}} \tag{4}$$

Note that Eq. 3 can be seen as computing Softmax for all  $\phi(r_t, r_i)$ . We add temperatures in the Softmax operation for both model outputs and soft labels further adjust the normalized distribution shape:

$$y_i = \frac{e^{\phi(r_t, r_i)/\tau}}{\sum_{k=1}^{K} e^{\phi(r_t, r_i)/\tau}}$$
 (5)

According to the experimental results, using  $\tau=0.1$  can improve the performance from 71.9% to 72.7%.

**Separate**  $\sigma$  In SimCC, the horizontal and vertical labels are encoded using the same  $\sigma$ . We empirically explore a simple strategy to set separate  $\sigma$  for them:

$$\sigma = \sqrt{\frac{W_S}{16}} \tag{6}$$

where  $W_S$  is the bin number in the horizontal and vertical directions respectively. This step improves the accuracy from 72.7% to 72.8%.

**Larger convolution kernel** We experiment with different kernel sizes of the last convolutional layer and find that using a larger kernel size gives a performance improvement over using  $1 \times 1$  kernel. Finally, we chose to use a  $7 \times 7$  convolutional layer, which achieves 73.3% AP. We compare model performances with different kernel sizes in Table 2. Additionally, we also compare the effect of different temperature factors in Table 3 using the final model architecture.

Table 2. Comparison of different kernel sizes

Kernel Size	mAP
1x1	72.8
3x3	73.2
5x5	73.2
7x7	73.3
9x9	73.0

Table 3. Comparison of different temperature factors.

$1/\tau$	mAP
1	unstable
5	73.1
10	73.3
15	73.0

More epochs and multi-dataset training Increasing the training epochs brings extra gains to the model performance. Specifically, training 270 and 420 epochs reach 73.5% AP and 73.7% AP respectively. To further exploit the model's potential, we enrich the training data by combining COCO [37] and AI Challenger [58] datasets together for pre-training and fine-tuning, with a balanced sampling ratio. The performance finally achieves 75.3% AP.

#### 3.5. Inference pipeline

Beyond the pose estimation model, we further optimize the overall top-down inference pipeline for lower latency and better robustness. We use the skip-frame detection mechanism as in BlazePose [3], where human detection is performed every K frames, and in the interval frames the bounding boxes are generated from the last pose estimation results. Additionally, to achieve smooth prediction over frames, we use OKS-based pose NMS and OneEuro [9] filter in the post-processing stage.

## 4. Experiments

#### 4.1. Settings

The training settings in our experiments are shown in Tabel. 7. As described in Sec. 3.2, we conduct a heatmap-based pre-training [24] which follows the same training

Table 4. Body pose estimation results on COCO validation set. We only report GFLOPs of pose model and ignore the detection model. Flip test is not used.

Metho	ds	Backbone	Detector	Det. Input Size	Pose Input Size	GFLOPs	AP	Extra Data
	TinyPose TinyPose	Wider NLiteHRNet Wider NLiteHRNet	YOLOv3 YOLOv3	$ \begin{vmatrix} 608 \times 608 \\ 608 \times 608 \end{vmatrix} $	$\begin{array}{ c c c }\hline 128\times96\\ 256\times192\\ \end{array}$	0.08 0.33	52.3 60.9	
PaddleDetection [2]	TinyPose TinyPose	Wider NLiteHRNet Wider NLiteHRNet	Faster-RCNN Faster-RCNN	N/A N/A	$\begin{array}{c c} 128 \times 96 \\ 256 \times 192 \end{array}$	0.08 0.33	56.1 65.6	AIC(220K) +Internal(unknown)
	TinyPose TinyPose	Wider NLiteHRNet Wider NLiteHRNet	PicoDet-s PicoDet-s	$\begin{vmatrix} 320 \times 320 \\ 320 \times 320 \end{vmatrix}$	$\begin{array}{c c} 128 \times 96 \\ 256 \times 192 \end{array}$	0.08 0.33	48.4 56.5	
AlphaPose [18]	FastPose(DUC) FastPose(DUC)	ResNet 50 ResNet-50 ResNet-152	YOLOv3 YOLOv3 YOLOv3		$\begin{array}{c c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \end{array}$	5.91 9.71 15.99	71.2 71.7 72.6	_
	FastPose(DUC) FastPose(DUC)	ResNet 50 ResNet-50 ResNet-152	Faster-RCNN Faster-RCNN Faster-RCNN	N/A N/A N/A	$\begin{array}{c c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \end{array}$	5.91 9.71 15.99	69.7 70.3 71.3	
	RTMPose-t RTMPose-s RTMPose-m RTMPose-l	CSPNeXt-t CSPNeXt-s CSPNeXt-m CSPNeXt-l	Faster-RCNN Faster-RCNN Faster-RCNN Faster-RCNN	N/A N/A N/A N/A	$\begin{array}{c c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16	65.8 69.6 73.6 74.8	-
	RTMPose-t RTMPose-s RTMPose-m RTMPose-l	CSPNeXt-t CSPNeXt-s CSPNeXt-m CSPNeXt-l	YOLOv3 YOLOv3 YOLOv3 YOLOv3		$\begin{array}{c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16	66.0 70.3 74.7 75.7	
MMPose [11]	RTMPose-t RTMPose-s RTMPose-m RTMPose-l	CSPNeXt-t CSPNeXt-s CSPNeXt-m CSPNeXt-I	Faster-RCNN Faster-RCNN Faster-RCNN Faster-RCNN	N/A N/A N/A N/A	$\begin{array}{c c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16	67.1 71.1 75.3 76.3	
	RTMPose-t RTMPose-s RTMPose-m RTMPose-l	CSPNeXt-t CSPNeXt-s CSPNeXt-m CSPNeXt-l	PicoDet-s PicoDet-s PicoDet-s PicoDet-s	$ \begin{vmatrix} 320 \times 320 \\ 320 \times 320 \\ 320 \times 320 \\ 320 \times 320 \end{vmatrix} $	$\begin{array}{c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16	64.3 68.8 73.2 74.2	AIC(220K)
	RTMPose-t RTMPose-s RTMPose-m RTMPose-l RTMPose-l	CSPNeXt-t CSPNeXt-s CSPNeXt-m CSPNeXt-l CSPNeXt-m CSPNeXt-l	RTMDet-nano RTMDet-nano RTMDet-nano RTMDet-nano RTMDet-m RTMDet-m	$\begin{array}{c} 320 \times 320 \\ 640 \times 640 \\ 640 \times 640 \end{array}$	$\begin{array}{c} 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16 1.93 4.16	64.4 68.5 73.2 74.2 75.7 76.6	

strategies used in the fine-tuning except for shorter epochs. All our models are trained on 8 NVIDIA A100 GPUs. And we evaluate the model performance by mean Average Precision (AP).

#### 4.2. Benchmark Results

COCO COCO [37] is the most popular benchmark for 2d body pose estimation. We follow the standard splitting of train2017 and val2017, which contains 118K and 5k images for training and validation respectively. We extensively study the pose estimation performance with different off-the-shelf detectors including YOLOv3 [47], Faster-RCNN [48], and RTMDet [42]. To conduct a fair comparison with AlphaPose [18] which doesn't use extra training data, we also report the performance of RTMPose only trained on COCO. As shown in Table 4, RTMPose outperforms competitors by a large margin with much lower complexity and shows strong robustness for detection.

COCO-SinglePerson Popular pose estimation opensource algorithms like BlazePose [3], MoveNet [56], and PaddleDetection [2] are designed primarily for singleperson or sparse scenarios, which are practical in mobile applications and human-machine interactions. For a fair comparison, we construct a COCO-SinglePerson dataset that contains 1045 single-person images from the COCO val2017 set to evaluate RTMPose as well as other approaches. For MoveNet, we follow the official inference pipeline to apply a cropping algorithm, namely using the coarse pose prediction of the first inference to crop the input image and performing a second inference for better pose estimation results. The evaluation results in Table 5 show that RTMPose archives superior performance and efficiency even compared to previous solutions tailored for the singleperson scenario.

**COCO-WholeBody** We also validate the proposed RTM-Pose model on the whole-body pose estimation task with

Table 5. Body pose estimation results on COCO-SinglePerson validation set. We sum up top-down methods' GFLOPs of detection and
pose for a fair comparison with bottom-up methods. "*" denotes double inference. Flip test is not used.

Metho	ds	Backbone	Detector	Det. Input Size	Pose Input Size	GFLOPs	AP	Extra Data
MediaPipe [3]	BlazePose-Lite	BlazePose	N/A	256 × 256	N/A	N/A	29.3	Internal(85K)
	BlazePose-Full	BlazePose	N/A	$256 \times 256$	N/A	N/A	35.4	<u> </u>
MoveNet [56]	Lightning	MobileNetv2	N/A	$192 \times 192$	N/A	0.54	53.6*	Internal(23.5K)
Movemet [36]	Thunder	MobileNetv2 depth×1.75	N/A	$256 \times 256$	N/A	2.44	64.8*	internal(23.3K)
PaddleDetection [2]	TinyPose	Wider NLiteHRNet	PicoDet-s	$320 \times 320$	$128 \times 96$	0.55	58.6	AIC(220K)
raddieDetection [2]	TinyPose	Wider NLiteHRNet	PicoDet-s	$320 \times 320$	$256 \times 192$	0.80	69.4	+Internal(unknown)
	RTMPose-t	CSPNeXt-t	RTMDet-nano	$320 \times 320$	$256 \times 192$	0.67	72.1	
MMPose [11]	RTMPose-s	CSPNeXt-s	RTMDet-nano	$320 \times 320$	$256 \times 192$	0.91	77.1	AIC(220K)
	RTMPose-m	CSPNeXt-m	RTMDet-nano	$320 \times 320$	$256 \times 192$	2.23	82.4	AIC(220K)
	RTMPose-l	CSPNeXt-l	RTMDet-nano	$320 \times 320$	$256 \times 192$	4.47	83.5	

Table 6. Whole-body pose estimation results on COCO-WholeBody [28,60] V1.0 dataset. We only report the input size and GFLOPs of pose models in top-down approaches and ignore the detection model. "\*" denotes the model is pre-trained on AIC+COCO. "†" indicates multi-scale testing. Flip test is used.

	Method	Input Size	GFLOPs	whole	-body	bo	dy	fc	ot	fa	ce	ha	nd
				AP	AR	AP	AR	AP	AR	AP	AR	AP	AR
Whole-	SN† [22]	N/A	272.3	32.7	45.6	42.7	58.3	9.9	36.9	64.9	69.7	40.8	58.0
body	OpenPose [6]	N/A	451.1	44.2	52.3	56.3	61.2	53.2	64.5	76.5	84.0	38.6	43.3
Bottom-	PAF† [7]	512×512	329.1	29.5	40.5	38.1	52.6	5.3	27.8	65.6	70.1	35.9	52.8
up	AE [44]	512×512	212.4	44.0	54.5	58.0	66.1	57.7	72.5	58.8	65.4	48.1	57.4
	DeepPose [54]	384×288	17.3	33.5	48.4	44.4	56.8	36.8	53.7	49.3	66.3	23.5	41.0
	SimpleBaseline [59]	384×288	20.4	57.3	67.1	66.6	74.7	63.5	76.3	73.2	81.2	53.7	64.7
	HRNet [52]	384×288	16.0	58.6	67.4	70.1	77.3	58.6	69.2	72.7	78.3	51.6	60.4
	PVT [57]	384×288	19.7	58.9	68.9	67.3	76.1	66.0	79.4	74.5	82.2	54.5	65.4
Top-	FastPose50-dcn-si [18]	256×192	6.1	59.2	66.5	70.6	75.6	70.2	77.5	77.5	82.5	45.7	53.9
down	ZoomNet [28]	384×288	28.5	63.0	74.2	74.5	81.0	60.9	70.8	88.0	92.4	57.9	73.4
	ZoomNAS [60]	384×288	18.0	65.4	74.4	74.0	80.7	61.7	71.8	88.9	93.0	62.5	<b>74.0</b>
	RTMPose-m*	256×192	2.2	58.2	67.4	67.3	75.0	61.5	75.2	81.3	87.1	47.5	58.9
	RTMPose-1*	256×192	4.5	61.1	70.0	69.5	76.9	65.8	78.5	83.3	88.7	51.9	62.8
	RTMPose-1*	384×288	10.1	64.8	73.0	71.2	78.1	69.3	81.1	88.2	91.9	57.9	67.7
	RTMPose-x	384×288	18.1	65.2	73.2	71.2	78.0	68.1	80.4	89.0	92.2	59.3	68.7
	RTMPose-x*	384×288	18.1	65.3	73.3	71.4	78.4	69.2	81.0	88.9	92.3	59.0	68.5

Table 7. Training settings for RTMPose models.

8
AdamW
0.004
Flat-Cosine
1024
1000
0.05 (RTMPose-m/l)
0 (RTMPose-t/s)
0.9998 (RTMPose-s/m/l)
no EMA (RTMPose-t)
210 (pre-train)
420 (fine-tune)

COCO-WholeBody [28, 60] V1.0 dataset. As shown in Table 6, RTMPose achieves superior performance and well balances accuracy and complexity. Specifically, our RTMPose-m model outperforms previous open-source libraries [6, 18, 63] with significantly lower GFLOPs. And

by increasing the input resolution and training data we obtain competitive accuracy with SOTA approaches [28, 60].

**Other Datasets** As shown in Table 8 and Table 9, we further evaluate RTMPose on AP-10K [64], CrowdPose [32] and MPII [1] datasets. We report the model performance using ImageNet [13] pre-training for a fair comparison with baselines. Besides we also report the performance of our models pre-trained using a combination of COCO [37] and AI Challenger (AIC) [58], which achieves higher accuracy and can be easily reproduced by users with our provided pre-trained weights.

#### 4.3. Inference Speed

We perform the export, deployment, inference, and testing of models by MMDeploy [12] to test the inference speed on CPU and GPU respectively. Table 10 demonstrates the

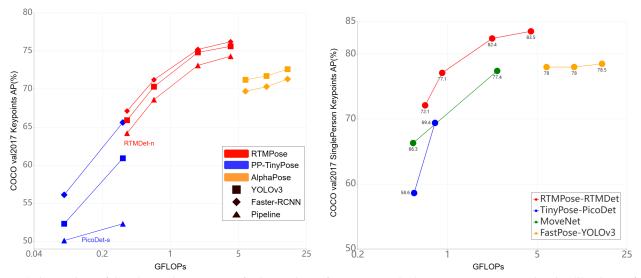


Figure 5. Comparison of GFLOPs and accuracy. Left: Comparison of RTMPose and other open-source pose estimation libraries on full COCO val set. Right: Comparison of RTMPose and other open-source pose estimation libraries on COCO-SinglePerson val set.

comparison of inference speed on the mobile device. We deploy RTMPose on the Snapdragon 865 chip with ncnn and inference with 4 threads. The TensorRT inference latency is tested in the half-precision floating-point format (FP16) on an NVIDIA GeForce GTX 1660 Ti GPU, and the ONNX latency is tested on an Intel I7-11700 CPU with ONNXRuntime with 1 thread. The inference batch size is 1. All models are tested on the same devices with 50 times warmup and 200 times inference for fair comparison. For TinyPose [2], we test it with both MMDeploy and FastDeploy, and note that ONNXRuntime speed on MMDeploy is slightly faster (10.58 ms vs. 12.84 ms). The results are shown in Table 11 and Table 12.

Table 8. Performance on different datasets. "\*" denotes the model is pre-trained on AIC+COCO and fine-tuned on the corresponding dataset. Flip test is used.

Dataset	Methods	Backbone	Input Size	GFLOPs	AP
AP-10K [64]	SimpleBaseline [59] HRNet [52] RTMPose-m RTMPose-m*	ResNet-50 HRNet-w32 CSPNeXt-m CSPNeXt-m	$\begin{array}{c} 256 \times 256 \\ 256 \times 256 \\ 256 \times 256 \\ 256 \times 256 \\ \end{array}$	7.28 10.27 <b>2.57</b> <b>2.57</b>	68.0 72.2 <b>68.4</b> <b>72.2</b>
CrowdPose [32]	SimpleBaseline [59] HRNet [52] RTMPose-m RTMPose-m*	ResNet-50 HRNet-w32 CSPNeXt-m CSPNeXt-m	$\begin{array}{c} 256 \times 192 \\ \end{array}$	5.46 7.7 1.93 1.93	63.7 67.5 66.9 70.6

Table 9. Comparison on MPII [1] validation set. "\*" denotes the model is pre-trained on AIC+COCO and fine-tuned on MPII. Flip test is used.

Dataset	Methods	Backbone	Input Size	GFLOPs	PCKh@0.5
MPII [1]	SimpleBaseline [59] HRNet [52] SimCC [35] TokenPose [36] RTMPose-m RTMPose-m*	ResNet-50 HRNet-w32 HRNet-w32 L/D24 CSPNeXt-m CSPNeXt-m	$\begin{array}{c} 256 \times 256 \\ 256 \times 256 \end{array}$	7.28 10.27 10.34 11.0 2.57 2.57	88.2 90.0 90.0 90.2 88.9 90.7

Table 10. Comparison of inference speed on Snapdragon 865. RTMPose models are deployed and tested using ncnn.

Method	s	Input Size	GFLOPs	AP(GT)	FP32(ms)	FP16(ms)
PaddleDetection [2]	TinyPose TinyPose	$\begin{array}{ c c c }\hline 128\times96\\ 256\times192\end{array}$	0.08 0.33	58.4 68.3	4.57 14.07	3.27 8.33
MMPose [11]	RTMPose-t RTMPose-s RTMPose-m RTMPose-l	$\begin{array}{c} 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ 256 \times 192 \\ \end{array}$	0.36 0.68 1.93 4.16	68.4 72.8 77.3 78.3	15.84 25.01 49.46 85.75	9.02 13.89 26.44 45.37

Table 11. Inference speed on CPU and GPU. RTMPose models are deployed and tested using ONNXRuntime and TensorRT respectively. Flip test is not used in this table.

R	esults	Input Size	GFLOPs	AP	CPU(ms)	GPU(ms)
	TinyPose	$256 \times 192$	0.33	65.6	10.580	3.055
	LiteHRNet-30	$256 \times 192$	0.42	66.3	22.750	6.561
	RTMPose-t	$256 \times 192$	0.36	67.1	3.204	1.064
COCO [37]	RTMPose-s	$256\times192$	0.68	71.2	4.481	1.392
	HRNet-w32+UDP	$256 \times 192$	7.7	75.1	37.734	5.133
	RTMPose-m	$256 \times 192$	1.93	75.3	11.060	2.288
	RTMPose-1	$256\times192$	4.16	76.3	18.847	3.459
	HRNet-w32+DARK	$256 \times 192$	7.72	57.8	39.051	5.154
	RTMPose-m	$256 \times 192$	2.22	59.1	13.496	4.000
COCO-	RTMPose-1	$256\times192$	4.52	62.2	23.410	5.673
WholeBody [28]	HRNet-w48+DARK	$384 \times 288$	35.52	65.3	150.765	13.974
	RTMPose-1	$384 \times 288$	10.07	66.1	44.581	7.678

Table 12. Pipeline Inference speed on CPU, GPU and Mobile device.

Model	Input Size	GFLOPs	Pipeline AP	CPU(ms)	GPU(ms)	Mobile(ms)
RTMDet-nano RTMPose-t	$\begin{array}{c c} 320\times320 \\ 256\times192 \end{array}$	0.31 0.36	64.4	12.403	2.467	18.780
RTMDet-nano RTMPose-s	$\begin{array}{ c c c c c }\hline 320\times320\\256\times192\\\hline\end{array}$	0.31 0.42	68.5	16.658	2.730	21.683
RTMDet-nano RTMPose-m	$\begin{array}{ c c c c c }\hline 320\times320\\ 256\times192\\ \end{array}$	0.31 1.93	73.2	26.613	4.312	32.122
RTMDet-nano RTMPose-l	$\begin{array}{ c c c c c }\hline 320\times320\\ 256\times192\\ \end{array}$	0.31 4.16	74.2	36.311	4.644	47.642

#### 5. Conclusion

This paper empirically explores key factors in pose estimation such as the paradigm, model architecture, training strategy, and deployment. Based on the findings we present a high-performance real-time multi-person pose estimation framework, RTMPose, which achieves excellence in balancing model performance and complexity and can be deployed on various devices (CPU, GPU, and mobile devices) for real-time inference. We hope that the proposed algorithm alone with its open-sourced implementation can meet some of the demand for applicable pose estimation in industry, and benefit future explorations on the human pose estimation task.

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