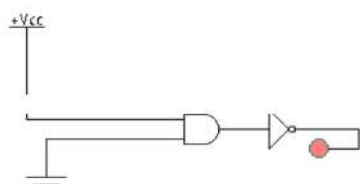
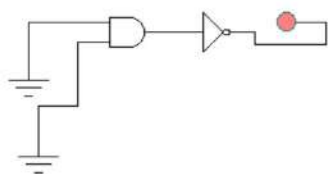
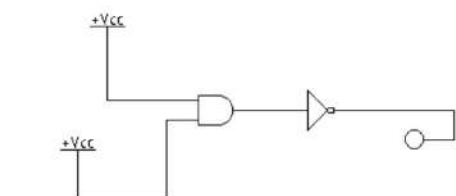
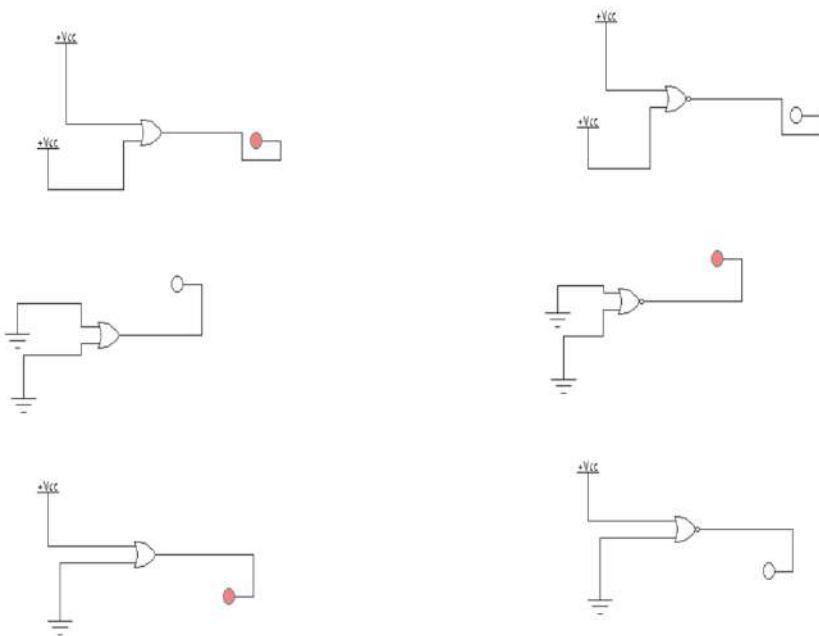


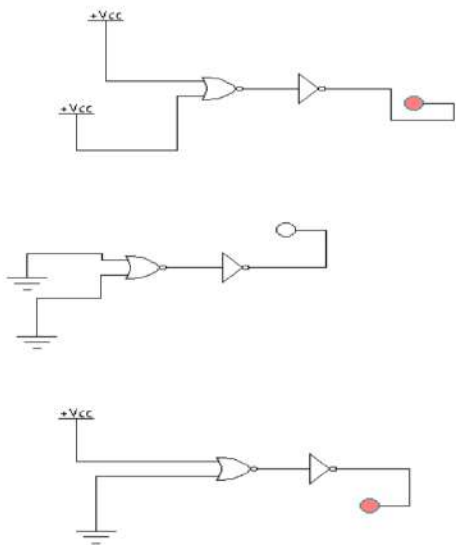
A	B	<u>A.B</u>	(A.B)'
0	0	0	1
1	1	1	0
1	0	0	1
0	1	0	1



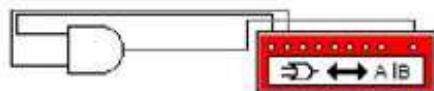
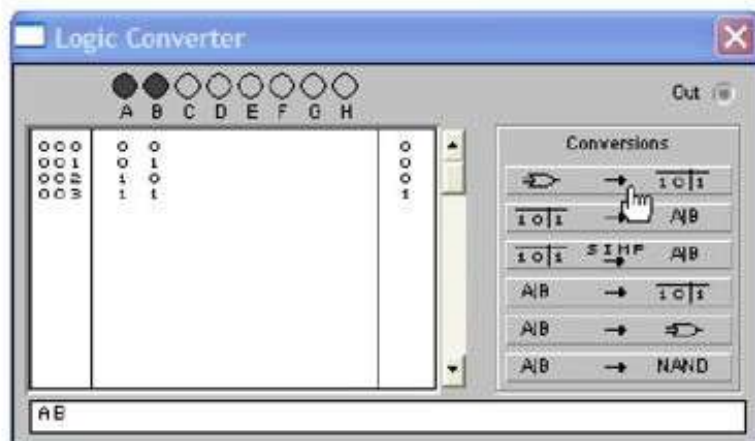
A	B	$(A.B)'$
0	0	1
1	1	0
1	0	1
0	1	1



A	B	A+B	(A+B)'
0	0	0	1
1	1	1	0
1	0	1	0
0	1	1	0



A	B	$((A+B)')'$
0	0	0
1	1	1
1	0	1
0	1	1





**Logic Converter**

Out: ☐

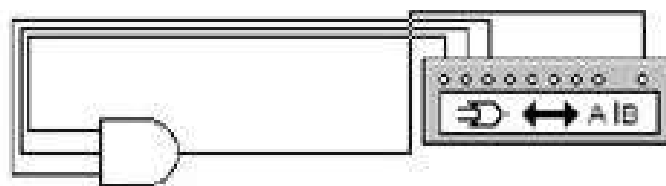
A B C D E F G H

000	0	0							1
001	0	1							0
002	1	0							0
003	1	1							0

Conversions:

- ☐  $\rightarrow$   $\overline{101}$
- $\overline{101} \rightarrow A|B$
- $\overline{101} \xrightarrow{\text{IMP}} A|B$
- $A|B \rightarrow \overline{101}$
- $A|B \rightarrow$  ☐
- $A|B \rightarrow \text{NAND}$

A' B'



### Logic Converter

Out ☐

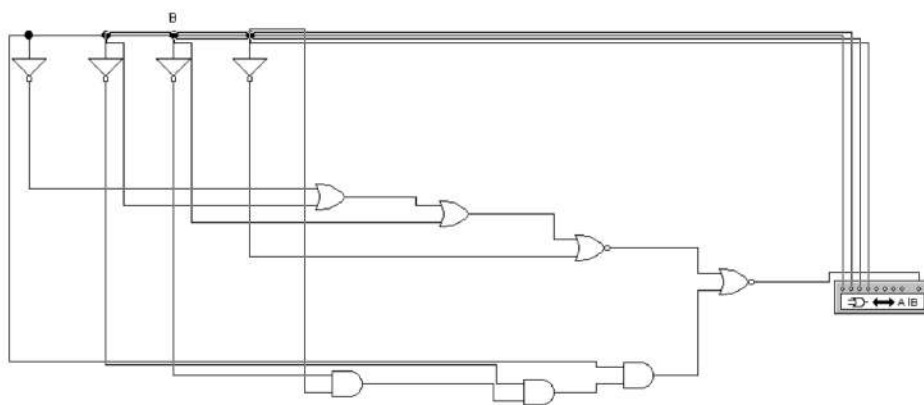
A B C D E F G H

000	0	0	0					0
001	0	0	1					0
002	0	1	0					0
003	0	1	1					0
004	1	0	0					0
005	1	0	1					0
006	1	1	0					0
007	1	1	1					1

ABC

#### Conversions

- ☐  $\rightarrow$   $\overline{101}$
- $\overline{101} \rightarrow$   $A/B$
- $\overline{101} \xrightarrow{\text{IMP}} A/B$
- $A/B \rightarrow \overline{101}$
- $A/B \rightarrow$  ☐
- $A/B \rightarrow$  NAND



Logic Converter

A B C D E F G H

000	0	0	0	0	0	0	0	1
001	0	0	0	0	1	0	0	1
002	0	0	0	1	0	0	0	1
003	0	0	1	0	0	0	0	1
004	0	1	0	0	0	0	0	1
005	0	1	0	1	0	0	0	1
006	0	1	1	0	0	0	0	1
007	0	1	1	1	0	0	0	1
008	1	0	0	0	0	0	0	1
009	1	0	0	1	0	0	0	1
010	1	0	1	0	0	0	0	1
011	1	0	1	1	0	0	0	1
012	1	1	0	0	0	0	0	1
013	1	1	0	1	0	0	0	1
014	1	1	1	0	0	0	0	1
015	1	1	1	1	0	0	0	1

Conversions

→   
 →   
 →   
 →   
 →   
 →

1