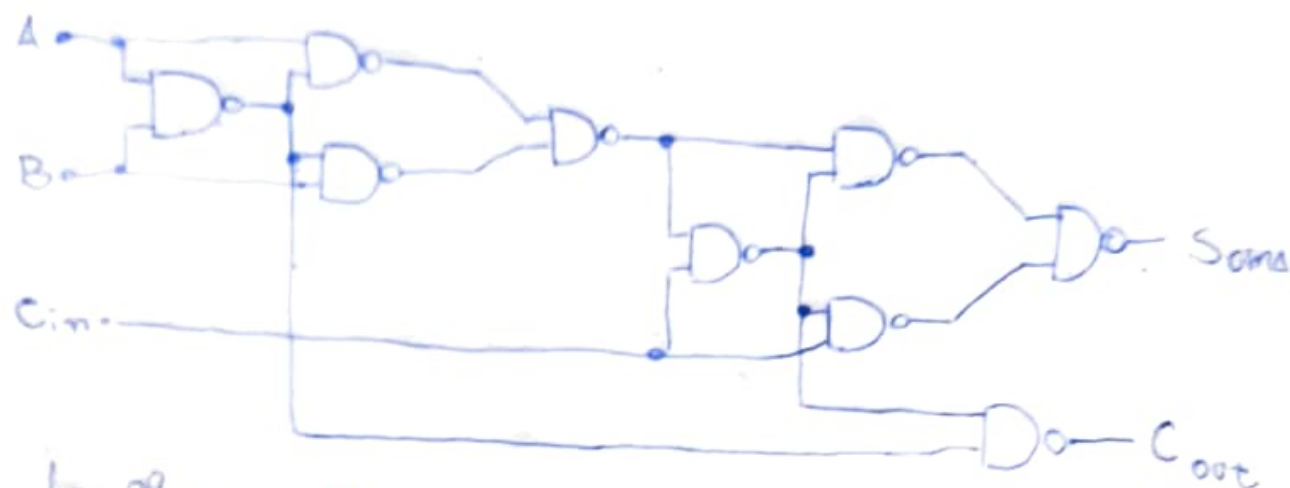
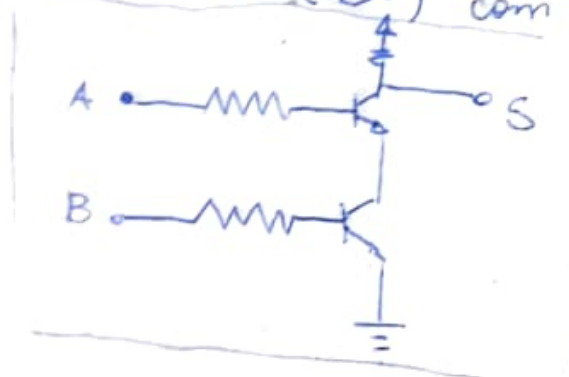


Full adder com Transistores.

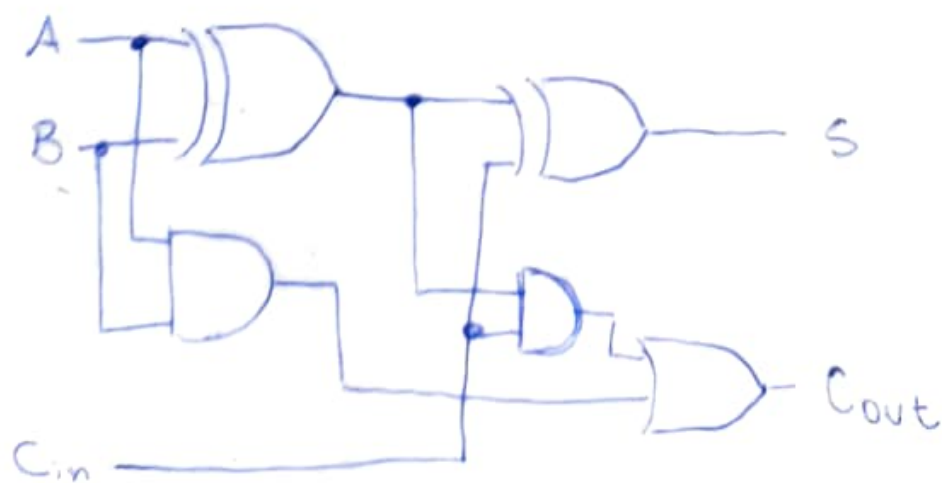


Full adder com NAND.

* 1) NAND (D₀) com transistores.



Obs: (Full adder pode ser feito também com porta XOR, AND e OR).



⇒ D₀: XOR

⇒ D₁: AND

⇒ D₂: OR