NO.	Instruction	Addr.	ASM	Comment
0	00000013	0	addi x0, x0, 0	
1	01c00083	4	lb x1, 0x01C(x0)	# F0F0F0F0 in 0x1C# FFFFFFF0 miss,read 0x010~0x01C to set 1 line 0
2	01c01103	8	lh x2, 0x01C(x0)	# FFFFF0F0 hit
3	01c02183	C	lw x3,0x01C(x0)	# F0F0F0F0 hit
4	01c04203	10	lbu x4,0x01C(x0)	# 000000F0 hit
5	01c05283	14	lhu x5,0x01C(x0)	# 0000F0F0 hit
6	21002003	18	lw x0,0x210(x0)	# miss,read 0x210~0x21C tocache set 1 line 1
7	abcde0b7	1C	lui x1 0xABCDE	
8	71c08093	20	addi x1, x1,0x71C	# x1 = 0xABCDE71C
9	00100023	24	sb x1, 0x0(x0)	# miss, read 0x000~0x00C tocache set 0 line 0
10	00101223	28	sh x1, 0x4(x0)	# hit
11	00102423	2c	sw x1, 0x8(x0)	# hit
12	20002303	30	lw x6,0x200(x0)	# miss, read 0x200~0x20Cto cache set 0 line 1
13	40002383	34	lw x7,0x400(x0)	# miss, write 0x000~0x00C back to ram,then read 0x400~40C to cache set 0 line 0
14	41002403	38	lw x8,0x410(x0)	# miss, no write back because of clean,read 0x410~41C to chache set 1 line0
15	0ed06813	3C	lori x16, x0,0xED	# end
16	ffdff06f	40	jal x0, loop	