Pacemaker Project Report

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1 Model Descriptions

In all the templates described below, **x** is a global clock that was declared in the Descriptions part of the model.

1.1 Heart

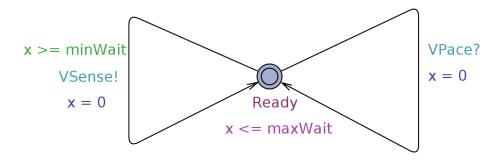


Figure 1: The Heart Model

The Heart starts in the Ready state. It can either beat naturally (when it sends VSense!) or it can beat as a result of a simulated signal that the pacemaker sends (hence the synchronization with VPace?). The values for minWait and maxWait were selected based on estimations of what seemed reasonable, but they can be changed if other values are more appropriate.

1.2 Ventricle

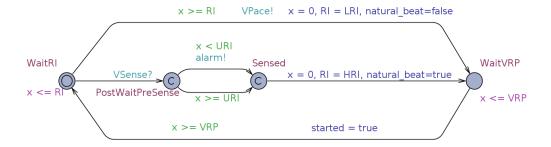


Figure 2: The Ventricle Model

The Ventricle starts in the WaitRI state. Here, it waits to sense the occurrence of a natural heartbeat within a certain time frame, and if the time bound is exceeded it sends a stimulus to the heart. The Ventricle then moves into the WaitVRP state, which is a period during which no signals can be received. Once the VRP has elapsed, the Ventricle will return to the WaitRI state and wait for Sense signals. If this is the first cycle, a boolean called started will be set to true.

We have implemented hysteresis pacing in this model. That is, the pacemaker only sends a simulated beat when the natural beating period has surpassed HRI; after this, RI is set to LRI. The pacemaker will not cease simulated beating until the period of the patients heartrate is less than or equal to LRI, after which RI is reset to HRI.

The URI value is used to determine if the heart is beating too fast. At this point, there is nothing that the pacemaker can do, but it can set an alarm. This functionality is provided by the PostWaitPreSense and Sensed states. These synchronize the alarm with the Observer.

1.3 Observer

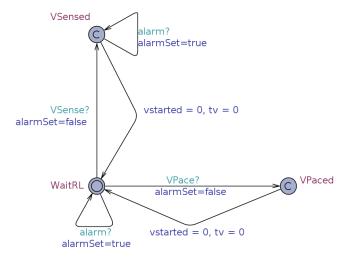


Figure 3: The Observer Model

The Observer is used to model the external monitor of the interaction between the pacemaker and the heart. The Observer simply waits for a sensed heartbeat or a simulated heartbeat, changes states, and then changes states back. In the implementation, these state transitions would likely be logged so that if a doctor or someone else wanted to assess the health of the heart they could use the frequency of simulated heartbeats as an assessment parameter.

There is an alarm that will set the alarmSet variable if the natural beating frequency is greater than the maximum expected beating frequency (URI). We note that currently, the alarmSet variable is not triggering anything, but in the real implementation, the alarmSet variable will cause a notification to be sent to the doctor/patient.

2 Query Descriptions

2.1 A[] not deadlock

This is the standard deadlock query. This query makes sure that our system does not get stuck in a particular state and stop progressing.

2.2 A[] (Ventricle.x < VRP && Ventricle.started) imply (Ventricle.WaitVRP || Ventricle.PostWaitPreSense || Ventricle.Sensed)

If the system has started, the pacemaker will always be waiting for the ventricle refractory period to expire prior to doing anything else.

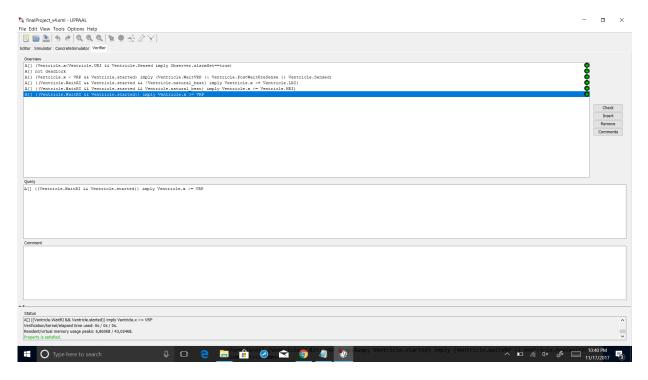


Figure 4: All queries pass

2.3 A[] ((Ventricle.WaitRI && Ventricle.started && !Ventricle.natural_beat) imply Ventricle.x <= Ventricle.LRI)

It will always be the case that if a simulated beat occurs, then the pacemaker will wait for a natural beat for the length of the LRI before continuing to pace.

2.4 A[] ((Ventricle.WaitRI && Ventricle.started && Ventricle.natural_beat) imply Ventricle.x <= Ventricle.HRI)

It will always be the case that if the heart has been beating normally, then the pacemaker will wait until the heart rate is less than hysteresis rate before intervening.

2.5 A[] ((Ventricle.WaitRI && Ventricle.started)) imply Ventricle.x >= VRP

It is always the case that if the system has started and is currently waiting, the ventricle refractory period of the previous beat-cycle is complete.

2.6 A[] (Ventricle.x < Ventricle.URI && Ventricle.Sensed imply Observer.alarmSet == true)

If the heart is beating faster than the upper rate limit, then an alarm will be set on the monitor.

3 References

- 1. PACEMAKER System Specification, Copyright 2007 Boston Scientific, January 3, 2007.
- Pacemaker Timing Cycles-F13-F17-v3.pdf, CIS 441/541: Embedded Systems for CPS/IoT, Fall 2017 by Prof. Insup Lee, Department of Computer and Information Science, School of Engineering and Applied Science, University of Pennsylvania.

3. Kirk, M. Basic principles of pacing: Chapter 1. http://www.BPL_Images/Content_store/Sample_chapter/97807279156	