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| **Date:13/9/2014** |  |
| **Time:** | 4pm |
| **Venue:** | Lib Project Room 4.4 |
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| **Attendees:** | All present |
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| **Agenda:** | 1. Logical Diagram  2. System Sequence Diagram  3. Overall Design Layout  4. Plan Iteration 3-5  5. Update PPlog |

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|  |  |  | **Due Date** |
| 1 | Finish System Sequence Diagram | Yuxuan&Wudi | 13 September |
| 2. | Finish wireframe design | Aayush&Eva | 13 September |
| 3. | Finish Logical diagram | Max&Wudi | 13 September |
| 4.  5. | Finish Iteration3-5 plan  Update PPlog | Everyone  Everyone | 13 September  13 Septembe |

After exchanging opinions, we have come up with the finalized System Sequence Diagram, wireframe design and logical diagram.

Details for Iteration 3-5 have been updated, and will upload to pplog after the meeting by Project Managers for corresponding iterations.

The meeting was adjourned at 8.30 pm. These minutes will be circulated and adopted if there are no amendments reported in the next three days.

Prepared by,

Yuan Yuxuan

Vetted and edited by,

Aayush Garg