

Title page: EEDG/CE 6302 Lab Report 8

CE6302, Embedded Systems, 302 Laboratory - 83204

Lab Topic: MSP432: UART Communication between two MSP432 boards

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1. Objective:

Purpose: In lab 8, the purpose is to explore UART (Universal Asynchronous Receiver/Transmitter) communication and learn how to configure UART communication on MSP432 microcontrollers. UART is a widely used serial communication protocol for transmitting and receiving data between devices. To establish a valid UART connection between transmitter and receiver, configuration for both devices is needed. After that, a demo of sending a character from the transmitter and turning on the corresponding LED based on the receiving character in the receiver is performed to validate the UART connection.

Methods used: UART using serial communication to transmit data. The data frame consists of start bit, data bits, parity bit(optional), and stop bit. Using two register to configure the baud rate(UCAxBRW and UCAxMCTLW), UCAxBRW stores the prescaler value and UCAxMCTLW is the modulator with UCOS16, UCBRSx, and UCBRFx.

Results: After calculating and configuring the value of UCBRx, UCBRFx, and UCBRSx, a desired baud rate is set on both transmitter and receiver in channel A2. And in the transmitter, the channel A0 is also set for receiving characters from the computer. When the transmitter connects to the computer and type character r, g, b in CCS terminal, the red, green, and blue LED light in the receiver will switch on respectively.

Software used: Code Composer Studio (CCS)

Major Conclusions: In the lab, by configuring the Baud rate generator, UART channel A2 is established between transmitter and receiver, and another UART channel A0 is established between transmitter and computer. By type characters in the CCS terminal in the computer, the receiver toggles its LED with a different color correspondingly.

2. Introduction:

2.1 Hardware and Software Background Information

Code Composer Studio is an integrated development environment (IDE) for TI's microcontrollers and processors. It comprises a suite of tools used to develop and debug embedded applications. Code Composer Studio includes an optimizing C/C++ compiler, source code editor, project build environment, debugger, profiler and many other features. Code Composer Studio combines the advantages of the Eclipse® and Theia frameworks with advanced capabilities from TI resulting in a compelling feature-rich environment. The cloud-based Code Composer Studio enables development in the cloud without the need to download and install large amounts of software.

2.2 Purpose of the Experiment

The purpose of this project is to explore and implement UART (Universal Asynchronous Receiver/Transmitter) communication on MSP432 microcontrollers. By configuring UART communication between two MSP432 devices, the project demonstrates how to establish two-way communication between a transmitter and a receiver. The primary goal is to transmit characters from the transmitter side, which are then received by the receiver side to toggle and display different LED colors. This helps in understanding how UART works for serial data transmission and reception, along with hands-on experience in configuring UART on embedded systems like the MSP432.

2.3 Summary of the Experiment

In lab 8, UART connections based on modules A0 and A2 are established. With configuring the Baud rate generator(using SMCLK, BRW=19, set BRF to 9, enable oversampling), the desired baud rate is set. By type 'r', 'g', 'b', the character is sent from computer to transmitter, and then sent from transmitter to receiver. By receiving the character in the receiver, it toggles the LED with corresponding color.

2.4 Findings of the experiment

When setting the BRF and BRS bits inside the MCTLW register, a corresponding offset is needed. And when Oversampling Mode (OS16) is on, the clock source frequency is divided by 16 to determine the actual baud rate.

3. Explanation:

3.1 Experiment procedure

This lab exercise focuses on implementing UART communication between two MSP432P401R microcontrollers using Code Composer Studio. The procedure involves creating separate projects for a transmitter and a receiver. For the transmitter, EUSCI_A0 is configured to receive characters from a keyboard, while EUSCI_A2 is set up to transmit these characters to the receiver. Both UART modules are configured with SMCLK as the clock source, and the baud rate is set to 9600 using appropriate BRW and modulation control word calculations. The receiver project configured EUSCI_A2 to receive characters from the transmitter and implemented logic to toggle LED2 based on the received character's color ('r' for red, 'g' for green, 'b' for blue). Error handling is included to blink LED1 twice if an unexpected character is received.

The hardware setup involves connecting two MSP432 LaunchPads to a computer via micro-USB cables. After compiling and loading the respective code onto each board, the UART communication is established by cross-connecting the TX and RX pins (P3.2 and P3.3) between the transmitter and receiver boards. This setup allows for practical implementation and testing of UART communication, providing hands-on experience with serial data transmission, interrupt handling, and GPIO control in embedded systems programming.

3.2 Experiment Images for Part 1

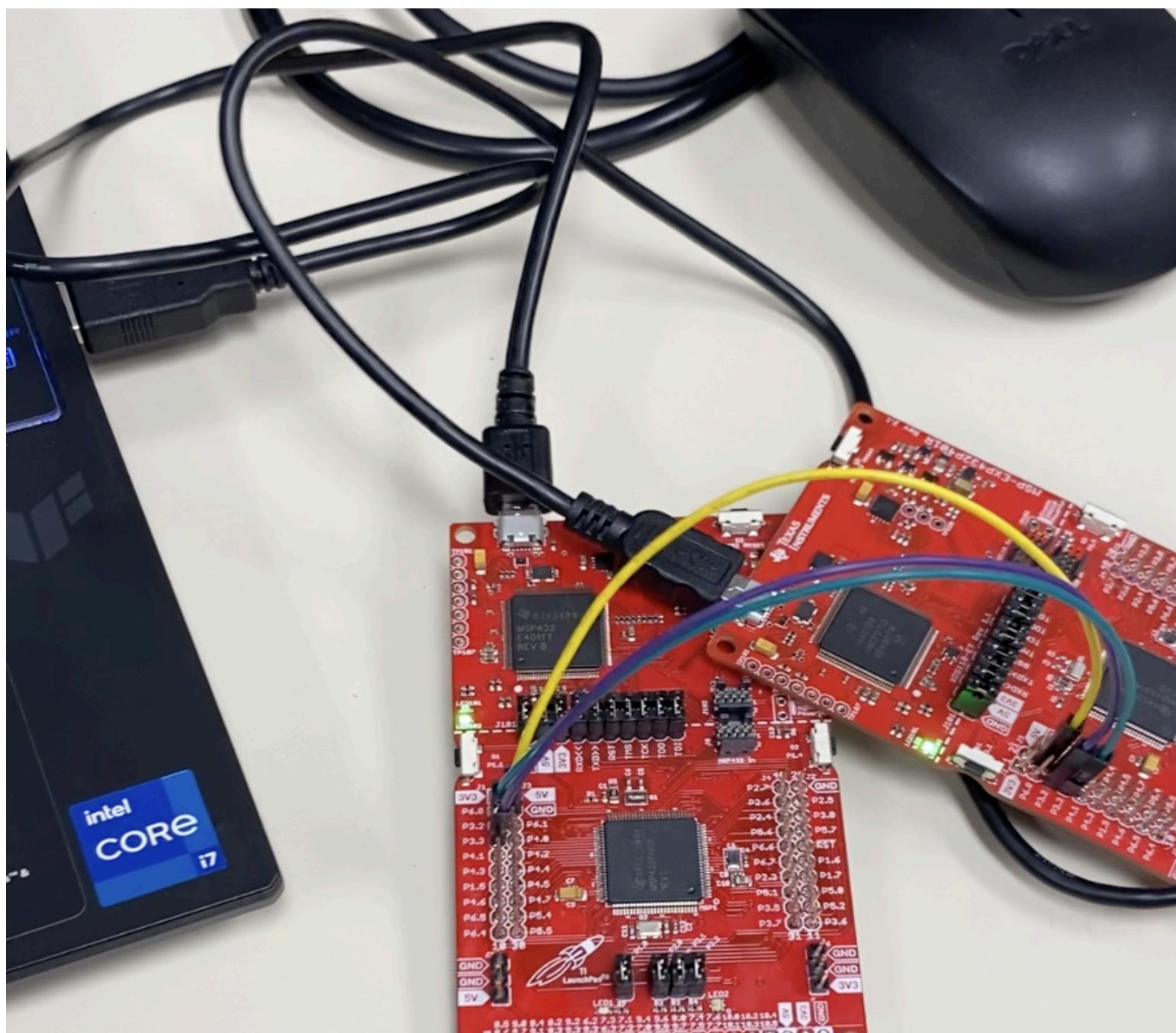


Image 3.2-1: connection between transmitter and computer / transmitter and receiver.

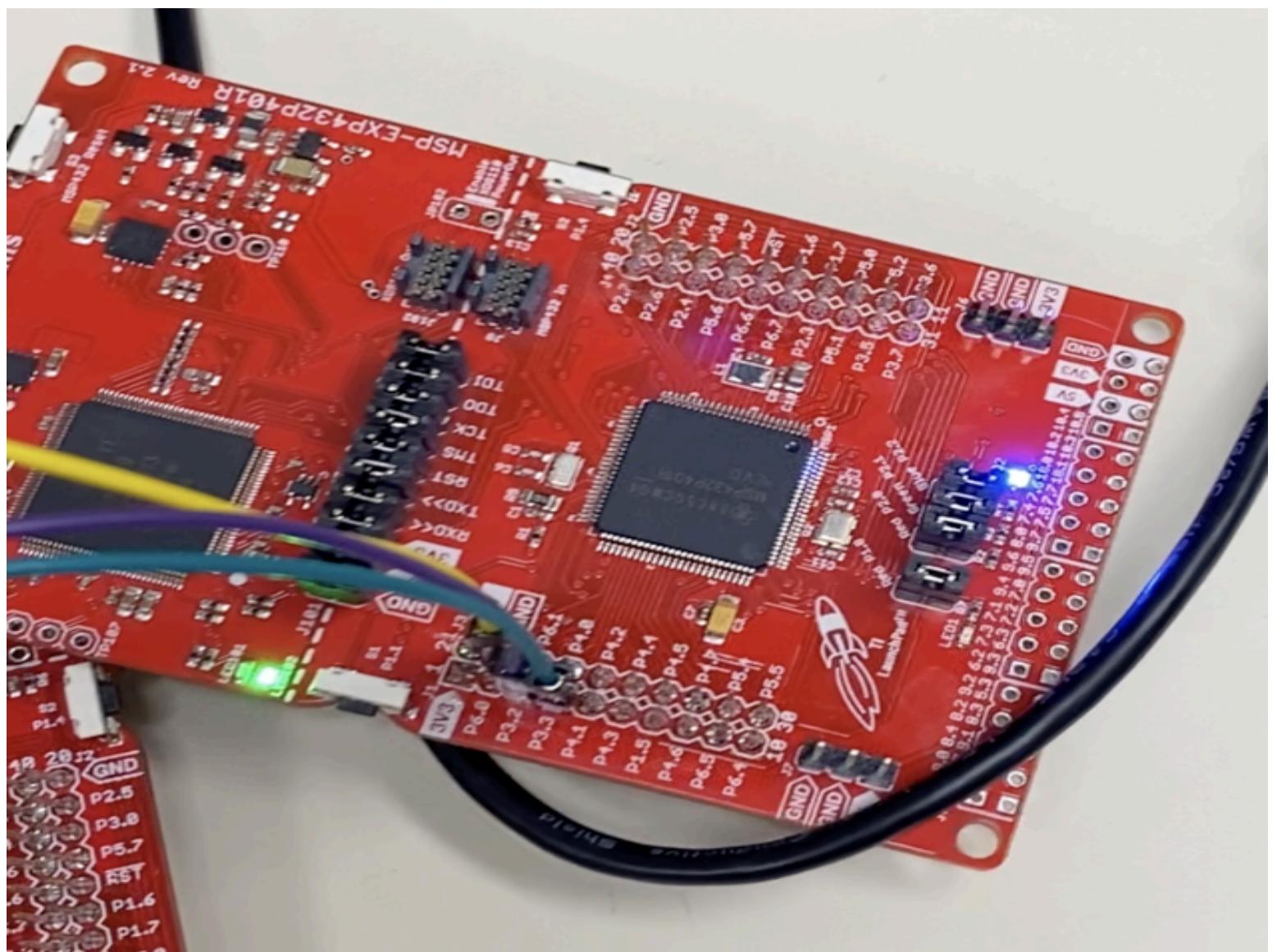


Image 3.2-2: The blue LED on the receiver turns on

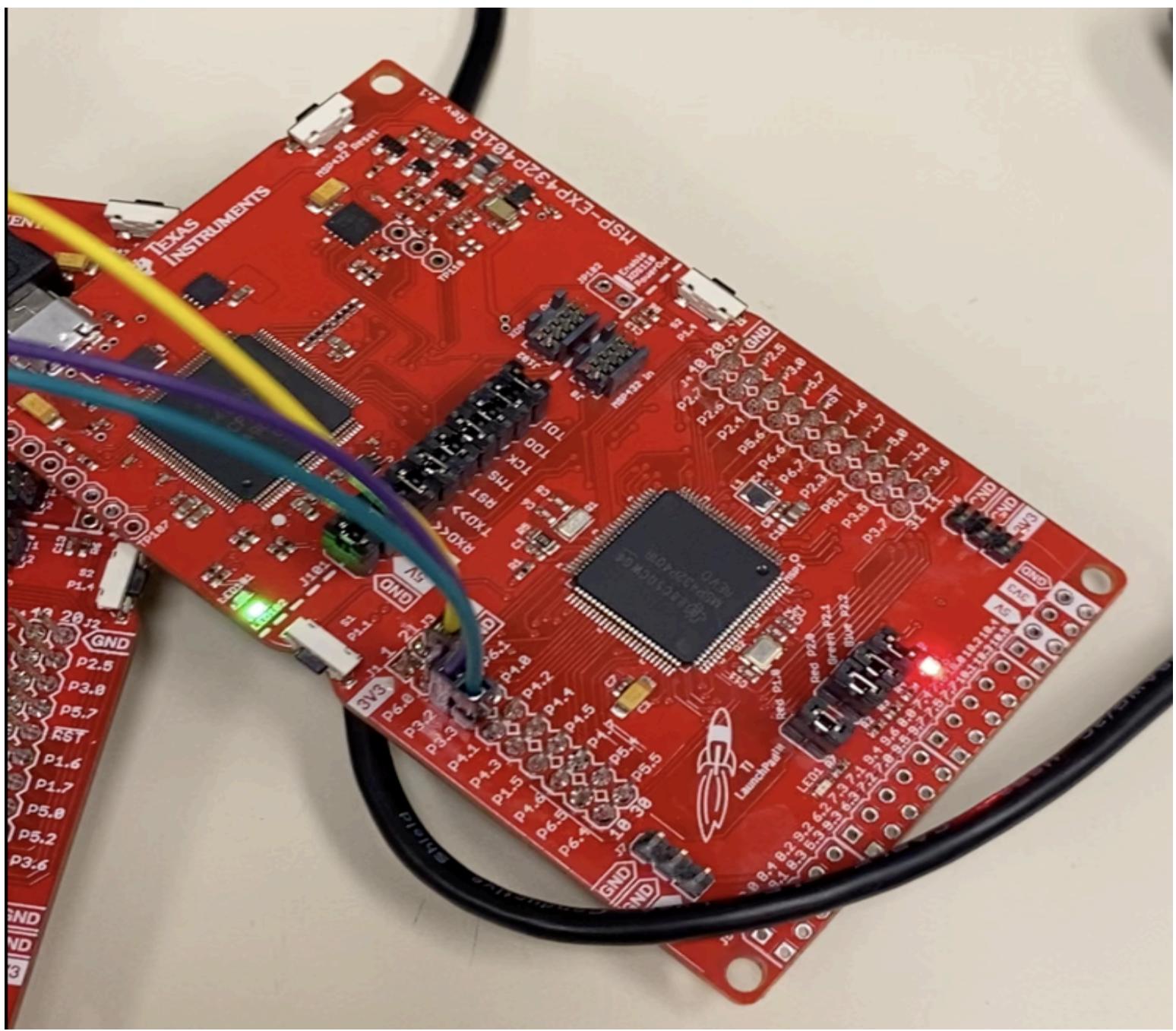


Image 3.2-3: The red LED on the receiver turns on

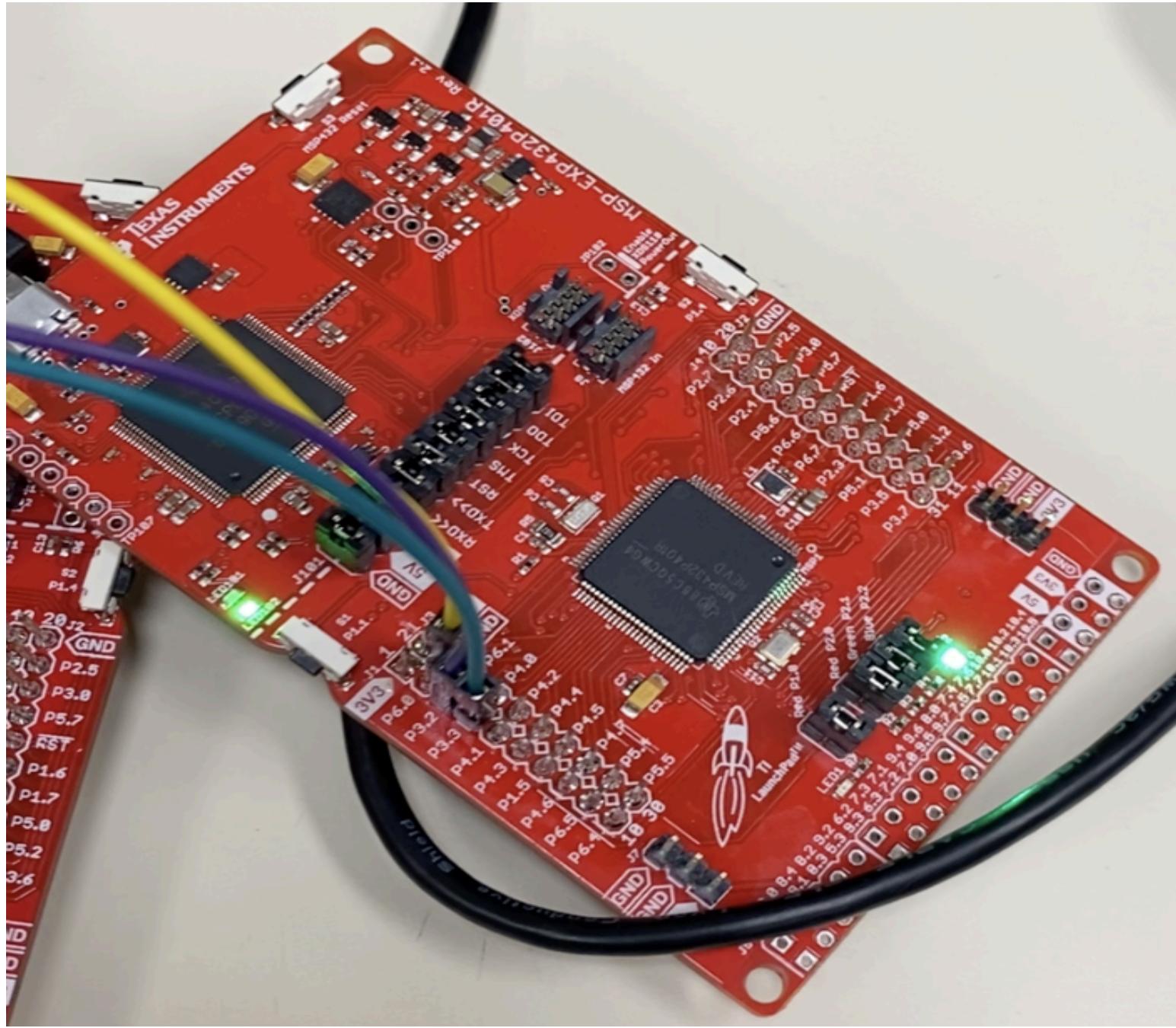


Image 3.2-4: The green LED on the receiver turns on

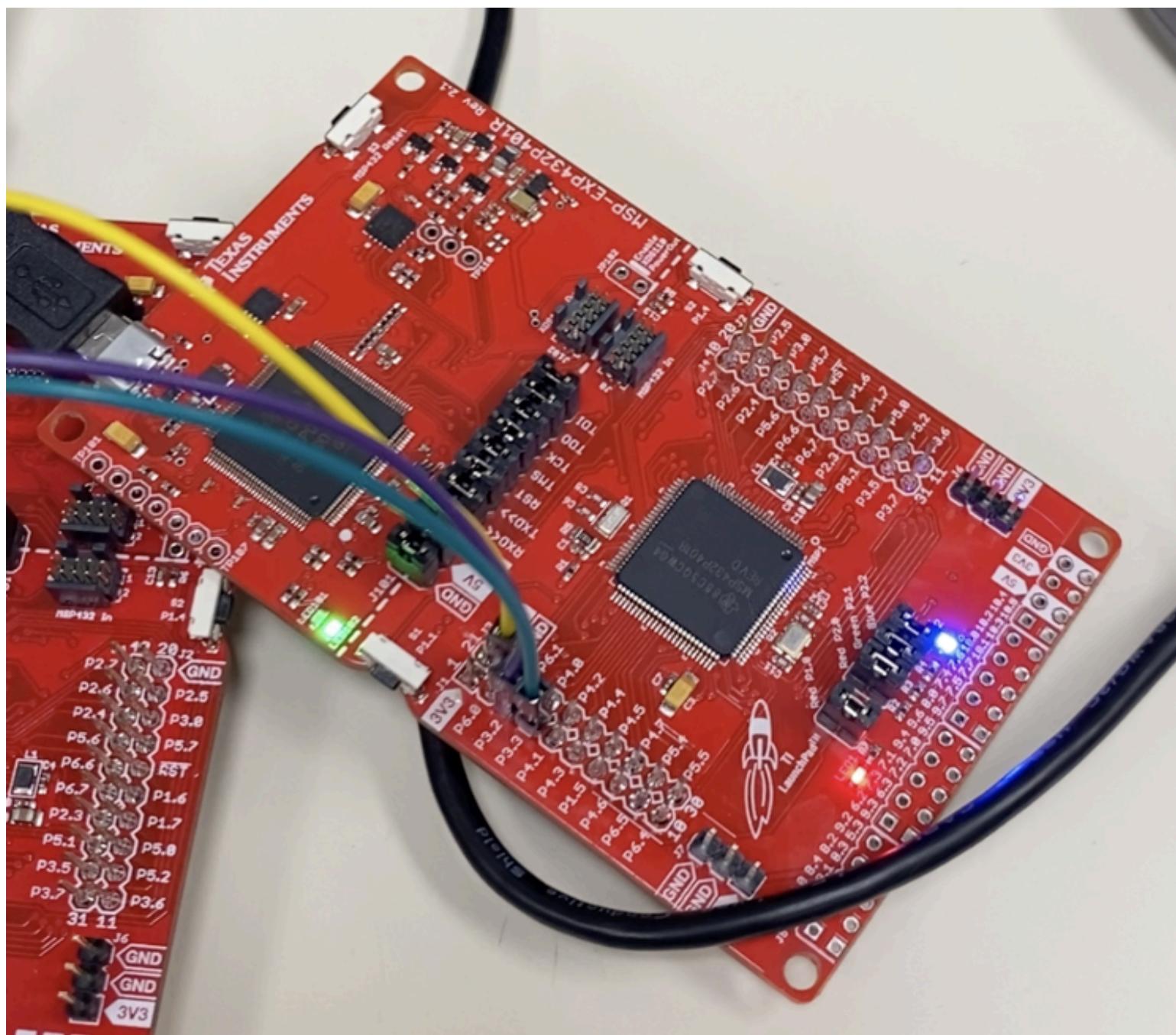


Image 3.2-5: The error LED on the receiver flashes

3.3 Code

```

1 #include "msp.h"
2 #include<stdint.h>
3 #include<stdbool.h>
4
5 void sendString(char *str);
6 void sendChar(char s);
7
8 void main(void)
9 {
10     WDT_A->CTL = WDT_A_CTL_PW | WDT_A_CTL_HOLD; // stop watchdog timer
11
12     // Enable UART0 Pins
13     // P1.2->RX
14     // P1.3->TX
15     P1->SEL0 |= BIT2 | BIT3;
16     P1->SEL1 &= ~(BIT2 | BIT3);
17
18     // UART0 Configuration
19     EUSCI_A0->CTLW0 = EUSCI_A_CTLW0_SWRST; // Clear previous configuration of UART
20     EUSCI_A0->CTLW0 |= EUSCI_A_CTLW0_SSEL__SMCLK; // Use SMCLK as clock source (3MHz)
21     //N = clock source / baud rate = 3000000 / 9600 / 16 = 19
22     EUSCI_A0->BRW = 19; // Baud Rate 9600
23
24     //19.53125 - 19 = 0.53125 * 16 = 8.5, round up to 9
25     EUSCI_A0->MCTLW = (9 << EUSCI_A_MCTLW_BRF_OFS | EUSCI_A_MCTLW_OS16);
26
27     EUSCI_A0->CTLW0 &= ~EUSCI_A_CTLW0_SWRST; // Release reset
28     EUSCI_A0->IFG &= ~EUSCI_A_IFG_RXIFG; // Clear interrupt flag
29     EUSCI_A0->IE |= EUSCI_A_IE_RXIE; // Enable RX interrupt
30
31     // Enable UART2 Pins
32     // P3.2->RX
33     // P3.3->TX
34     P3->SEL0 |= BIT2 | BIT3;
35     P3->SEL1 &= ~(BIT2 | BIT3);
36
37     // UART2 Configuration
38     EUSCI_A2->CTLW0 = EUSCI_A_CTLW0_SWRST; // Clear previous configuration of UART
39     EUSCI_A2->CTLW0 |= EUSCI_A_CTLW0_SSEL__SMCLK; // Use SMCLK as clock source
40     EUSCI_A2->BRW = 19; // Baud Rate 9600
41
42     //19.53125 - 19 = 0.53125 * 16 = 8.5, round up to 9
43     EUSCI_A2->MCTLW = (9 << EUSCI_A_MCTLW_BRF_OFS | EUSCI_A_MCTLW_OS16);
44     EUSCI_A2->CTLW0 &= ~EUSCI_A_CTLW0_SWRST; // Release reset
45
46     EUSCI_A2->IFG &= ~EUSCI_A_IFG_RXIFG; // Clear interrupt flag
47     EUSCI_A2->IE |= EUSCI_A_IE_RXIE; // Enable RX interrupt
48
49     // Enable NVIC for UART0
50     NVIC->ISER[0] = 1 << (EUSCI_A0_IRQn & 31);
51     // Enable global interrupts
52     __enable_irq();

```

```

54     sendString("Enter r for red, g for green, b for blue!\r\n"); // send message
55
56     while (1)
57     {
58         // do nothing
59     }
60 }
61
62 void EUSCIA0_IRQHandler(void)
63 {
64     if (EUSCI_A0->IFG & EUSCI_A_IFG_RXIFG) // receive interrupt
65     {
66         char c = EUSCI_A0->RXBUF; // store data into character buffer, and clear flag
67         sendString(&c);
68         sendChar(c); // display character through the SERIAL port
69     }
70 }
71
72 void sendString(char *str)
73 {
74     int i = 0;
75     while (str[i] != '\0')
76     {
77         while (!(EUSCI_A0->IFG & EUSCI_A_IFG_TXIFG)); // Wait until TXBUF is empty
78         EUSCI_A0->TXBUF = str[i]; // Send character through buffer
79         i++;
80     }
81 }
82
83 void sendChar(char s)
84 {
85     while (!(EUSCI_A2->IFG & EUSCI_A_IFG_TXIFG)); // Wait until TXBUF is empty
86     EUSCI_A2->TXBUF = s; // Send character through buffer
87 }

```

Image 3.3-1: code for transmitter

```

1 #include "msp.h"
2
3 void main(void)
4 {
5     WDT_A->CTL = WDT_A_CTL_PW | WDT_A_CTL_HOLD; // stop watchdog timer
6
7     // Error LEDs
8     P1->DIR |= BIT0;      // set BIT0 as OUTPUT
9     P1->OUT &= ~(BIT0); // set BIT0 as LOW
10    P1->SEL0 &= ~(BIT0);
11    P1->SEL1 &= ~(BIT0);
12
13    // RGB LEDs
14    P2->DIR |= BIT0 | BIT1 | BIT2;      // set BIT0,1,2 as OUTPUTS
15    P2->OUT &= ~(BIT0 | BIT1 | BIT2); // set BIT0,1,2 as LOW
16    P2->SEL0 &= ~(BIT0 | BIT1 | BIT2);
17    P2->SEL1 &= ~(BIT0 | BIT1 | BIT2);
18
19    // Enable UART2 Pins (P3.2->RX, P3.3->TX)
20    P3->SEL0 |= BIT2 | BIT3;
21    P3->SEL1 &= ~(BIT2 | BIT3);
22
23    // UART2 Configuration
24    EUSCI_A2->CTLW0 = EUSCI_A_CTLW0_SWRST; // Clear previous configuration of UART
25    EUSCI_A2->CTLW0 |= EUSCI_A_CTLW0_SSEL_SMCLK; // Use SMCLK as clock source
26    EUSCI_A2->BRW = 19; // Baud Rate 9600
27
28    //19.53125 - 19 = 0.53125 * 16 = 8.5, round up to 9
29    EUSCI_A2->MCTLW = (9 << EUSCI_A_MCTLW_BRF_OFS | EUSCI_A_MCTLW_OS16);
30    EUSCI_A2->CTLW0 &= ~EUSCI_A_CTLW0_SWRST; // Release reset
31
32    EUSCI_A2->IFG &= ~EUSCI_A_IFG_RXIFG; // Clear interrupt flag
33    EUSCI_A2->IE |= EUSCI_A_IE_RXIE; // Enable RX interrupt
34
35    // Enable NVIC for UART2
36    NVIC->ISER[0] = 1 << (EUSCIA2_IRQn & 31);
37
38    // Enable global interrupts
39    __enable_irq();
40
41    while (1); // Infinite loop
42 }
43
44 void EUSCIA2_IRQHandler(void)
45 {
46     if (EUSCI_A2->IFG & EUSCI_A_IFG_RXIFG) // receive interrupt
47     {
48         char c = EUSCI_A2->RXBUF; // read the received character
49         int i, j; // Declare variables outside the loops
50
51         switch (c)
52         {
53             case 'r':
54                 P2->OUT &= ~(BIT0); // set BIT0 as LOW
55                 P2->OUT &= ~(BIT1); // set BIT0 as LOW
56                 P2->OUT &= ~(BIT2); // set BIT0 as LOW

```

```

57     P2->OUT ^= BIT0; // Toggle Red LED (P2.0)
58     break;
59   case 'g':
60     P2->OUT &= ~(BIT0); // set BIT0 as LOW
61     P2->OUT &= ~(BIT1); // set BIT0 as LOW
62     P2->OUT &= ~(BIT2); // set BIT0 as LOW
63     P2->OUT ^= BIT1; // Toggle Green LED (P2.1)
64
65     break;
66   case 'b':
67     P2->OUT &= ~(BIT0); // set BIT0 as LOW
68     P2->OUT &= ~(BIT1); // set BIT0 as LOW
69     P2->OUT &= ~(BIT2); // set BIT0 as LOW
70     P2->OUT ^= BIT2; // Toggle Blue LED (P2.2)
71
72     break;
73
74
75   default:
76     // Blink Error LED (P1.0) twice
77     for (i = 0; i < 2; i++)
78     {
79       P1->OUT ^= BIT0; // Toggle Error LED (P1.0)
80       for (j = 0; j < 100000; j++); // Simple delay
81       P1->OUT ^= BIT0; // Toggle Error LED off
82       for (j = 0; j < 100000; j++); // Simple delay
83     }
84     break;
85   }
86
87   EUSCI_A2->IFG &= ~EUSCI_A_IFG_RXIFG; // Clear the receive interrupt flag
88 }
89 }

```

Image 3.3-2: code for receiver

4. Discussions and Conclusions:

4.1 Comparison of your experimental results with the research/theory of the concept with reasoning.

In this lab, the experimental setup involved configuring UART communication on the MSP432 microcontroller, enabling data exchange between a transmitter and a receiver board to control RGB LEDs. Theoretical concepts, such as baud rate, start/stop bits, and parity, were applied practically to ensure accurate data transmission. The baud rate was set to 9600, matching the typical standards in UART communication, and the experiment successfully demonstrated how microcontrollers manage asynchronous serial data transfer without a shared clock. The LEDs toggled based on received data, confirming the successful implementation of UART interrupts and clock configurations. The results aligned with the theoretical understanding of UART, showcasing its reliability and efficiency in real-time communication and hardware control.

4.2 Learnings from the experiment

This lab provided an understanding of UART (Universal Asynchronous Receiver/Transmitter) communication, a widely used protocol for serial data exchange between devices. It involved configuring UART on the MSP432 microcontroller to enable data transmission and reception between two devices. The setup allowed for the transmission of characters from a transmitter board to control the RGB LEDs on a receiver board. The LEDs toggled based on the received data, demonstrating how to manage communication and control hardware components through interrupts and UART configuration. Key concepts included setting up clock configurations, adjusting the baud rate (in this case, 9600), and using software delay loops for timing. This exercise highlighted how microcontrollers communicate and interact with peripheral devices using UART protocols, ensuring reliable and accurate data transfer.

5. References:

Texas Instruments, <https://www.ti.com/tool/CCSTUDIO>