Homework & Solutions

$$|a| S = \frac{130 \text{ nm}}{32 \text{ nm}} = 4.0625.$$

Area scales by 1/52 = 0.06059

Anew = 0.06059 x Aoul = 0.06059 x 0.2 Mm² = 0.01212 Mm².

b. | Capacitance scales by 1/s = 0.2462.

Crew = 1/5 x Cold = 0:2462 x 50 ff = 12.31 ff.

c. 7 sales by 1/s = 0.2462

Then = 1/5 x Told = 0.2462 x 20 ps = 4.92 ps.

d. R is constant in Denard Scaling => Rnew = Rold = 0.1 st.

Prob (all INV are good) = (1-00000003)1,000,000 = 74.190

Prob (au A0I22 are good) = (1-0.0000045) 200,000 = 40.7%

Prob(au NOR2 are good) = (1-0.00001) 200,000 = 13.5%

Yield = 11 Prob = 3.02%.

Prob(INV) = (1-0.00000003)500,000 = 86.1%

Prob (AUI22) = (1-0.0000045) 100,000 = 63.8%

Prob(NOR2) = (1-0.00001)(00,000 = 36.8%

Tierd = 17.4%

c. Fros (NAND2) = (1-0.000001) 100,000 = 90.5%

Prob(INV) = (1-0.0000003) 323,333 = 90.5% Prob(A0I22) = (1-0.0000045) 66,667 = 74.1%

Pros (NOR2) = (1-0.00001) 66,667 = 51.3%

Yield = 31.1%.

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39 Using Poisson's model.
    Yield = e-AD = e-2mm2 x 1/2mm2 = e-1 = 36.8%
b. Tield = e - 1 mm x 1/2 mm = = = 1/2 = 60.65%.
    Yield = 2 0.5 mm²x 1/2 mm² = e - 1/4 = 77.88%:
9. Yield = e-2nm². //nm² = e-2 = 13.53%
6. Yield = e -1mm²x //mm² = e-1 = 36.8%.
c. Yield = e -0.5mm²x //mm² = e-0.5 = 60.65%.
4. Inverter Variance = Estandard deviation)2 = (IPS)2 = 1 P52
   Expected delay of ring oscillator has a mean of 11×10/5 =110/s.
   Ring Oscillator also has a variance of 11x 1Ps2 = 11ps2
   >RO standard deviation = JIIps2 = 3.317ps.
   From table 7.9, maximum of 100 ROs will have a mean of 110 ps + (2.5 x 3.22 ps)
                                                                     = 1/8 ps.
                                         and standard deviation = 0.43×3.22 ps
. 9. Period = EX118PS = 236PS (Positive 8 negative edge).
   frequency = 1/236 PS = 4.24 GHz.
6: 99.7% is M+30 (mean +[3x standard deviation]) => Delay = 118+(3x1.38)
                                                                  = 122 Ps.
   Period = 2 x 122 = 244 ps => f = 1/244 ps = 4.10 GHz.
          failure rate. I III

infant Useful oferating life Wear.

mortality out
5.
   Chips are most likely to fail really early or have a long life. A good idea
    to weed out chips that will fail early is to stress chips during testing.
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