P1: INVERTER LAYOUT AND SIMULATION

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Clearly state the results as in the template table  
A black and white table with red writing

AI-generated content may be incorrect.

A screen shot of a computer

AI-generated content may be incorrect.

Fig1: Layout

A computer screen shot of a circuit

AI-generated content may be incorrect.  
Fig2: Schematic

A screenshot of a computer

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Fig3: DRC

A screenshot of a computer

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Fig4: LVS

A screenshot of a computer

AI-generated content may be incorrect.  
Fig5: HSpice test setup file  
  
A screenshot of a computer

AI-generated content may be incorrect.  
Fig6: Netlist file  
  
A screenshot of a computer

AI-generated content may be incorrect.  
Fig6: Waveforms from WaveView  
A screenshot of a computer

AI-generated content may be incorrect.  
Fig7: Screenshot of HSPICE output terminal showing the simulation results  
  
A screenshot of a computer

AI-generated content may be incorrect.  
Fig8: .mt0 file