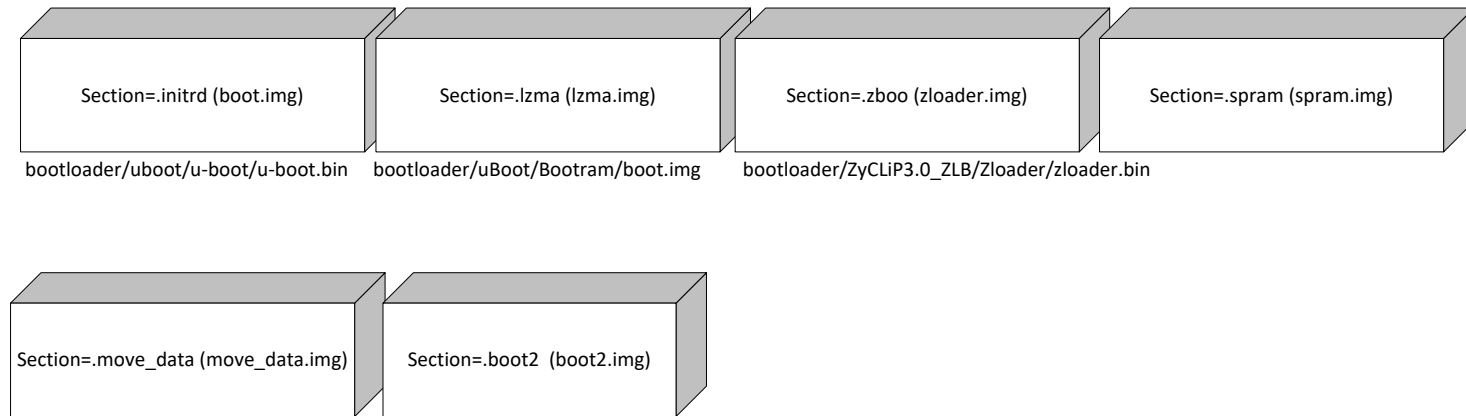
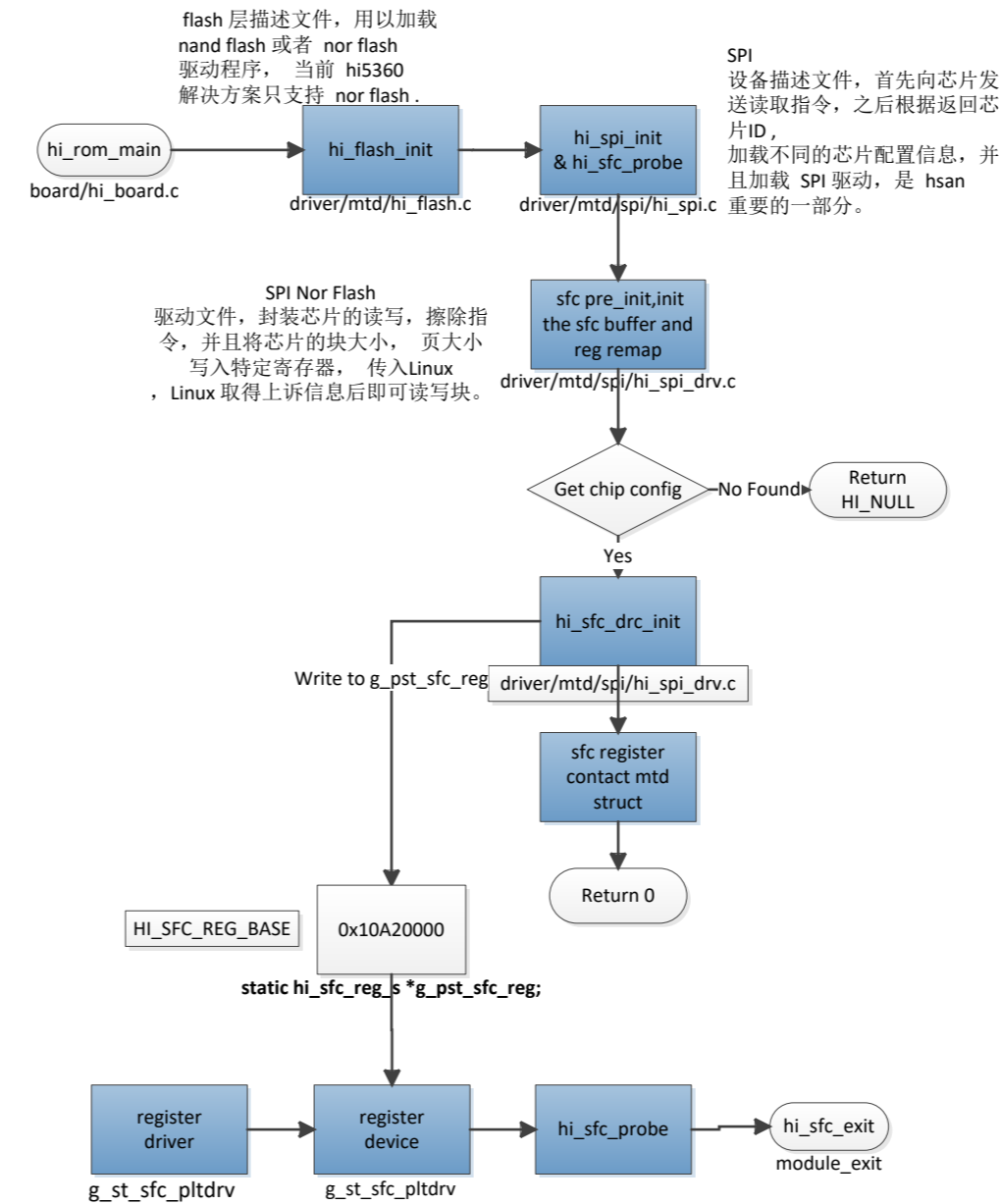
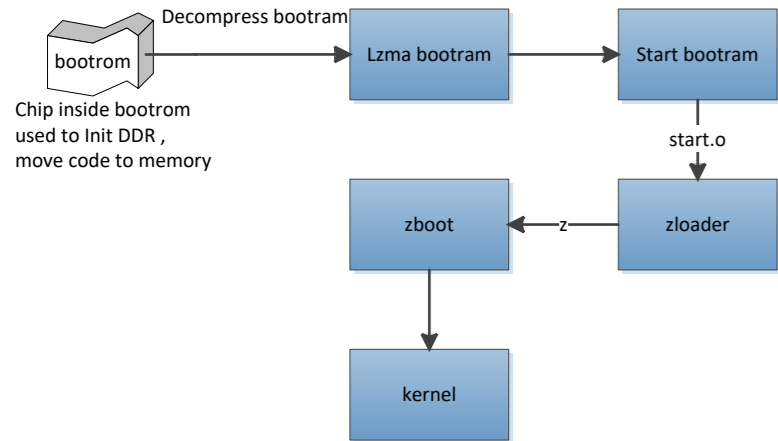
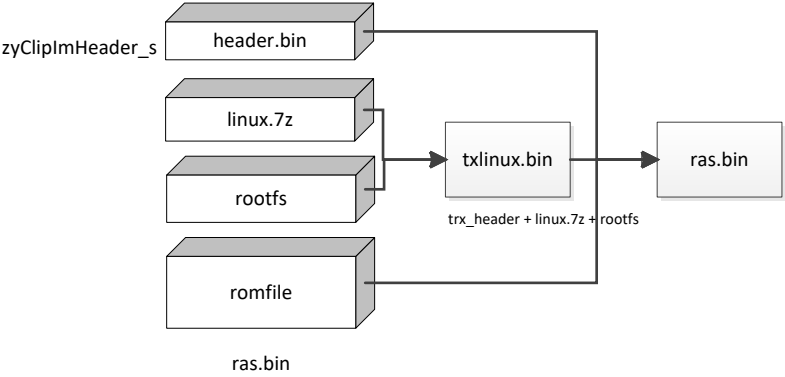
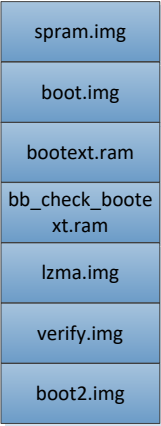


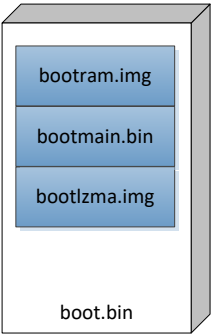
tcboot.bin Contents Layout





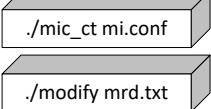


0x00000000



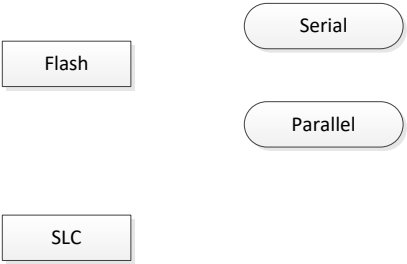
128k (start at 0x3f00), 256k(start at 0x3fe00)
, size=0xb7(182)byte , write magic_num to
0xffff0004

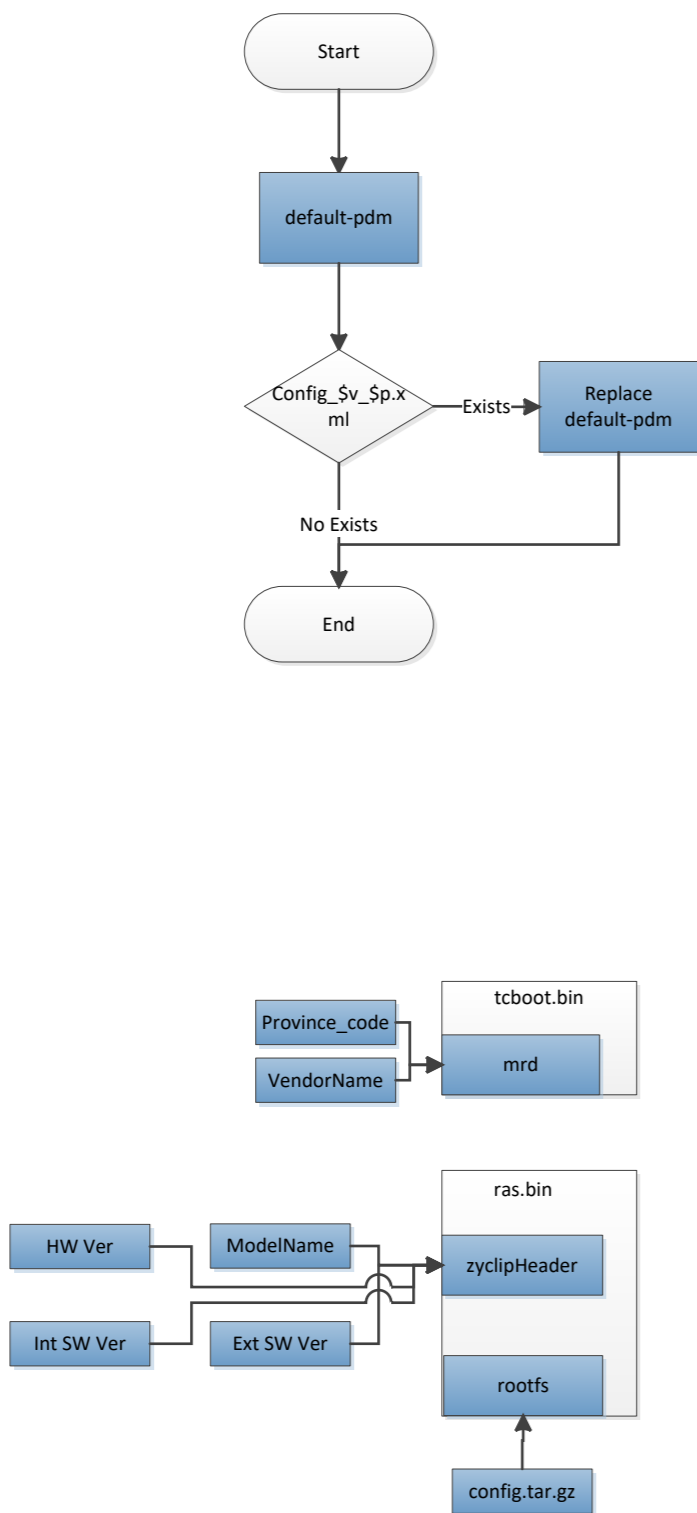
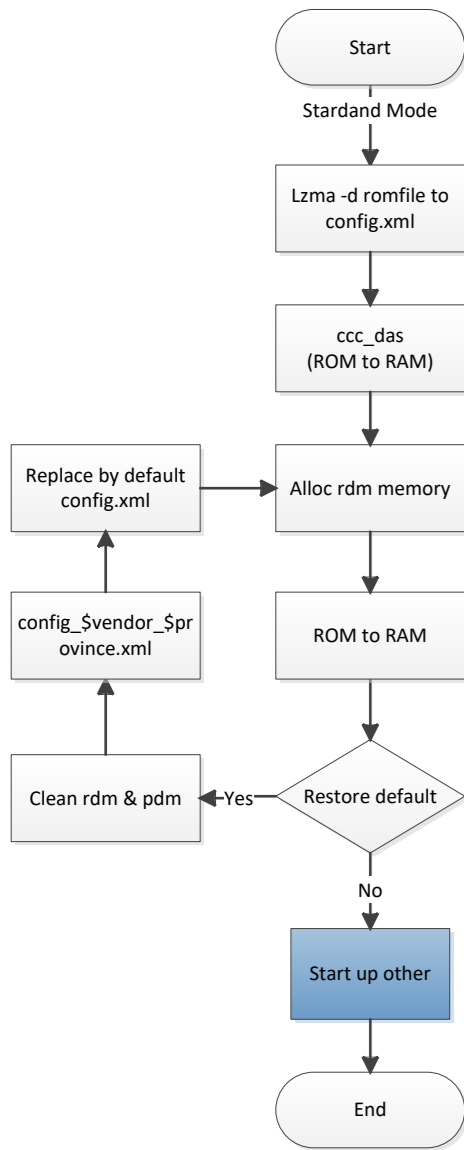
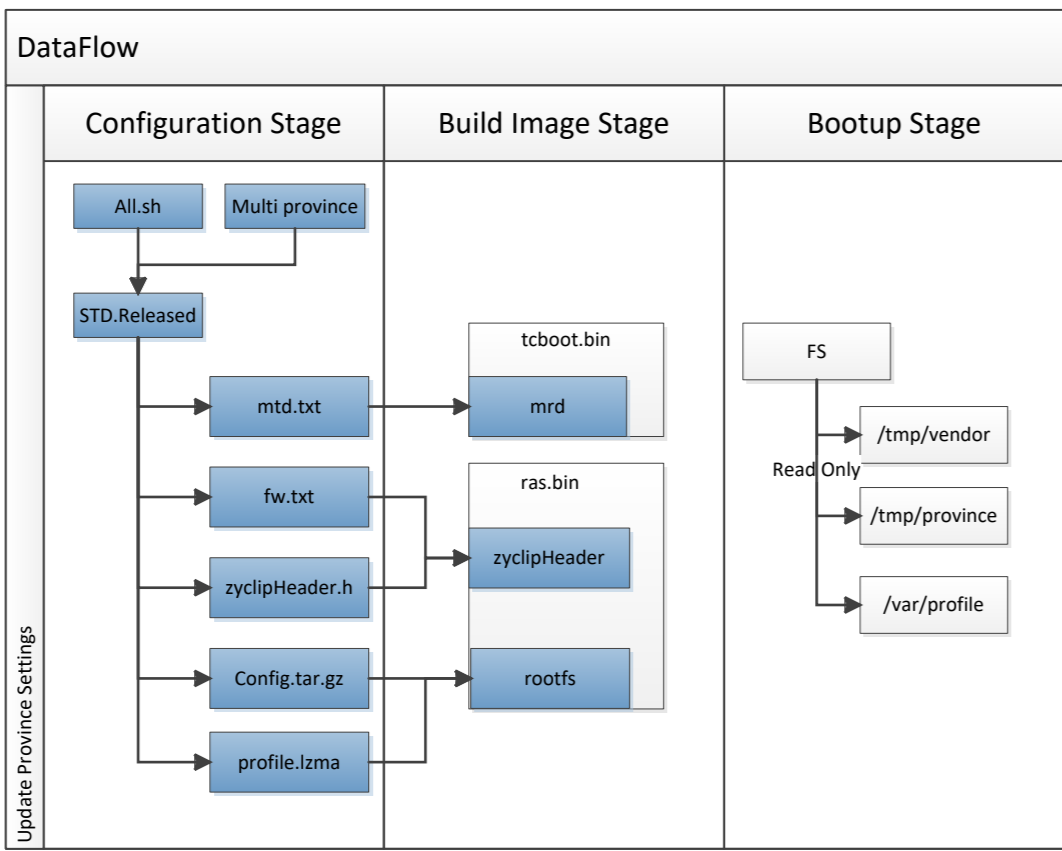
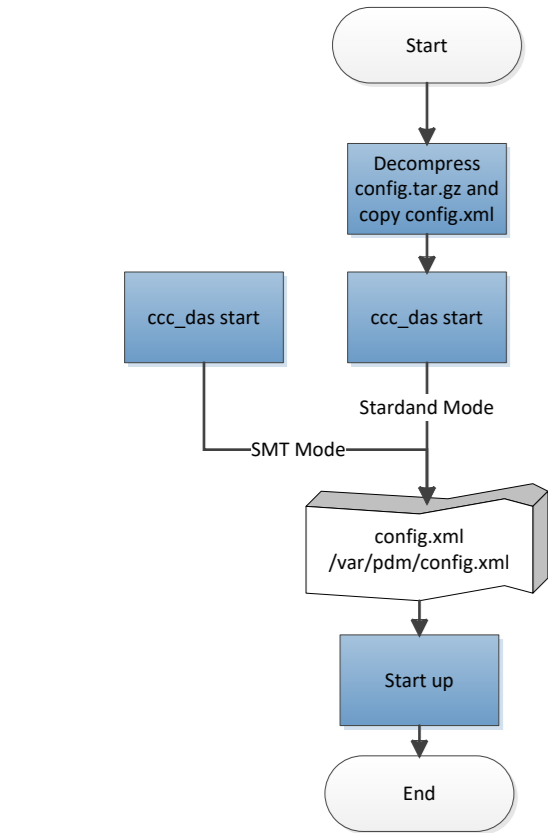
0x00004000



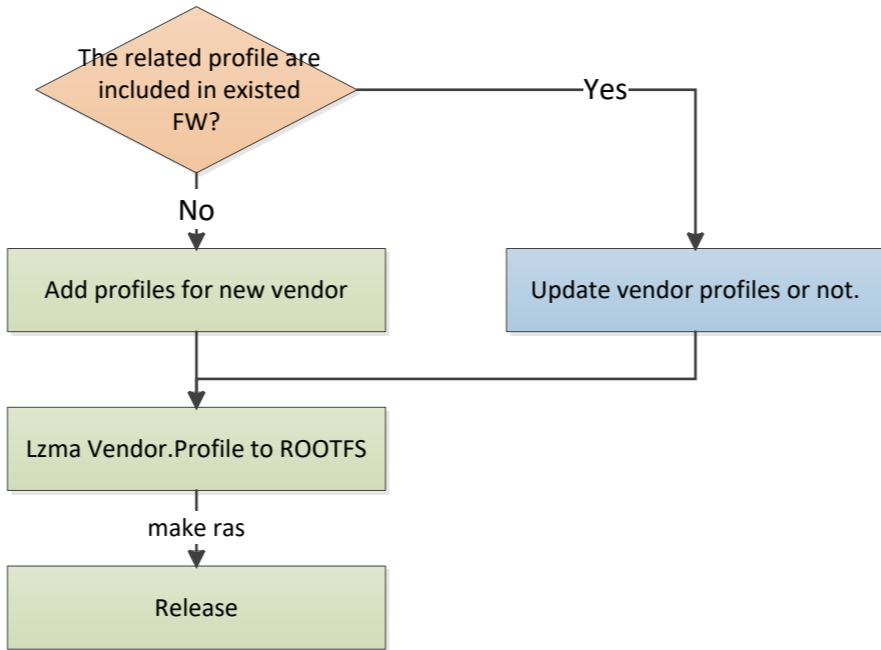
Static write from 0x3ff00 ~ 0x3ffeb,size
for 0xeb(235) byte

tcboot.bin

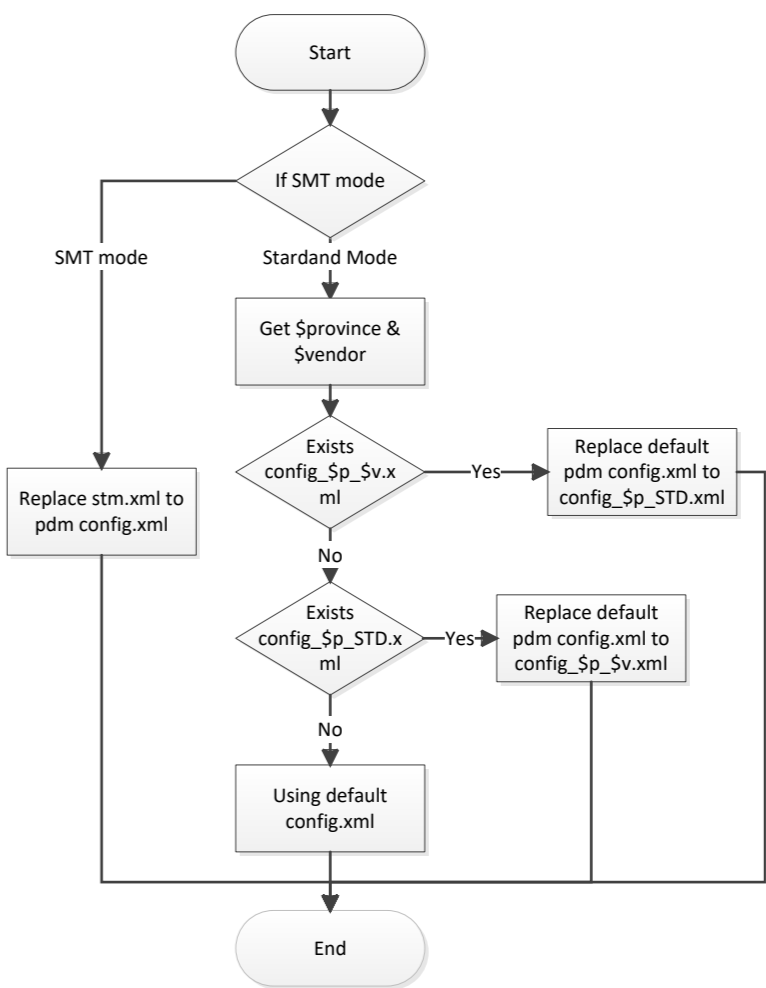
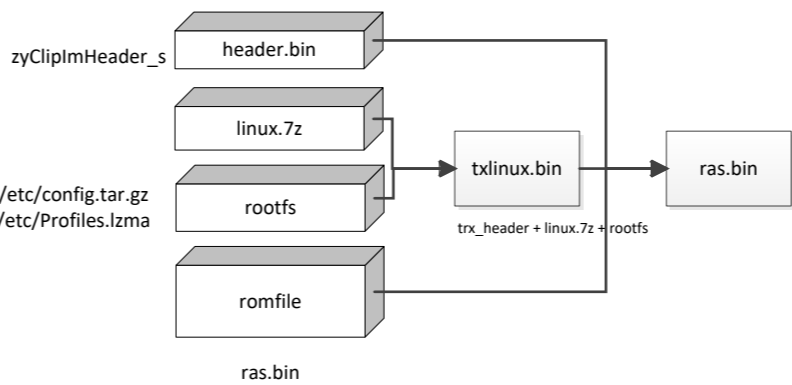
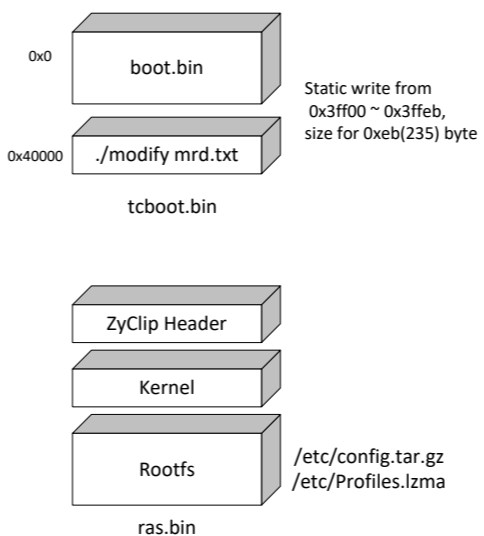
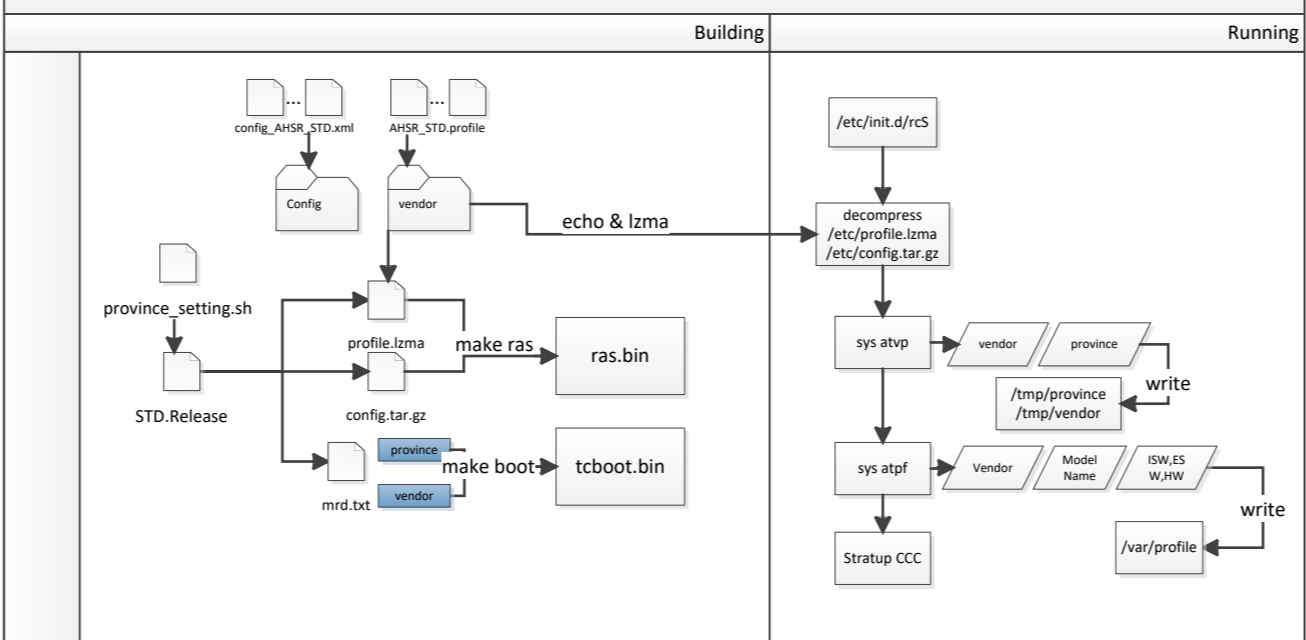


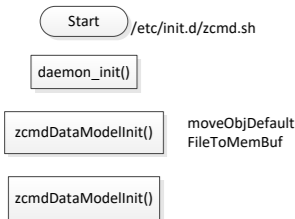
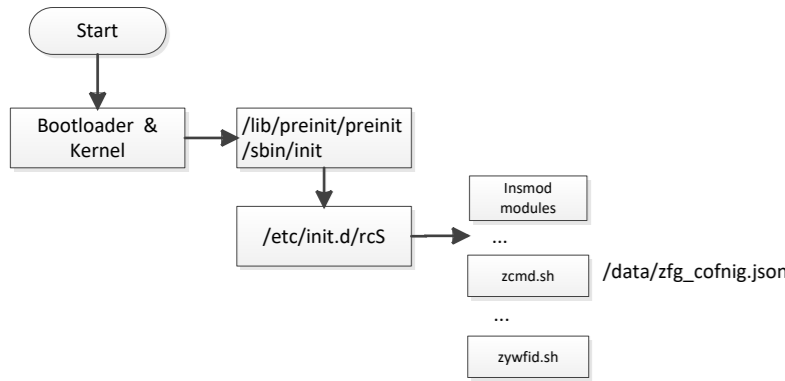
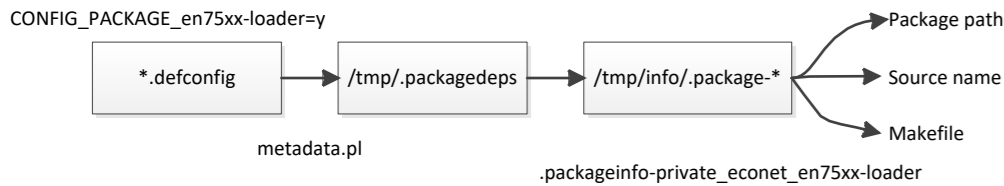


Flow of release FW for A new Vendor

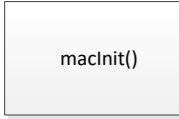
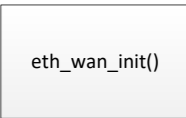


Multiple-Vendors Arch

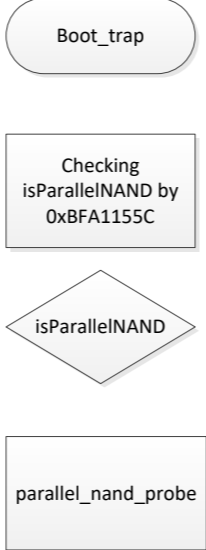




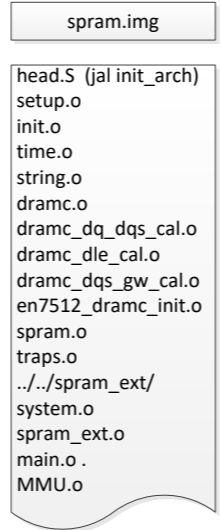
Ether Port Mapping



Paraller NAND Flash SartUp Process



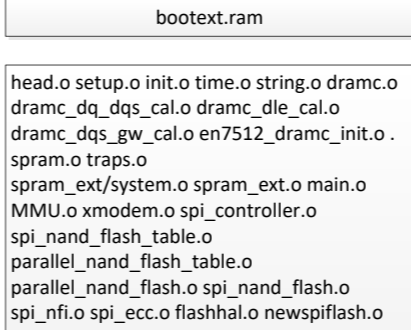
ld_spram_en7516.script



ld.script4



ld_ext_en7516.script



ld_bb_check.script

